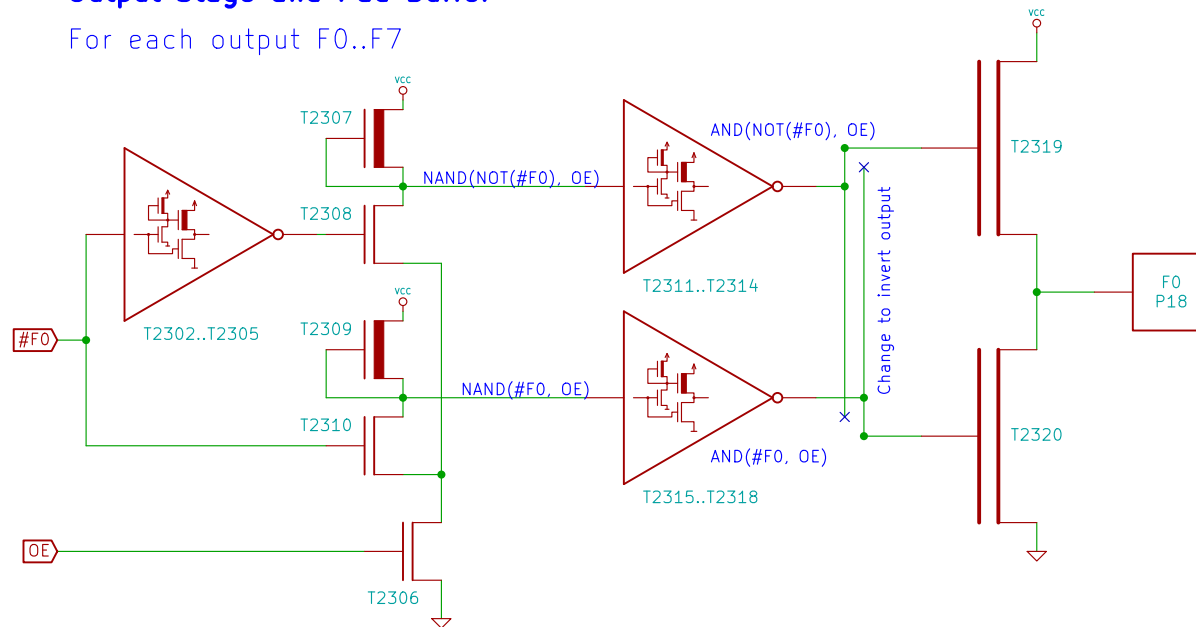


File: backgate.sch		
Sheet: /Backgate Bias Generator/		
Title:		
Size: A4	Date: 20 dec 2012	Rev:
KiCad E.D.A.		Id: 2/4

Output Stage and Pad Buffer

For each output F0..F7

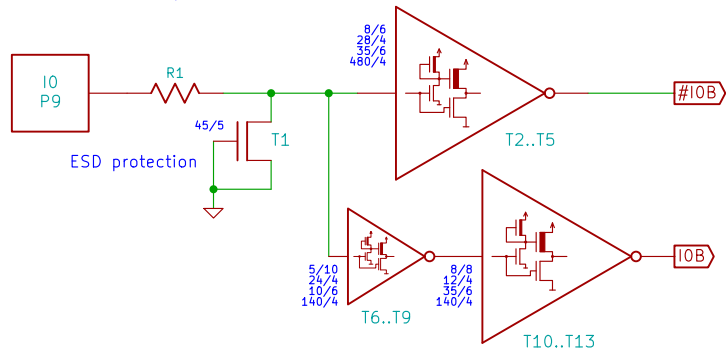


F0 T2302..T2320
F1 T2321..T2339
F2 T2340..T2358
F3 T2359..T2377
F4 T2378..T2396
F5 T2397..T2415
F6 T2416..T2434
F7 T2435..T2453

File: outputs.sch		
Sheet: /Outputs/		
Title:		
Size: A4	Date: 20 dec 2012	Rev:
KiCad E.D.A.		Id: 3/4

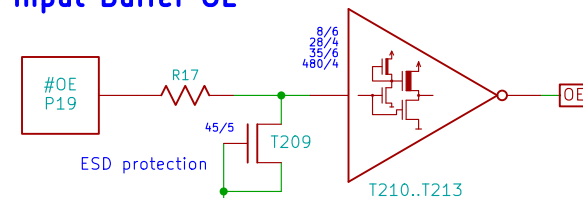
Input Buffer and Inverter

For each input I0..I15



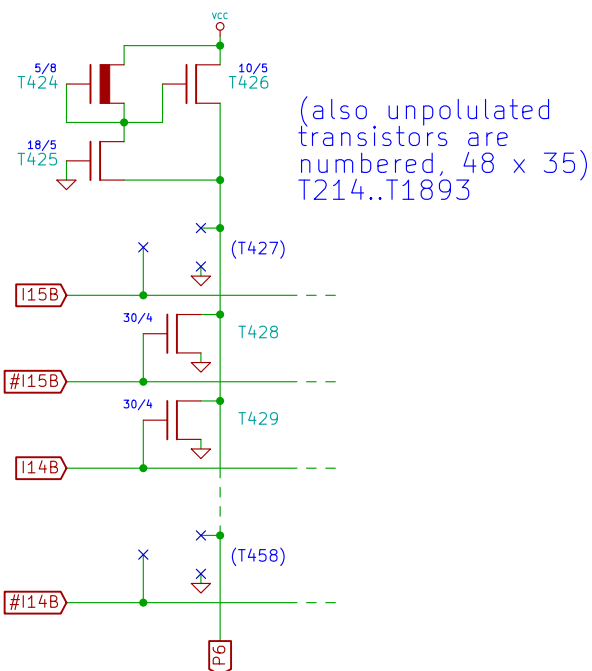
I0 R1, T1..T13
 I1 R2, T14..T26
 I2 R3, T27..T39
 I3 R4, T40..T52
 I4 R5, T53..T65
 I5 R6, T66..T78
 I6 R7, T79..T91
 I7 R8, T92..T104
 I8 R9, T105..T117
 I9 R10, T118..T130
 I10 R11, T131..T143
 I11 R12, T144..T156
 I12 R13, T157..T169
 I13 R14, T170..T182
 I14 R15, T183..T195
 I15 R16, T196..T208

Input Buffer OE



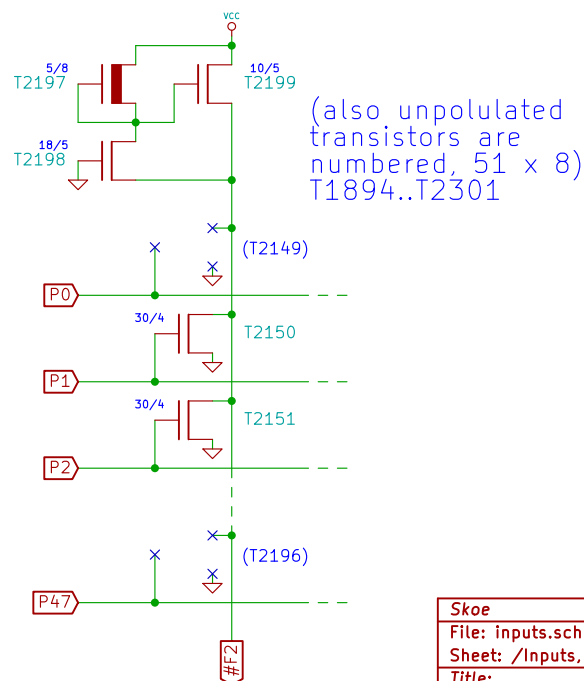
Product Term Array (NOR)

For each signal I0B..#I15B
 and for each product term P0..P47



Sum Term Array (NOR)

For each product term P0..P47
 and for each sum term #F0..#F7



Skoe		
File: inputs.sch		
Sheet: /Inputs, Arrays/		
Title:		
Size: A4	Date: 20 dec 2012	Rev:
KiCad E.D.A.		Id: 4/4