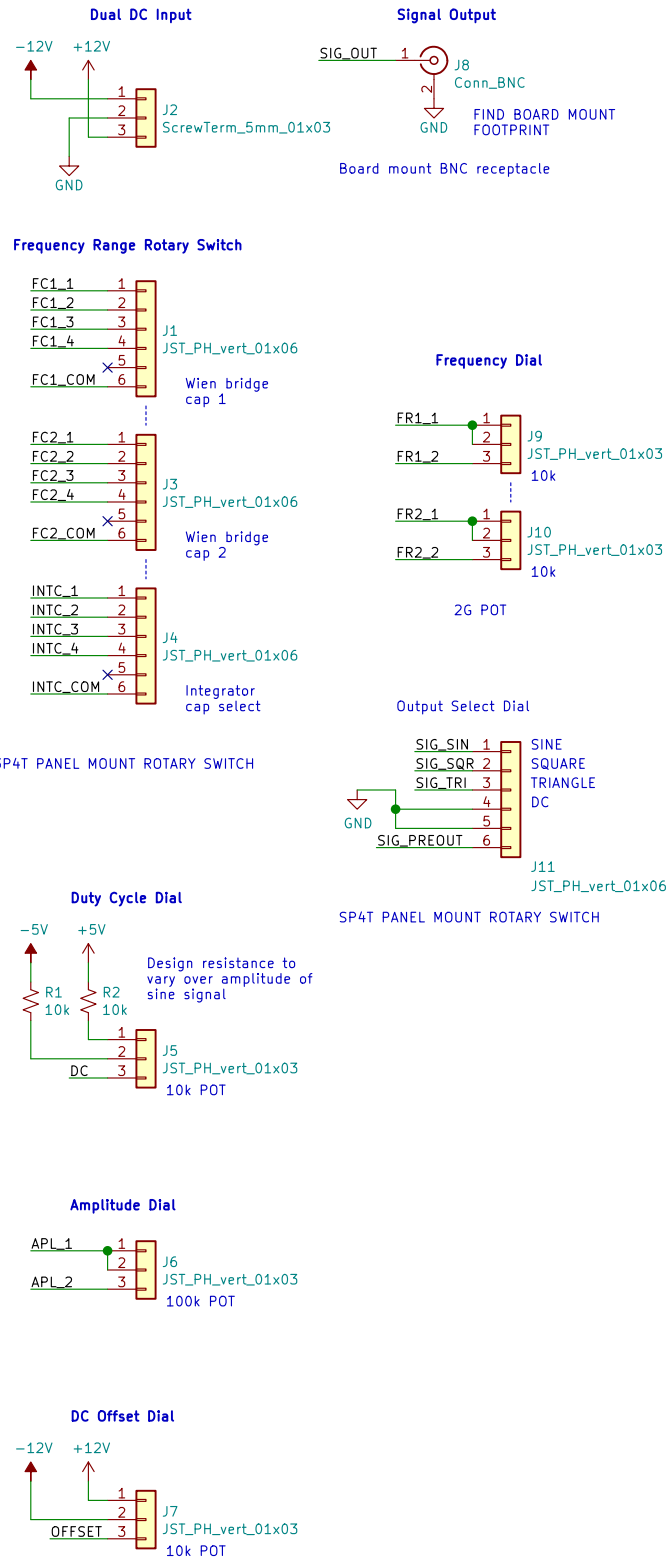
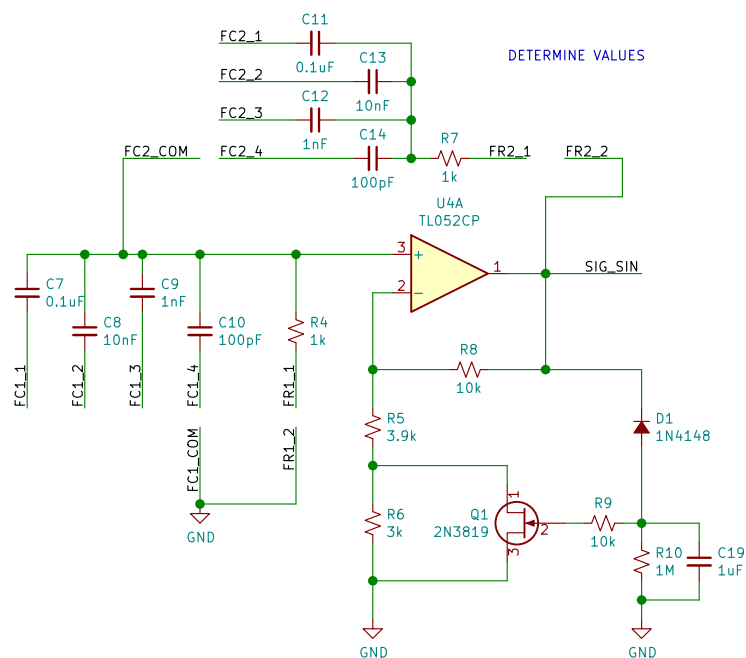


CONNECTORS

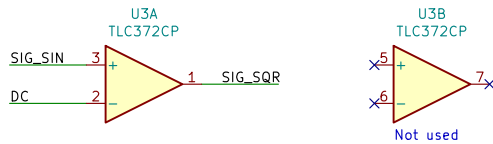


SINE GENERATOR

Wien bridge with selectable frequency decade capacitors, dual gang frequency potentiometer, and JFET AGC

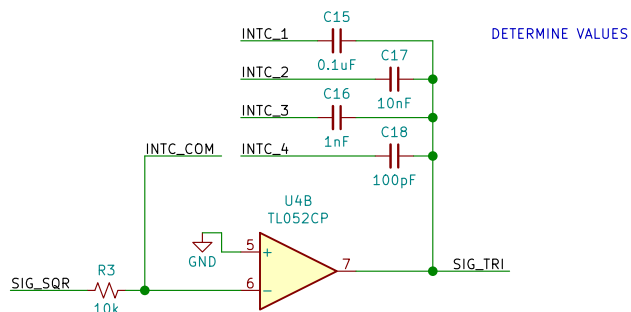


SQUARE GENERATOR

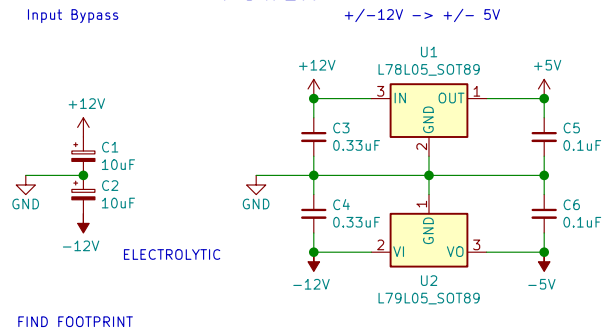


Find a better comparator?

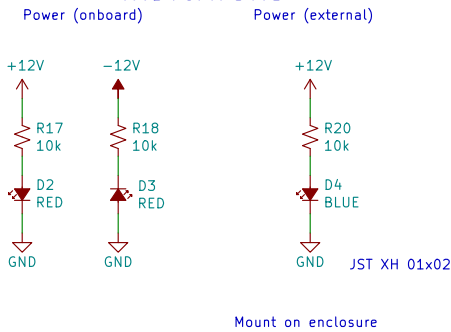
TRIANGLE GENERATOR



POWER

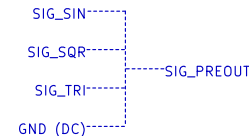


INDICATORS



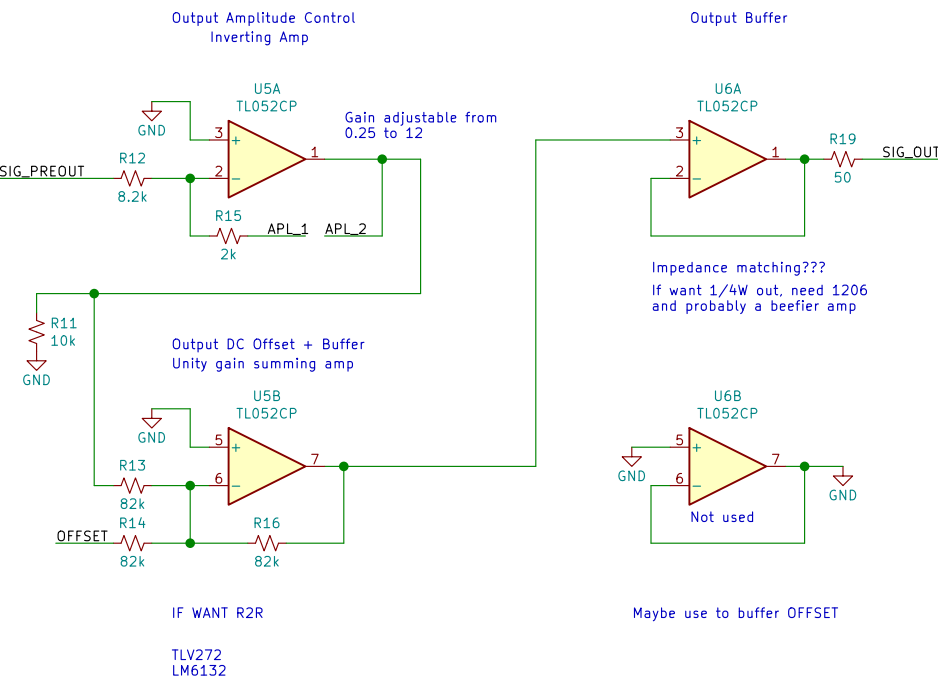
OUTPUT SELECT

SP4T SWITCH



Off board

OUTPUT AMPLITUDE / OFFSET



FOOTPRINTS

ALL SMD PASSIVES 0805

ALL SMD LEDs 1206

ALL SINGLE/DUAL OPAMPS DIP8 Socket

NOTES

If have time, use the AC/DC converter design. Otherwise integrate 12VDC input through a wall wart, panel mount rocker switch, board mount PTC fuse, and switching converter to +/- 12VDC

ALL POT CONN JST PH 3P

1 END

2 END

3 WIPER

Ray Sun

EE 90

Analog Electronics Project Laboratory

California Institute of Technology

Sheet: /

File: triumph-main.sch

Caltech EE

TRIUMPH

Title: Triumph Analog Function Generator - Main Board

Size: B Date: 2019-04-21

Rev: A

KiCad E.D.A. kicad (5.1.0)-1

Id: 1/1