

CONNECTORS

DC Input

VCC

9-18V IN

J1
JST_XH_vert_01x04

GNDREF

Dual DC Output

+12V

GND

-12V

J2
JST_XH_vert_01x04

VCC

F1
PTC_500mA

PS1
RS-1212D

+Vin +Vout
-Vin -Vout

0V

C1 1uF

C2 0.1uF

C3 47uF

C4 10uF

C5 10uF

C6 0.1uF

C7 0.1uF

C8 0.01uF

C9 0.01uF

+12V

-12V

GND

GNDREF

REPLACE WITH CORRECT FOOTPRINT
ONCE POWER BUDGET DETERMINED

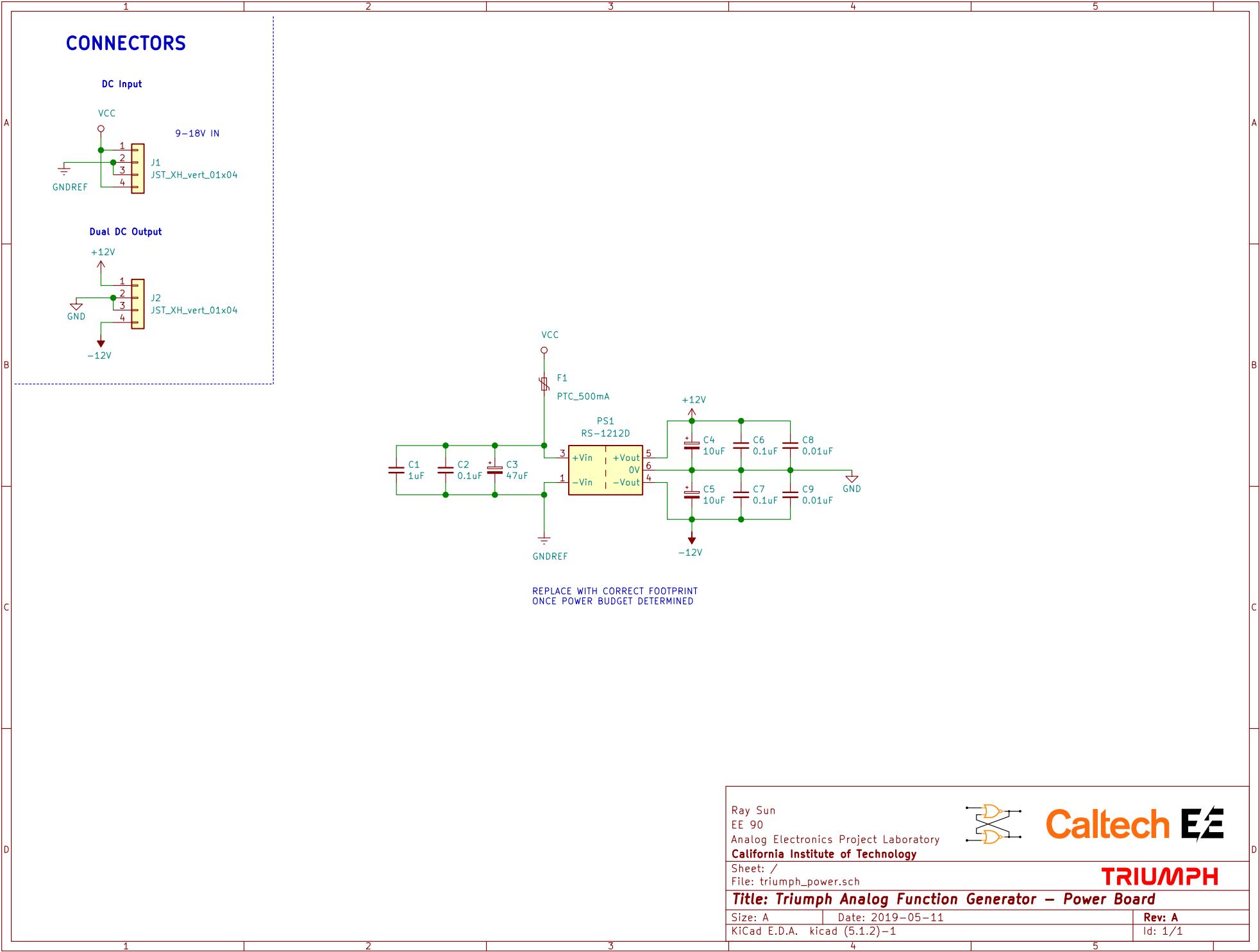
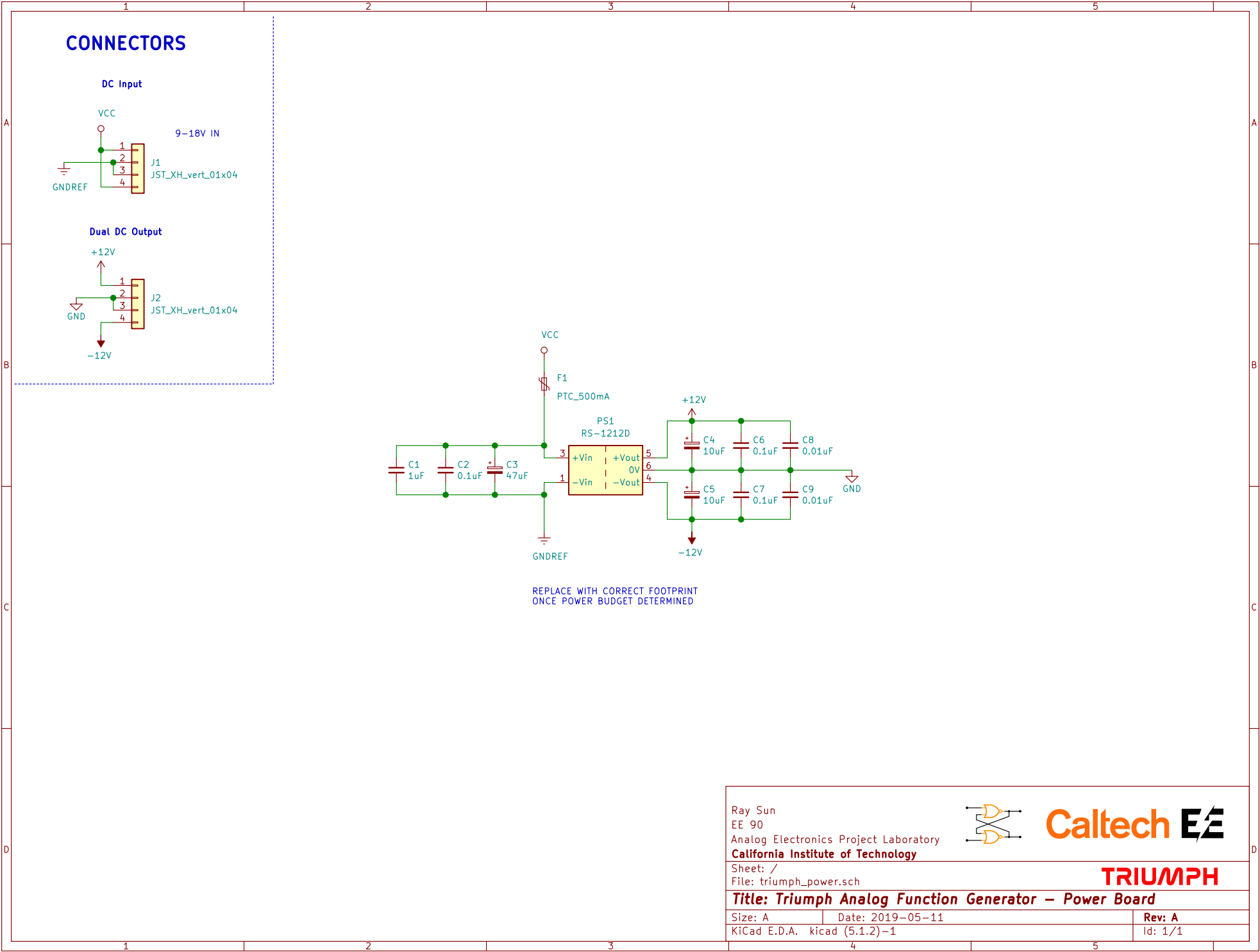
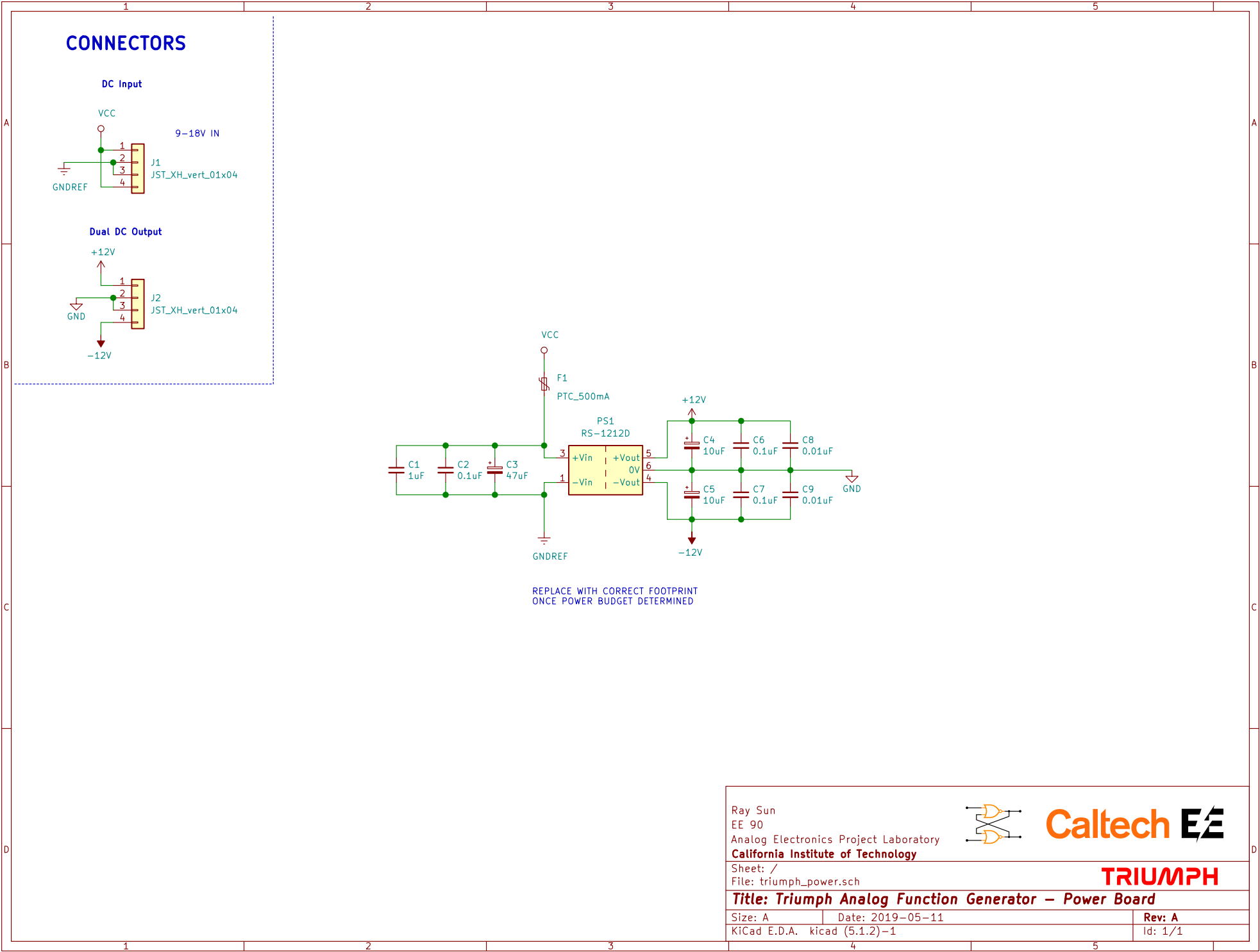
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Ray Sun
EE 90
Analog Electronics Project Laboratory
California Institute of Technology

TRIUMPH

Title: Triumph Analog Function Generator – Power Board

Size: A Date: 2019-05-11 Rev: A
KiCad E.D.A. kicad (5.1.2)-1 Id: 1/1



1 2 3 4 5

A

B

C

D

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