ORDERED Tracking information DELIVERED) Model	Supplier	PN	Description	QTY	Cost	Ext Cost	Notes
	NI Products							
	OctoClock-G CDA-2990	NI	784306-01	CDA-2990 8 CHANNEL CLOCK DISTRIBUTION ACCESSORY WITH GPSDO	1		\$0.00	
Add part numbers to shopping cart here	OctoClock CDA-2990	NI	784305-01	CDA-2990,8 CHANNEL CLOCK DISTRIBUTION ACCESSORY	2		\$0.00	
Shopping Cart - National Instruments (ni.com)	USRP N320	NI	786503-01	USRP N320 (2 TX/RX CHANNELS, 200 MHZ BW) - ETTUS RESEARCH	10		\$0.00	
	USRP N321	NI	786504-01	USRP N321 (2 TX/RX CHANNELS, 200 MHZ BW, W/LO DISTRIBUTION) -	6		\$0.00	
	10Gb SFP Cables	NI	783343-01	10 GIGABIT SFP+ ETHERNET CABLE, 1 M - ETTUS RESEARCH	16			or the x710 etwork Card
	.3M SMA to SMA Cable	NI	763444-01	SMA MALE TO SMA MALE CABLE, 50 OHM, 38.1 CM	10		\$0.00	21WOLK CALO
	0.6M SMA to SMA Cable & Attenuator	NI	782781-01	2 0.6M SMA-M to SMA-M cables and 2 SMA-F to SMA-M Attenuators			\$0.00	
	2M SMA to SMA Cables	NI	783470-01	CABLE ASSEMBLY, SMA TO SMA, 2M			\$0.00	
	RF Torque Screwdriver and SMA Driver Bit	NI	780895-01	RF Torque Screwdriver and SMA Driver Bit	1		\$0.00	
	USRP N3xx Rack Mount Accessory	NI	786467-01	USRP N3xx Rack Mount Accessory	8		\$0.00	
	<u>'</u>	1	700407 01	OSIT NOAK ROCK MOUTH ACCESSORY	- 0		\$0.00	
	Server & Network Cards							
	Server	SuperMicro	4124GS-TNR	Dual AMD EPYC™ 7002 GPU Server	1		\$0.00	
	M.2 storage drive	Amazon	SB-RKT4P-2TB	Sabrent 2TB Rocket 4 Plus NVMe 4.0 Gen4 PCIe M.2 Internal SSD Extre			\$0.00	
	M.2 PCIe controller	Highpoint	SSD7505	HighPoint SSD7505 PCIe 4.0 x16 4-Channel M.2 NVMe RAID Controller	2		\$0.00	
							De	o not mix different
	Network Adapter x710 4 SFP+ Ports	CDW	X710DA4FH	Intel Ethernet Converged Network Adapter X710-DA4 - network adapte	. 1			etwork cards.
	Network Adapter X/10 4 SFFF Forts	CDW	A710DA4111	inter Ethernet Converged Network Adapter X710-DA4 - Hetwork adapte			30.00 116	twork cards.
							De	o not mix different
	Network Adapter e810 2 QSFP+ Ports	CDW	E810CQDA2	Intel Ethernet Network Adapter E810-CQDA2 - network adapter - PCIe	, 2			etwork cards.
								se with E810-CQ
	QSFP28 to SFP28	CDW	MCP7F00-A002R30N	Mellanox LinkX 100GBase direct attach cable - 6.6 ft	4		\$0.00 ca	
		1						
	Rack Assembly							
	42U Open Frame Rack Enclosure Server Cabinet 3000lb Capacity	CDW	SR42UBEXPND		1		\$0.00	
	1U Adjustable Mount Depth Heavy Duty Vented Rack Mount Shelf	CDW- StarTech	ADJSHELFHDV		2		\$0.00	
	APC rack shelf - 1U	CDW- APC	AR8125		2		\$0.00	
	Rackmount extension bracket	CDW	B018-000-1P5		8		\$0.00	
		Rackmount						
	HW1032-50 Cage	Solutions	HW1032-50 Cage	Screws, Cage Nuts, Washers	5			
		IAENCLOSURES.C						
	Relay Rack or 4 Post Rack Cabinet.	OM	IAB103V10-1U	IAB103V10-1U 1U 3 inch Rack Extender for Industrial Standard 19 inch			\$0.00	
	Vented Server Rack Mount Shelf	StarTech	ADJSHELF	StarTech.com 1U Adjustable Vented Server Rack Mount Shelf - 175lbs -			\$0.00	
	Tripp Lite Rack Enclosure Server Blanking Panel Kit	CDW- Tripp-Lite	3849592	Tripp Lite Rack Enclosure Server Cabinet 4 Piece Blanking Panel Kit 19 in			\$0.00	
	Side Panel for SR42UB Rack Enclosure with Key Lock Latch	Amazon	SR42SIDE	Tripp Lite SR42SIDE Side Panel for SR42UB Rack Enclosure with Key Loc			\$0.00	
	Black Slotted-Duct Raceway	Amazon	RMT312A	Black Box 2.25" H x 1.5" W, 72" L Black Slotted-Duct Raceway	2		\$0.00	
	Rack Devices							
	Network Switch	∟l Amazon	Cisco SG110-24		1		\$0.00	
	Basic Power Distribution Unit	CDW	PDU1220	Tripp Lite PDU Single Phase Basic Horizontal 120V 2.4kw 13 5-15/20R 2			\$0.00	
	Switched Power Distribution Unit	CDW	PDUMH20NET	Tripp Lite PDU Switched 120V 20A 5-15/20R 16 Outlet 1URM	1		\$0.00	
	8 way power splitter/combiner	Mini Circuits	ZN8PD1-63W-S+		8		\$0.00	
	4 way power splitter/combiner	Mini Circuits	ZN4PD1-63HP-S+		2		\$0.00	
	Rack Console	c cuits		KVM rack or equivalent			Ç0.00	
				······				
	Stanley 31610 SurgeMax Pro 9 Outlet Metal Surge Protector, Black	Stanley / Amazon	31610	Stanley 31610 SurgeMax Pro 9 Outlet Metal Surge Protector, Black	2		\$0.00	
	Cables & Management							
	Cat 6 RJ45 Cables	l Amazon	160021-5x5		5		\$0.00	
	10' power cable	CDW	P006-010	Tripp Lite Computer Power Extension Cord Adapter 10A 18AWG 5-15P			\$0.00	
	6' power cable	CDW	P006-010 P006-006	Tripp Lite Computer Power Extension Cord Adapter 10A 18AWG 5-15P			\$0.00	
	Cable ties	Amazon	91140	VELCRO Brand ONE-WRAP Cable Ties 100Pk 8 x 1/2" Black Cord Org			\$0.00	
	TR Industrial Multi-Purpose UV Resistant Black Cable Ties, 8 inches, 100 Pack	TR Industrial	TR88302	TR Industrial Multi-Purpose UV Resistant Black Cable Ties, 8 inches, 100			\$0.00	
	TR Industrial Multi-Purpose UV Resistant Black Cable Ties, 8 Inches, 100 Pack TR Industrial Multi-Purpose UV Resistant Black Cable Ties, 12 inches, 100 Pack	TR Industrial	TR88303	TR Industrial Multi-Purpose UV Resistant Black Cable Ties, 8 Inches, 10			\$0.00	
	3M Double Sided Tape	3M	7.9387E+11	3M Double Sided Tape, 3M Heavy Duty Mounting Waterproof VHB Foa			\$0.00	
I	эмі родріє зідец таре	JIVI	1.330/ET11	Sivi Double Sided Tape, Sivi neavy Duty Mounting Waterproof VHB F0a			ψU.UU	

32	Basio	32	mount to back	
31	Networ	k Switch	31	mount to back
30			30	i
29			29	i
28	Ser	rver	28	
27			27	
26	K۱	/M	26	i
25		USRP N 320 1	25	i
24	USRP N 320 0		24	i
23		USRP N 320 3	23	i
22	USRP N 320 2		22	i
21	OctoClock CD	A-2990 Unit 1	21	mount to back
	Shelf for Ca	ables Unit 0		Ī
20	8-way Splitter Unit 0	8-way Splitter Unit 1	20	mount to front
	8-way Splitter Unit 2	8-way Splitter Unit 3		
19		USRP N 320 5	19	Ī
18	USRP N 320 4		18	
17		USRP N 320 7	17	
16	USRP N 320 6		16	1
15	OctoClock CD	A-2990 Unit 0	15	mount to back
14	Shelf for Cables Unit 1		14	mount to front
14	4-way Splitter Unit 0	4-way Splitter Unit 1	14	inount to none
13		USRP N 320 9	13	
12	USRP N 320 8		12	
11		USRP N 320 11	11	1
10	USRP N 320 10		10	l
9	OctoClock CD	9	mount to back	
	Shelf for Ca	ables Unit 2		
8	8-way Splitter Unit 4	8-way Splitter Unit 5	8	mount to front
	8-way Splitter Unit 6	8-way Splitter Unit 7		
7		USRP N 320 13	7	l
6	USRP N 320 12		6	l
5		USRP N 320 15	5	
4	USRP N 320 14		4	
3			3	
2			2	

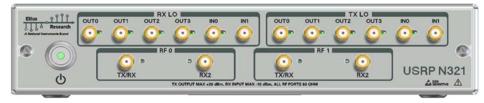
N320 front



N320 back



N321 front



N321 back



OctoClock

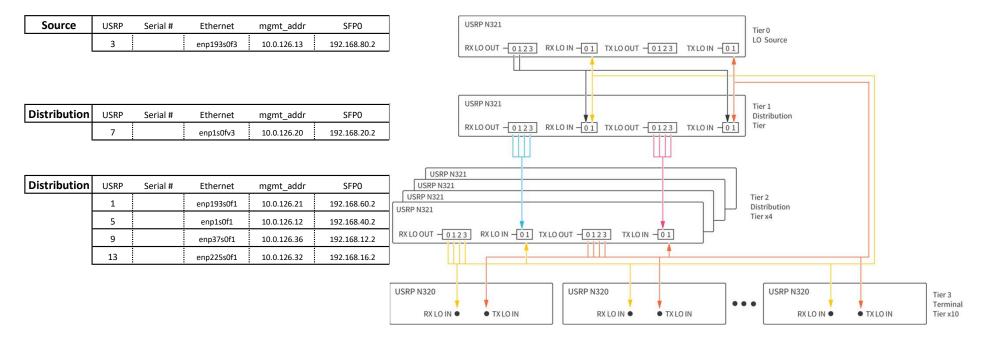


8-way-splitter



4-way-splitter





Terminal	USRP	Serial #	Ethernet	mgmt_addr	SFP0
	0		Enp193s0f0	10.0.126.14	192.168.50.2
	2		Enp193s0f2	10.0.126.22	192.168.70.2
	4		enp1s0f0	10.0.126.17	192.168.30.2
	6		enp1s0f2	10.0.126.23	192.168.10.2
	8		enp37s0f0	10.0.126.35	192.168.11.2

Terminal	USRP	Serial #	Ethernet	mgmt_addr	SFP0
	10		enp37s0f2	10.0.126.37	192.168.13.2
	11		enp37s0f3	10.0.126.34	192.168.14.2
	12		enp225s0f0	10.0.126.30	192.168.15.2
	14		enp225s0f2	10.0.126.28	192.168.17.2
	15		enp225s0f3	10.0.126.31	192.168.18.2

Status	Item	Serial Number	Rack Position
	OctoClock-G CDA-2990 unit0		15
	OctoClock CDA-2990 unit1		21
	OctoClock CDA-2990 unit2		9
	USRP_N320_0		24
	USRP_N321_1		25
	USRP_N320_2		22
	USRP_N321_3		23
	USRP_N320_4		18
	USRP_N321_5		19
	USRP_N320_6		16
	USRP_N321_7		17
	USRP_N320_8		12
	USRP_N321_9		13
	USRP_N320_10		10
	USRP_N320_11		11
	USRP_N320_12		6
	USRP_N321_13		7
	USRP_N320_14		4
	USRP_N320_15		5
	Server		27,28,29,30
	Network Switch		31
	Basic Power Distribution Unit		32
	Switched Power Distribution Unit		3
	KVM Rack Console		26
	APC rack shelf unit0	N/A	20
	APC rack shelf unit1	N/A	14
	APC rack shelf unit2	N/A	8
	8-way power splitter unit 0	N/A	20
	8-way power splitter unit 1	N/A	20
	8-way power splitter unit 2	N/A	20
	8-way power splitter unit 3	N/A	20
	8-way power splitter unit 4	N/A	8
	8-way power splitter unit 5	N/A	8
	8-way power splitter unit 6	N/A	8
	8-way power splitter unit 7	N/A	8
	4-way power splitter unit 0	N/A	14
	4-way power splitter unit 1	N/A	14

				Connections					
F	rom			То					
Device P	ort	1	Device	Port					
octo0 1	LOM_in	↑	NC	NC					
octo0 F	PPS_in	1	NC	NC					
octo0 C	GPS_ant	1	NC	NC					
octo0 F	REF_switch	1	SET T	O GPSDO					
octo0 1	L0M_1	1	octo1	10M_in					
octo0 1	LOM_2	1	octo2	10M_in					
octo0 1	L0M_3	→	NC	NC					
octo0 1	L0M_4	\rightarrow	NC	NC					
octo0 1	LOM_5	1	NC	NC					
octo0 1	LOM_6	1	NC	NC					
octo0 1	LOM_7	1	NC	NC					
octo0 1	L0M_8	\rightarrow	NC	NC					
octo0 F	PPS_1	→	octo1	PPS_in					
octo0 F	PPS_2	\rightarrow	octo2	PPS_in					
octo0 F	PS_3	\rightarrow	NC	NC					
octo0 F	PPS_4	→	NC	NC					
octo0 F	PS_5	→	NC	NC					
octo0 F	PPS_6	↑	NC	NC					
octo0 F	PPS_7	\rightarrow	NC	NC					
octo0 F	PS_8	\rightarrow	NC	NC					
octo0 r	ntwrk	→	NC	NC					
octo1 1	LOM_in	1	octo0	10M_1					
octo1 P	PPS_in	→	octo0	PPS_1					
octo1 C	GPS_ant	→	NC	NC					
octo1 F	REF_switch	\rightarrow	SET T	O Ref In					
octo1 ¹	L0M_1	→	usrp0	ref in					
octo1 1	L0M_2	→	usrp1	ref in					
octo1 1	LOM_3	→	usrp2	ref in					
octo1 1	L0M_4	\rightarrow	usrp3	ref in					
octo1 1	LOM_5	\rightarrow	usrp4	ref in					
octo1 1	L0M_6	→	usrp5	ref in					
octo1 1	LOM_7	→	usrp6	ref in					
octo1 1	LOM_8	↑	usrp7	ref in					
octo1 F	PPS_1	↑	usrp0	pps in					
octo1 F	PPS_2	→	usrp1	pps in					
octo1 F	PPS_3	\rightarrow	usrp2	pps in					

Connections					
	From			То	
Device	Port	\rightarrow	Device	Port	
octo1	PPS_4	\rightarrow	usrp3	pps in	
octo1	PPS_5	\rightarrow	usrp4	pps in	
octo1	PPS_6	\rightarrow	usrp5	pps in	
octo1	PPS_7	\rightarrow	usrp6	pps in	
octo1	PPS_8	\rightarrow	usrp7	pps in	
octo1	ntwrk	\rightarrow	NC	NC	
octo2	10M_in	\rightarrow	octo0	10M_2	
octo2	PPS_in	\rightarrow	octo0	PPS_2	
octo2	GPS_ant	\rightarrow	NC	NC	
octo2	REF_switch	\rightarrow	SET ⁻	ΓΟ Ref In	
octo2	10M_1	\rightarrow	usrp8	ref in	
octo2	10M_2	\rightarrow	usrp9	ref in	
octo2	10M_3	\rightarrow	usrp10	ref in	
octo2	10M_4	→	usrp11	ref in	
octo2	10M_5	→	usrp12	ref in	
octo2	10M_6	\rightarrow	usrp13	ref in	
octo2	10M_7	\rightarrow	usrp14	ref in	
octo2	10M_8	→	usrp15	ref in	
octo2	PPS_1	→	usrp8	pps in	
octo2	PPS_2	\rightarrow	usrp9	pps in	
octo2	PPS_3	\rightarrow	usrp10	pps in	
octo2	PPS_4	→	usrp11	pps in	
octo2	PPS_5	\rightarrow	usrp12	pps in	
octo2	PPS_6	\rightarrow	usrp13	pps in	
octo2	PPS_7	\rightarrow	usrp14	pps in	
octo2	PPS_8	\rightarrow	usrp15	pps in	
octo2	ntwrk	\rightarrow	NC	NC	
_		_	_		

usrp0	rf0:tx/rx	1	8way0	Port0
usrp0	rf1:tx/rx	1	8way0	Port1
usrp0	rf0:rx	1	8way4	Port0
usrp0	rf1:rx	1	8way4	Port1
usrp0	loin:tx	1	usrp1	txlo:out0
usrp0	loin:rx	1	usrp1	rxlo:out0
usrp0	ntwrk	1	N_Swch	port0
usrp0	ref in	1	octo1	10M_1
usrp0	pps in	1	octo1	PPS_1
usrp0	SFP+0	→	Server	port0
usrp0	SFP+1	→	NC	NC

From To Device Port → Device Port usrp1 rxlo:out0 → usrp0 rxlo:in1 usrp1 rxlo:out1 → usrp1 rxlo:in1 usrp1 rxlo:out2 → usrp2 rxlo:in1 usrp1 rxlo:in0 → usrp3 rxlo:in1 usrp1 rxlo:in1 → usrp1 rxlo:out1 usrp1 txlo:out1 → usrp0 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp2 txlo:in1 usrp1 txlo:in0 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf1:rx → 8way4 port7 usrp1 ref in → octo1 10M_2 usrp1 ref in → octo1	Connections						
usrp1 rxlo:out0 → usrp0 rxlo:in1 usrp1 rxlo:out1 → usrp1 rxlo:in1 usrp1 rxlo:out2 → usrp2 rxlo:in1 usrp1 rxlo:out3 → usrp3 rxlo:in1 usrp1 rxlo:in0 → usrp7 rxlo:out0 usrp1 rxlo:in1 → usrp1 rxlo:in1 usrp1 txlo:out1 → usrp1 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp7 txlo:out1 usrp1 txlo:in1 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp7 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf1:tx/rx → sway0 port2 <tr< th=""><th></th><th colspan="6">From To</th></tr<>		From To					
usrp1 rxlo:out1 → usrp1 rxlo:in1 usrp1 rxlo:out2 → usrp2 rxlo:in1 usrp1 rxlo:out3 → usrp3 rxlo:in1 usrp1 rxlo:in0 → usrp7 rxlo:out0 usrp1 rxlo:in1 → usrp1 rxlo:out1 usrp1 txlo:out0 → usrp0 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:in0 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp2 txlo:out1 usrp1 txlo:in1 → usrp2 txlo:out1 usrp1 txlo:in1 → usrp2 txlo:out1 usrp1 txlo:out2 → sway0 port3 usrp1 trf1:rx → sway4 port2	Device	Port	\rightarrow	Device	Port		
usrp1 rxlo:out2 → usrp2 rxlo:in1 usrp1 rxlo:out3 → usrp3 rxlo:in1 usrp1 rxlo:in0 → usrp7 rxlo:out0 usrp1 rxlo:in1 → usrp1 rxlo:out1 usrp1 txlo:out1 → usrp0 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:rx → 8way4 port1 usrp2 rf0:tx/rx → 8way4 port2	usrp1	rxlo:out0	\rightarrow	usrp0	rxlo:in1		
usrp1 rxlo:out3 → usrp3 rxlo:in1 usrp1 rxlo:in0 → usrp7 rxlo:out0 usrp1 rxlo:in1 → usrp1 rxlo:out1 usrp1 txlo:out0 → usrp1 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port3 usrp1 ref in → octo1 10M_2 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf0:tx/rx → 8way0 port2 <	usrp1	rxlo:out1	\rightarrow	usrp1	rxlo:in1		
usrp1 rxlo:in0 → usrp7 rxlo:out0 usrp1 rxlo:in1 → usrp1 rxlo:out1 usrp1 txlo:out0 → usrp0 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf1:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 rf1:rx → 8way4 port8 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 10M_2 usrp1 pps in → octo1 NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf1:rx <td>usrp1</td> <td>rxlo:out2</td> <td>\rightarrow</td> <td>usrp2</td> <td>rxlo:in1</td>	usrp1	rxlo:out2	\rightarrow	usrp2	rxlo:in1		
usrp1 rxlo:in1 → usrp1 rxlo:out1 usrp1 txlo:out0 → usrp0 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf0:tx/rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf0:rx → 8way4 port8 usrp1 rf1:rx → 8way4 port8 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 sFP+0 → Server port1 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf0:tx/rx → 8way0 port3 usrp2 rf0:rx → 8way0 port3 usrp2 rf1:rx	usrp1	rxlo:out3	\rightarrow	usrp3	rxlo:in1		
usrp1 txlo:out0 → usrp0 txlo:in1 usrp1 txlo:out1 → usrp1 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 rf1:rx → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port2 usrp2 rf1:rx → 8way0 port2 usrp2 rf0:rx → 8way0 port2 usrp2 rf1:rx → 8way0 port3 usrp2 rf1:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 rf1:rx → warp1 txlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+0 → Server port1	usrp1	rxlo:in0	\rightarrow	usrp7	rxlo:out0		
usrp1 txlo:out1 → usrp1 txlo:in1 usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 sFP+0 → Server port1 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf1:rx → 8way4 port3 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 rxlo:out2 usrp2 ntwrk → N_Swch	usrp1	rxlo:in1	\rightarrow	usrp1	rxlo:out1		
usrp1 txlo:out2 → usrp2 txlo:in1 usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf0:rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf0:rx → 8way4 port3 usrp2 rf0:rx → 8way4 port2 usrp2 rf1:rx → wsrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1	usrp1	txlo:out0	\rightarrow	usrp0	txlo:in1		
usrp1 txlo:out3 → usrp3 txlo:in1 usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way4 port7 usrp1 rf1:rx → 8way4 port7 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf0:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 </td <td>usrp1</td> <td>txlo:out1</td> <td>\rightarrow</td> <td>usrp1</td> <td>txlo:in1</td>	usrp1	txlo:out1	\rightarrow	usrp1	txlo:in1		
usrp1 txlo:in0 → usrp7 txlo:out0 usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port2 usrp2 rf0:rx → 8way0 port3 usrp2 rf0:rx → 8way4 port3 usrp2 rf0:rx → way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 sFP+0 → Server port1	usrp1	txlo:out2	\rightarrow	usrp2	txlo:in1		
usrp1 txlo:in1 → usrp1 txlo:out1 usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf0:rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf0:rx → 8way4 port3 usrp2 rf0:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+0 → Server port1	usrp1	txlo:out3	\rightarrow	usrp3	txlo:in1		
usrp1 rf0:tx/rx → 8way0 port7 usrp1 rf1:tx/rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf0:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp1	txlo:in0	\rightarrow	usrp7	txlo:out0		
usrp1 rf1:tx/rx → 8way0 port8 usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 sFP+0 → Server port1	usrp1	txlo:in1	\rightarrow	usrp1	txlo:out1		
usrp1 rf0:rx → 8way4 port7 usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf1:rx → 8way4 port3 usrp2 rf1:rx → 8way4 port3 usrp2 loin:rx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp1	rf0:tx/rx	\rightarrow	8way0	port7		
usrp1 rf1:rx → 8way4 port8 usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf1:rx → 8way4 port3 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp1	rf1:tx/rx	\rightarrow	8way0	port8		
usrp1 ntwrk → N_Swch port1 usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf1:rx → 8way4 port3 usrp2 rf1:rx → away4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 ref in → octo1 10M_3 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp1	rf0:rx	\rightarrow	8way4	port7		
usrp1 ref in → octo1 10M_2 usrp1 pps in → octo1 PPS_2 usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf1:rx → 8way4 port2 usrp2 loin:tx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp1	rf1:rx	\rightarrow	8way4	port8		
usrp1pps in \rightarrow octo1PPS_2usrp1SFP+0 \rightarrow Serverport1usrp1SFP+1 \rightarrow NCNCusrp2rf0:tx/rx \rightarrow 8way0port2usrp2rf1:tx/rx \rightarrow 8way4port3usrp2rf1:rx \rightarrow 8way4port3usrp2rf1:rx \rightarrow 8way4port3usrp2loin:tx \rightarrow usrp1txlo:out2usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp1	ntwrk	\rightarrow	N_Swch	port1		
usrp1 SFP+0 → Server port1 usrp1 SFP+1 → NC NC usrp2 rf0:tx/rx → 8way0 port2 usrp2 rf1:tx/rx → 8way0 port3 usrp2 rf0:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp1	ref in	\rightarrow	octo1	10M_2		
usrp1SFP+1 \rightarrow NCNCusrp2rf0:tx/rx \rightarrow 8way0port2usrp2rf1:tx/rx \rightarrow 8way0port3usrp2rf0:rx \rightarrow 8way4port2usrp2rf1:rx \rightarrow 8way4port3usrp2loin:tx \rightarrow usrp1txlo:out2usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp1	pps in	\rightarrow	octo1	PPS_2		
usrp2rf0:tx/rx \rightarrow 8way0port2usrp2rf1:tx/rx \rightarrow 8way0port3usrp2rf0:rx \rightarrow 8way4port2usrp2rf1:rx \rightarrow 8way4port3usrp2loin:tx \rightarrow usrp1txlo:out2usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp1	SFP+0	\rightarrow	Server	port1		
usrp2rf1:tx/rx \rightarrow 8way0port3usrp2rf0:rx \rightarrow 8way4port2usrp2rf1:rx \rightarrow 8way4port3usrp2loin:tx \rightarrow usrp1txlo:out2usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp1	SFP+1	\rightarrow	NC	NC		
usrp2 rf0:rx → 8way4 port2 usrp2 rf1:rx → 8way4 port3 usrp2 loin:tx → usrp1 txlo:out2 usrp2 loin:rx → usrp1 rxlo:out2 usrp2 ntwrk → N_Swch Port2 usrp2 ref in → octo1 10M_3 usrp2 pps in → octo1 PPS_2 usrp2 SFP+0 → Server port1 usrp2 SFP+1 → NC NC	usrp2	rf0:tx/rx	\rightarrow	8way0	port2		
usrp2rf1:rx \rightarrow 8way4port3usrp2loin:tx \rightarrow usrp1txlo:out2usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp2	rf1:tx/rx	\rightarrow	8way0	port3		
usrp2loin:tx \rightarrow usrp1txlo:out2usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp2	rf0:rx	\rightarrow	8way4	port2		
usrp2loin:rx \rightarrow usrp1rxlo:out2usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp2	rf1:rx	\rightarrow	8way4	port3		
usrp2ntwrk \rightarrow N_SwchPort2usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp2	loin:tx	\rightarrow	usrp1	txlo:out2		
usrp2ref in \rightarrow octo110M_3usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp2	loin:rx	→	usrp1	rxlo:out2		
usrp2pps in \rightarrow octo1PPS_2usrp2SFP+0 \rightarrow Serverport1usrp2SFP+1 \rightarrow NCNC	usrp2	ntwrk		N_Swch	Port2		
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	usrp2	ref in	↑	octo1	10M_3		
usrp2 SFP+1 \rightarrow NC NC	usrp2	pps in	↑	octo1	PPS_2		
'	usrp2	SFP+0	\rightarrow	Server	port1		
<u> </u>	usrp2	SFP+1	\rightarrow	NC	NC		
usrp3 rxlo:out0	usrp3	rxlo:out0	\rightarrow	usrp7	rxlo:in1		
usrp3 rxlo:out1 \rightarrow usrp7 txlo:in1	usrp3	rxlo:out1	\rightarrow	usrp7	txlo:in1		
usrp3 rxlo:out2 \rightarrow NC NC	usrp3	rxlo:out2	\rightarrow	NC	NC		
usrp3 rxlo:out3 → NC NC	usrp3	rxlo:out3	\rightarrow	NC	NC		
usrp3 rxlo:in0 \rightarrow NC NC	usrp3	rxlo:in0	\rightarrow	NC	NC		
usrp3 rxlo:in1 \rightarrow usrp1 rxlo:out3	usrp3	rxlo:in1	\rightarrow	usrp1	rxlo:out3		
usrp3 txlo:out0 → NC NC	usrp3	txlo:out0	\rightarrow	NC	NC		
usrp3 txlo:out1 → NC NC	usrp3	txlo:out1	\rightarrow	NC	NC		

Connections					
From		То			
Port	\rightarrow	Device	Port		
txlo:out2	\rightarrow	NC	NC		
txlo:out3	→	NC	NC		
txlo:in0	\rightarrow	NC	NC		
txlo:in1	\rightarrow	usrp1	txlo:out3		
rf0:tx/rx	→	8way0	port5		
rf1:tx/rx	\rightarrow	8way0	port6		
rf0:rx	\rightarrow	8way4	port5		
rf1:rx	\rightarrow	8way4	port6		
ntwrk	→	N_Swch	Port3		
ref in	\rightarrow	octo1	10M_4		
pps in	\rightarrow	octo1	PPS_4		
SFP+0	\rightarrow	Server	port3		
SFP+1	\rightarrow	NC	NC		
	Port txlo:out2 txlo:out3 txlo:in0 txlo:in1 rf0:tx/rx rf1:tx/rx rf1:rx ntwrk ref in pps in SFP+0	From Port → txlo:out2 → txlo:in0 → txlo:in1 → rf0:tx/rx → rf1:tx/rx → rf1:rx → ntwrk → ref in → SFP+0 →	From Device txlo:out2 → NC txlo:out3 → NC txlo:in0 → NC txlo:in1 → usrp1 rf0:tx/rx → 8way0 rf1:tx/rx → 8way4 rf1:rx → 8way4 ntwrk → N_Swch ref in → octo1 pps in → server		

usrp4	rf0:tx/rx	\rightarrow	8way1	Port4
usrp4	rf1:tx/rx	\rightarrow	8way1	Port1
usrp4	rf0:rx	\rightarrow	8way5	Port0
usrp4	rf1:rx	→	8way5	Port1
usrp4	loin:tx	→	usrp5	txlo:out0
usrp4	loin:rx	\rightarrow	usrp5	rxlo:out0
usrp4	ntwrk	\rightarrow	N_Swch	port4
usrp4	ref in	\rightarrow	octo1	10M_5
usrp4	pps in	\rightarrow	octo1	PPS_5
usrp4	SFP+0	→	Server	port4
usrp4	SFP+1	\rightarrow	NC	NC
usrp5	rxlo:out0	\rightarrow	usrp4	rxlo:in1
usrp5	rxlo:out1	\rightarrow	usrp5	rxlo:in1
usrp5	rxlo:out2	\rightarrow	usrp6	rxlo:in1
usrp5	rxlo:out3	\rightarrow	usrp7	rxlo:in1
usrp5	rxlo:in0	\rightarrow	usrp7	rxlo:out0
usrp5	rxlo:in1	\rightarrow	usrp5	rxlo:out1
usrp5	txlo:out0	\rightarrow	usrp4	txlo:in1
usrp5	txlo:out1	\rightarrow	usrp5	txlo:in1
usrp5	txlo:out2	\rightarrow	usrp6	txlo:in1
usrp5	txlo:out3	\rightarrow	usrp7	txlo:in1
usrp5	txlo:in0	\rightarrow	usrp7	txlo:out0
usrp5	txlo:in1	\rightarrow	usrp5	txlo:out1
usrp5	rf0:tx/rx	\rightarrow	8way1	port7
usrp5	rf1:tx/rx	\rightarrow	8way1	port8

Connections					
	From			То	
Device	Port	\rightarrow	Device	Port	
usrp5	rf0:rx	\rightarrow	8way5	port7	
usrp5	rf1:rx	→	8way5	port8	
usrp5	ntwrk	1	N_Swch	port5	
usrp5	ref in	1	octo1	10M_6	
usrp5	pps in	→	octo1	PPS_6	
usrp5	SFP+0	1	Server	port5	
usrp5	SFP+1	1	NC	NC	
usrp6	rf0:tx/rx	1	8way1	port2	
usrp6	rf1:tx/rx	1	8way1	port3	
usrp6	rf0:rx	→	8way5	port2	
usrp6	rf1:rx	1	8way5	port3	
usrp6	loin:tx	↑	usrp5	txlo:out2	
usrp6	loin:rx	1	usrp5	rxlo:out2	
usrp6	ntwrk	1	N_Swch	Port6	
usrp6	ref in	1	octo1	10M_7	
usrp6	pps in	1	octo1	PPS_7	
usrp6	SFP+0	\rightarrow	Server	port6	
usrp6	SFP+1	→	NC	NC	
usrp7	rxlo:out0	\rightarrow	usrp1	rxlo:in0	
usrp7	rxlo:out1	\rightarrow	usrp5	rxlo:in0	
usrp7	rxlo:out2	\rightarrow	usrp9	rxlo:in0	
usrp7	rxlo:out3	→	usrp11	rxlo:in0	
usrp7	rxlo:in0	\rightarrow	usrp3	rxlo:out0	
usrp7	rxlo:in1	\rightarrow	usrp5	txlo:in0	
usrp7	txlo:out0	→	usrp1	txlo:in0	
usrp7	txlo:out1	\rightarrow	usrp5	txlo:in0	
usrp7	txlo:out2	\rightarrow	usrp9	txlo:in0	
usrp7	txlo:out3	\rightarrow	usrp11	txlo:in0	
usrp7	txlo:in0	\rightarrow	usrp3	rxlo:out1	
usrp7	txlo:in1	↑	usrp5	txlo:out3	
usrp7	rf0:tx/rx	\rightarrow	8way1	port5	
usrp7	rf1:tx/rx	\rightarrow	8way1	port6	
usrp7	rf0:rx	^	8way5	port5	
usrp7	rf1:rx	→	8way5	port6	
usrp7	ntwrk	↑	N_Swch	Port7	
usrp7	ref in	↑	octo1	10M_8	
usrp7	pps in	^	octo1	PPS_8	
usrp7	SFP+0	↑	Server	port7	
usrp7	SFP+1	\rightarrow	NC	NC	

Connections				
From				То
Device	Port	\rightarrow	Device	Port

usrp8	rf0:tx/rx	\rightarrow	8way2	Port4
usrp8	rf1:tx/rx	\rightarrow	8way2	Port1
usrp8	rf0:rx	\rightarrow	8way6	Port0
usrp8	rf1:rx	\rightarrow	8way6	Port1
usrp8	loin:tx	\rightarrow	usrp9	txlo:out0
usrp8	loin:rx	\rightarrow	usrp9	rxlo:out0
usrp8	ntwrk	\rightarrow	N_Swch	port8
usrp8	ref in	\rightarrow	octo2	10M_1
usrp8	pps in	\rightarrow	octo2	PPS_1
usrp8	SFP+0	→	Server	port8
usrp8	SFP+1	\rightarrow	NC	NC
usrp9	rxlo:out0	\rightarrow	usrp8	rxlo:in1
usrp9	rxlo:out1	\rightarrow	usrp9	rxlo:in1
usrp9	rxlo:out2	\rightarrow	usrp10	rxlo:in1
usrp9	rxlo:out3	\rightarrow	usrp11	rxlo:in1
usrp9	rxlo:in0	\rightarrow	usrp7	rxlo:out0
usrp9	rxlo:in1	\rightarrow	usrp9	rxlo:out1
usrp9	txlo:out0	\rightarrow	usrp8	txlo:in1
usrp9	txlo:out1	\rightarrow	usrp9	txlo:in1
usrp9	txlo:out2	\rightarrow	usrp10	txlo:in1
usrp9	txlo:out3	\rightarrow	usrp11	txlo:in1
usrp9	txlo:in0		usrp7	txlo:out0
usrp9	txlo:in1	\rightarrow	usrp9	txlo:out1
usrp9	rf0:tx/rx	→	8way2	port7
usrp9	rf1:tx/rx	\rightarrow	8way2	port8
usrp9	rf0:rx	→	8way6	port7
usrp9	rf1:rx	→	8way6	port8
usrp9	ntwrk	\rightarrow	N_Swch	port9
usrp9	ref in	\rightarrow	octo2	10M_2
usrp9	pps in	\rightarrow	octo2	PPS_2
usrp9	SFP+0	\rightarrow	Server	port9
usrp9	SFP+1	\rightarrow	NC	NC
usrp10	rf0:tx/rx	\rightarrow	8way2	port2
usrp10	rf1:tx/rx	\rightarrow	8way2	port3
usrp10	rf0:rx	\rightarrow	8way6	port2
usrp10	rf1:rx	\rightarrow	8way6	port3
usrp10	loin:tx	\rightarrow	usrp9	txlo:out2
usrp10	loin:rx	\rightarrow	usrp9	rxlo:out2

Connections				
	From		То	
Device	Port	\rightarrow	Device	Port
usrp10	ntwrk	\rightarrow	N_Swch	Port10
usrp10	ref in	→	octo2	10M_3
usrp10	pps in	\rightarrow	octo2	PPS_3
usrp10	SFP+0	^	Server	Port10
usrp10	SFP+1	^	NC	NC
usrp11	rf0:tx/rx	→	8way2	port2
usrp11	rf1:tx/rx	→	8way2	port3
usrp11	rf0:rx	\rightarrow	8way6	port2
usrp11	rf1:rx	→	8way6	port3
usrp11	loin:tx	→	usrp9	txlo:out2
usrp11	loin:rx	→	usrp9	rxlo:out2
usrp11	ntwrk	^	N_Swch	Port11
usrp11	ref in	^	octo2	10M_4
usrp11	pps in	\rightarrow	octo2	PPS_4
usrp11	SFP+0	→	Server	port11
usrp11	SFP+1	\rightarrow	NC	NC

usrp12	rf0:tx/rx	\rightarrow	8way3	Port4
usrp12	rf1:tx/rx	→	8way3	Port1
usrp12	rf0:rx	^	8way7	Port0
usrp12	rf1:rx	→	8way7	Port1
usrp12	loin:tx	↑	usrp13	txlo:out0
usrp12	loin:rx	→	usrp13	rxlo:out0
usrp12	ntwrk		N_Swch	port12
usrp12	ref in	\rightarrow	octo2	10M_5
usrp12	pps in	\rightarrow	octo2	PPS_5
usrp12	SFP+0	→	Server	port12
usrp12	SFP+1	→	NC	NC
usrp13	rxlo:out0	1	usrp12	rxlo:in1
usrp13	rxlo:out1	→	usrp13	rxlo:in1
usrp13	rxlo:out2	^	usrp14	rxlo:in1
usrp13	rxlo:out3	→	usrp15	rxlo:in1
usrp13	rxlo:in0	\rightarrow	usrp7	rxlo:out0
usrp13	rxlo:in1	\rightarrow	usrp13	rxlo:out1
usrp13	txlo:out0	\rightarrow	usrp12	txlo:in1
usrp13	txlo:out1	\rightarrow	usrp13	txlo:in1
usrp13	txlo:out2	→	usrp14	txlo:in1
usrp13	txlo:out3	→	usrp15	txlo:in1
usrp13	txlo:in0	\rightarrow	usrp7	txlo:out0
	·		-	

	Connections				
	From			То	
Device	Port	\rightarrow	Device	Port	
usrp13	txlo:in1	\rightarrow	usrp13	txlo:out1	
usrp13	rf0:tx/rx	\rightarrow	8way3	port7	
usrp13	rf1:tx/rx	\rightarrow	8way3	port8	
usrp13	rf0:rx	\rightarrow	8way7	port7	
usrp13	rf1:rx	\rightarrow	8way7	port8	
usrp13	ntwrk	→	N_Swch	port13	
usrp13	ref in	\rightarrow	octo2	10M_6	
usrp13	pps in	1	octo2	PPS_6	
usrp13	SFP+0	^	Server	port13	
usrp13	SFP+1	^	NC	NC	
usrp14	rf0:tx/rx	→	8way3	port2	
usrp14	rf1:tx/rx	1	8way3	port3	
usrp14	rf0:rx	\rightarrow	8way7	port2	
usrp14	rf1:rx	↑	8way7	port3	
usrp14	loin:tx	→	usrp13	txlo:out2	
usrp14	loin:rx	\rightarrow	usrp13	rxlo:out2	
usrp14	ntwrk	^	N_Swch	Port14	
usrp14	ref in	\rightarrow	octo2	10M_7	
usrp14	pps in	\rightarrow	octo2	PPS_7	
usrp14	SFP+0	\rightarrow	Server	port14	
usrp14	SFP+1	^	NC	NC	
usrp15	rf0:tx/rx	↑	8way3	port2	
usrp15	rf1:tx/rx	→	8way3	port3	
usrp15	rf0:rx	\rightarrow	8way7	port2	
usrp15	rf1:rx	^	8way7	port3	
usrp15	loin:tx	↑	usrp13	txlo:out2	
usrp15	loin:rx	\rightarrow	usrp13	rxlo:out2	
usrp15	ntwrk	\rightarrow	N_Swch	Port15	
usrp15	ref in	\rightarrow	octo2	10M_8	
usrp15	pps in	\rightarrow	octo2	PPS_8	
usrp15	SFP+0	\rightarrow	Server	port15	
usrp15	SFP+1	\rightarrow	NC	NC	
				mfO.tv/mv	

8way0	port0	\rightarrow	usrp0	rf0:tx/rx
8way0	port1	→	usrp0	rf1:tx/rx
8way0	port2	→	usrp1	rf0:tx/rx
8way0	port3	→	usrp1	rf1:tx/rx
8way0	port4(sum)	→	4way0	port0
8way0	port5	\rightarrow	usrp2	rf0:tx/rx
8way0	port6	\rightarrow	usrp2	rf1:tx/rx

Connections				
	From			То
Device	Port	\rightarrow	Device	Port
8way0	port7	\rightarrow	usrp3	rf0:tx/rx
8way0	port8	\rightarrow	usrp3	rf1:tx/rx
8way1	port0	\rightarrow	usrp4	rf0:tx/rx
8way1	port1	\rightarrow	usrp4	rf1:tx/rx
8way1	port2	\rightarrow	usrp5	rf0:tx/rx
8way1	port3	→	usrp5	rf1:tx/rx
8way1	port4(sum)	→	4way0	port1
8way1	port5	\rightarrow	usrp6	rf0:tx/rx
8way1	port6	\rightarrow	usrp6	rf1:tx/rx
8way1	port7	\rightarrow	usrp7	rf0:tx/rx
8way1	port8	\rightarrow	usrp7	rf1:tx/rx
8way2	port0	\rightarrow	usrp8	rf0:tx/rx
8way2	port1	\rightarrow	usrp8	rf1:tx/rx
8way2	port2	\rightarrow	usrp9	rf0:tx/rx
8way2	port3	\rightarrow	usrp9	rf1:tx/rx
8way2	port4(sum)	\rightarrow	4way0	port3
8way2	port5	\rightarrow	usrp10	rf0:tx/rx
8way2	port6	\rightarrow	usrp10	rf1:tx/rx
8way2	port7	\rightarrow	usrp11	rf0:tx/rx
8way2	port8	\rightarrow	usrp11	rf1:tx/rx
8way3	port0	\rightarrow	usrp12	rf0:tx/rx
8way3	port1	\rightarrow	usrp12	rf1:tx/rx
8way3	port2	\rightarrow	usrp13	rf0:tx/rx
8way3	port3	\rightarrow	usrp13	rf1:tx/rx
8way3	port4(sum)	\rightarrow	4way0	port4
8way3	port5	\rightarrow	usrp14	rf0:tx/rx
8way3	port6	\rightarrow	usrp14	rf1:tx/rx
8way3	port7	\rightarrow	usrp15	rf0:tx/rx
8way3	port8	\rightarrow	usrp15	rf1:tx/rx

8way4	port0	\rightarrow	usrp0	rf0:rx
8way4	port1	\rightarrow	usrp0	rf1:rx
8way4	port2	→	usrp1	rf0:rx
8way4	port3	→	usrp1	rf1:rx
8way4	port4(sum)	→	4way1	port0
8way4	port5	\rightarrow	usrp2	rf0:rx
8way4	port6	\rightarrow	usrp2	rf1:rx
8way4	port7	\rightarrow	usrp3	rf0:rx
8way4	port8	\rightarrow	usrp3	rf1:rx
8way5	port0	\rightarrow	usrp4	rf0:rx
			•	

Connections				
	From			То
Device	Port	\rightarrow	Device	Port
8way5	port1	\rightarrow	usrp4	rf1:rx
8way5	port2	\rightarrow	usrp5	rf0:rx
8way5	port3	\rightarrow	usrp5	rf1:rx
8way5	port4(sum)	\rightarrow	4way1	port1
8way5	port5	\rightarrow	usrp6	rf0:rx
8way5	port6	\rightarrow	usrp6	rf1:rx
8way5	port7	\rightarrow	usrp7	rf0:rx
8way5	port8	\rightarrow	usrp7	rf1:rx
8way6	port0	\rightarrow	usrp8	rf0:rx
8way6	port1	→	usrp8	rf1:rx
8way6	port2	→	usrp9	rf0:rx
8way6	port3	^	usrp9	rf1:rx
8way6	port4(sum)	\rightarrow	4way1	port3
8way6	port5	→	usrp10	rf0:rx
8way6	port6	→	usrp10	rf1:rx
8way6	port7	→	usrp11	rf0:rx
8way6	port8	\rightarrow	usrp11	rf1:rx
8way7	port0	\rightarrow	usrp12	rf0:rx
8way7	port1	→	usrp12	rf1:rx
8way7	port2	^	usrp13	rf0:rx
8way7	port3	^	usrp13	rf1:rx
8way7	port4(sum)	^	4way1	port4
8way7	port5	\rightarrow	usrp14	rf0:rx
8way7	port6	\rightarrow	usrp14	rf1:rx
8way7	port7	\rightarrow	usrp15	rf0:rx
8way7	port8	\rightarrow	usrp15	rf1:rx

4way0	port0	\rightarrow	8way0	port4(sum)
4way0	port1	\rightarrow	8way1	port4(sum)
4way0	port2(sum)	\rightarrow	4way1	port2(sum)
4way0	port3	\rightarrow	8way2	port4(sum)
4way0	port4	\rightarrow	8way3	port4(sum)
4way1	port0	\rightarrow	8way4	port4(sum)
4way1	port1	\rightarrow	8way5	port4(sum)
4way1	port2(sum)	\rightarrow	4way0	port2(sum)
4way1	port3	\rightarrow	8way6	port4(sum)
4way1	port4	\rightarrow	8way7	port4(sum)

N_Swch	port0	→	usrp0	ntwrk
N_Swch	port1	\rightarrow	usrp1	ntwrk

Connections					
	From		То		
Device	Port	\rightarrow	Device	Port	
N_Swch	port2	\rightarrow	usrp2	ntwrk	
N_Swch	port3	\rightarrow	usrp3	ntwrk	
N_Swch	port4	\rightarrow	usrp4	ntwrk	
N_Swch	port5	\rightarrow	usrp5	ntwrk	
N_Swch	port6	\rightarrow	usrp6	ntwrk	
N_Swch	port7	\rightarrow	usrp7	ntwrk	
N_Swch	port8	\rightarrow	usrp8	ntwrk	
N_Swch	port9	\rightarrow	usrp9	ntwrk	
N_Swch	port10	\rightarrow	usrp10	ntwrk	
N_Swch	port11	\rightarrow	usrp11	ntwrk	
N_Swch	port12	\rightarrow	usrp12	ntwrk	
N_Swch	port13	\rightarrow	usrp13	ntwrk	
N_Swch	port14	\rightarrow	usrp14	ntwrk	
N_Swch	port15	\rightarrow	usrp15	ntwrk	

Server	port0	\rightarrow	usrp0	Sfp+0
Server	port1	\rightarrow	usrp1	Sfp+0
Server	port2	\rightarrow	usrp2	Sfp+0
Server	port3	\rightarrow	usrp3	Sfp+0
Server	port4	\rightarrow	usrp4	Sfp+0
Server	port5	\rightarrow	usrp5	Sfp+0
Server	port6	→	usrp6	Sfp+0
Server	port7	\rightarrow	usrp7	Sfp+0
Server	port8	\rightarrow	usrp8	Sfp+0
Server	port9	\rightarrow	usrp9	Sfp+0
Server	port10	→	usrp10	Sfp+0
Server	port11	→	usrp11	Sfp+0
Server	port12	→	usrp12	Sfp+0
Server	port13	\rightarrow	usrp13	Sfp+0
Server	port14	\rightarrow	usrp14	Sfp+0
Server	port15	\rightarrow	usrp15	Sfp+0

Name Reference			
Device Name	Device Type		
octo0	octoclock-G CDA-2990		
octo1	octoclock-G CDA-2990		
octo2	octoclock-G CDA-2990		
usrp0	USRP N320		
usrp1	USRP N321		
usrp2	USRP N320		
usrp3	USRP N321		
usrp4	USRP N320		
usrp5	USRP N321		
usrp6	USRP N320		
usrp7	USRP N321		
usrp8	USRP N320		
usrp9	USRP N321		
usrp10	USRP N320		
usrp11	USRP N320		
usrp12	USRP N320		
usrp13	USRP N321		
usrp14	USRP N320		
usrp15	USRP N320		
8way0	8 way power splitter		
8way1	8 way power splitter		
8way2	8 way power splitter		
8way3	8 way power splitter		
8way4	8 way power splitter		
8way5	8 way power splitter		
8way6	8 way power splitter		
8way7	8 way power splitter		
4way0	4 way power splitter		
4way1	4 way power splitter		
N_Swch	Network Switch		