Automated reasoning about variability models with solvers

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Material

https://github.com/FAMILIAR-project/ HackOurLanguages-SIF

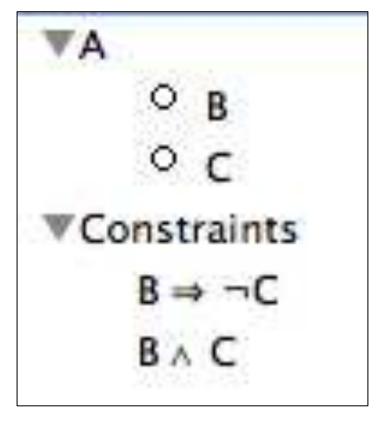
Plan

- Feature Models
 - Defacto standard for modeling product lines and variability
 - Syntax, semantics, automated reasoning, synthesis

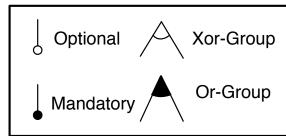
Contract

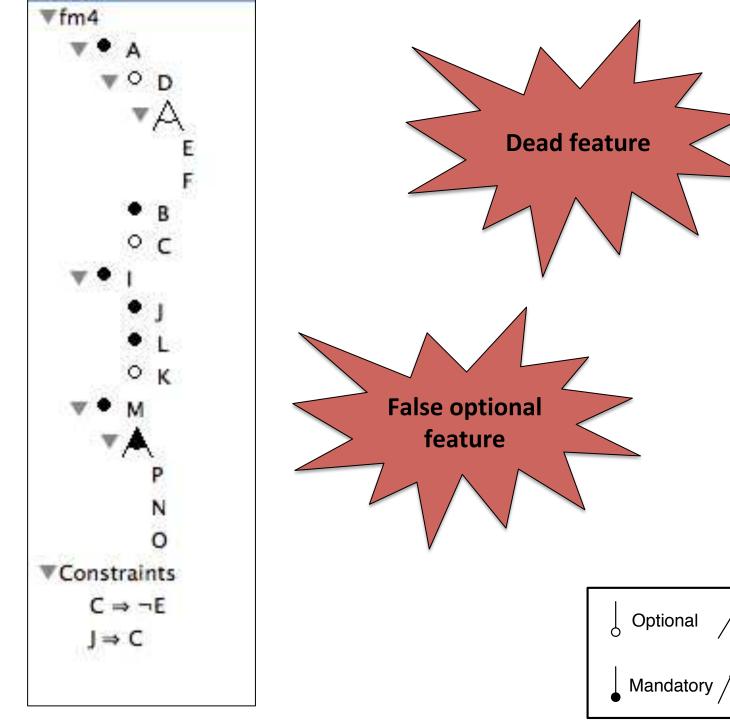
- The idea of software product lines and variability
 - You will be able to recognize this class of systems
 - Aware of the complexity, the specific development process, and existing techniques
- Feature modeling
 - A widely used formalism for modeling product lines and configurable systems in a broad sense
- Composing/Decomposing feature models with a domain-specific language
- Reverse engineering variability models

I want to analyze and play with my specification!

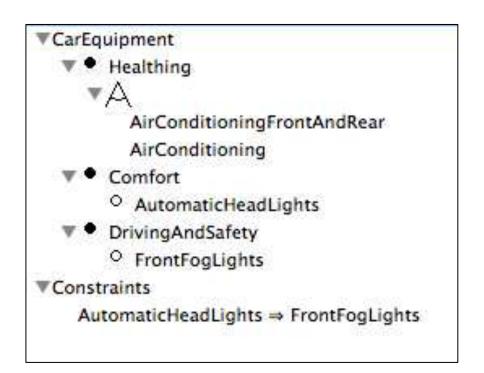


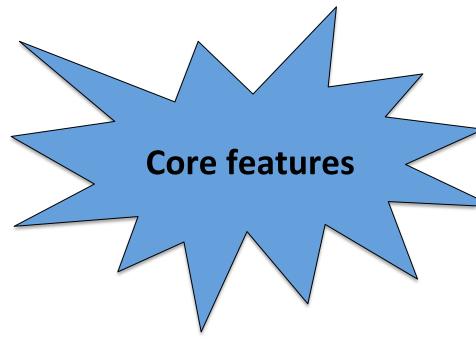




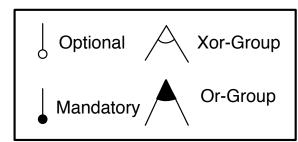


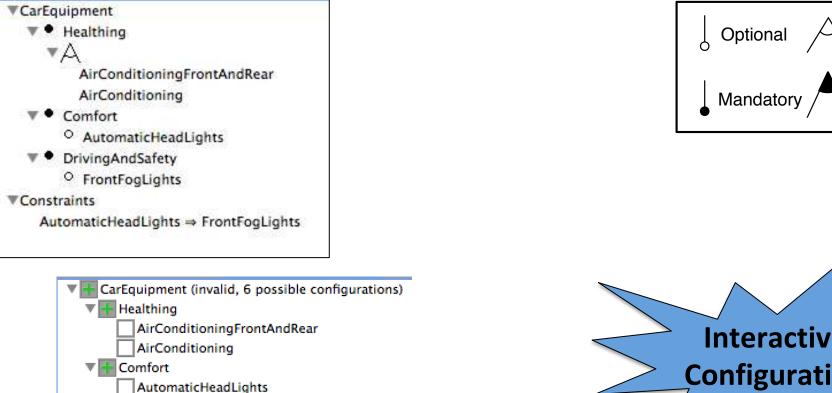
Xor-Group





{CarEquipment, Comfort, DrivingAndSafety, Healthing}





CarEquipment (valid, 3 possible configurations)

AirConditioningFrontAndRear

Healthing

Comfort

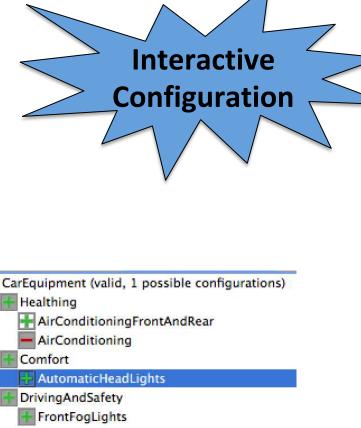
AirConditioning

DrivingAndSafety

FrontFogLights

AutomaticHeadLights

DrivingAndSafety
FrontFogLights



Feature Models and Automated Reasoning

Benavides et al. survey, 2010

	Batory [5]	Czamecki et al. [30]	Gheyi et al. [37]	Mannionet al. [51, 52]	Mendonca et al. [57]	Mendonca et al. [56]	Sun et al [74]	Thüm et al. [75]	van der Storm [86, 87]	Zhang et al. [102, 101]	Zhang et al. [103]	Yan et al. [100]	Benavides et al. [10, 11, 12]	Benavides et al. [15]	Djebii et al. [34]	Trinidad et al. [78, 76]	White et al. [99]	White et al. [97]	Abo Zaid et al. [1]	Fan et al. [35]	Wang et al. [92, 93]	Benavides et al. [14]	Benavides et al. [16]	Segura [70]	Bachmeyeret al. [4]	Cao et al. [20]	Fernandez et al. [36]	Hemakumar [41]	Gheyi et al. [38]	Kang et al. [43]	Mendonca et al [55]	Osman et al. [59, 60]	Salinesi et al. [66]	Van den Broek et al. [84]	Van Deursen et al. [88]	Von der Massen et al. [90]	Von der Massen et al. [91]	White et al. [98, 96]	Batory et al. [7]	Schobbens et al. [42, 68, 69]	Trinidad et al. [80]	Von der Massen et al. [89]
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#Products		+		0									+	+	+							+	+	:+:			+							+	⊕		+				-	
Dead features		2				+				+	+	+				+			+											0		+	+	+					~		~	~
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Redundancies			1					1																									I.									~
Variant features																																									~	
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Feature model notation	В	C	В	В	В	В	В	В	В	В	С	В	В	С	С	В	В	В	В	В	В	В	С	В	В	В	С	В	В	В	С	C	С	В	В	В	В	В	В	С	С	В
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Decision problems and complexity

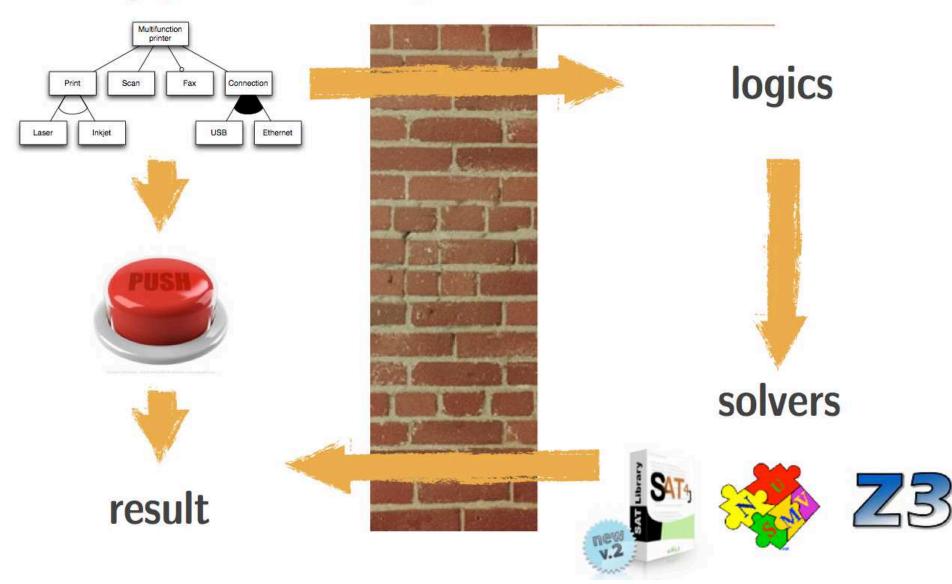
- Validity of a feature model
- Validity of a configuration
- Computation of dead and core features
- Counting of the number of valid configurations
- Equivalence between two feature models
- Satisfiability (SAT) problem
 - NP-complete

How to automate analysis of your feature models?

Binary Decision Diagram (BDD)

SAT solver

Typical implementations



Truth table, boolean function

fr	om		to	
X_1	X_2	X ₃	X_4	f
0	X ₂ 0 0 0 1 1 1 0 0 0 1 1 1 1 1 1 1	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
X ₁ 0 0 0 0 0 0 1 1 1 1 1 1	1	X ₃ 0 0 1 1 0 0 1 1 0 1 1 1 1 1 1 1 1 1 1	X ₄ 0 1 0 1 0 1 0 1 0 1 0 1 0 1 1 1 0 1 0	f 0 1 1 0 0 0 1 0 0 0 0 0 0

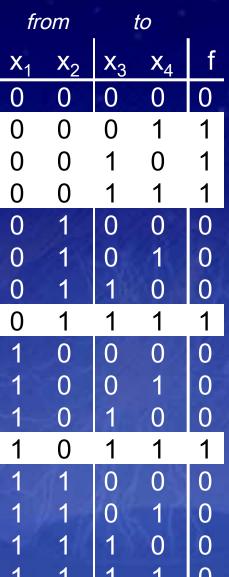
BDDs from Truth Tables

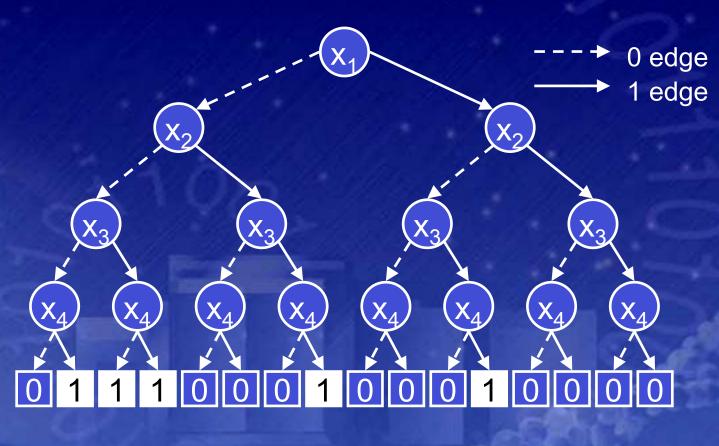
Truth Table **Binary Decision Tree** Binary Decision Diagram (BDD) Ordered Binary Decision Diagram (OBDD) Reduced Ordered Binary Decision Diagram

(ROBDD, simply called BDD)

Binary Decision Diagrams (Bryant 1986)

encoding of a truth table.



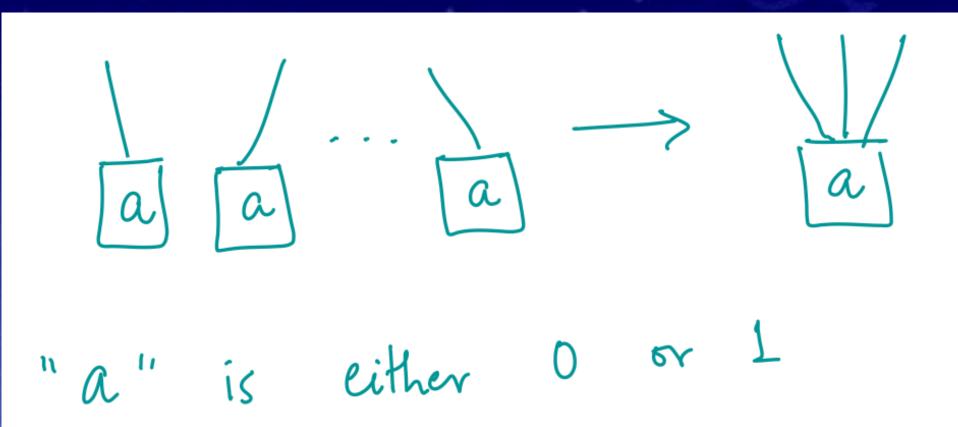


Reduction

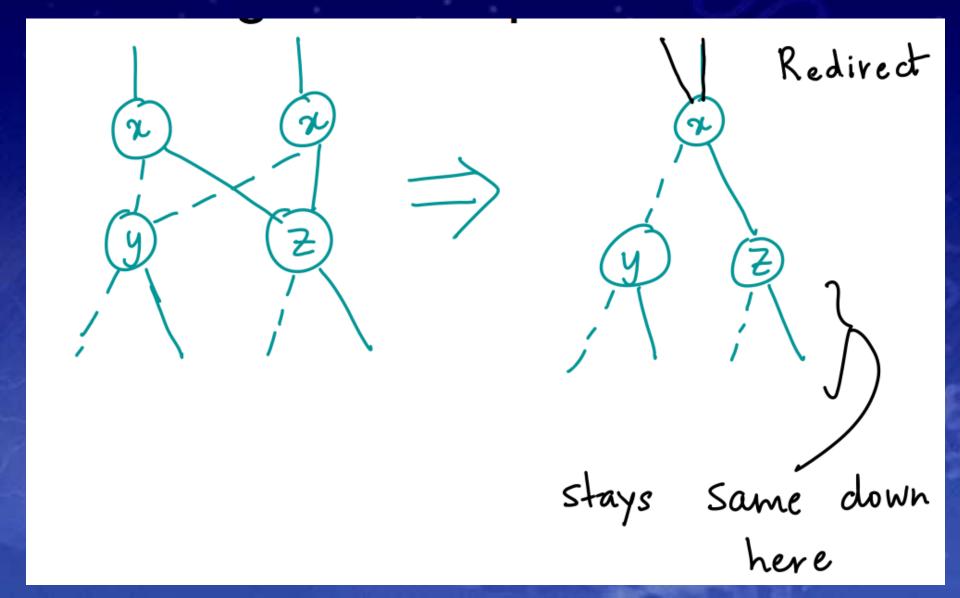
Identify Redundancies

- 3 Rules
 - Merge equivalent leaves
 - Merge isomorphic nodes
 - Eliminate redundant tests

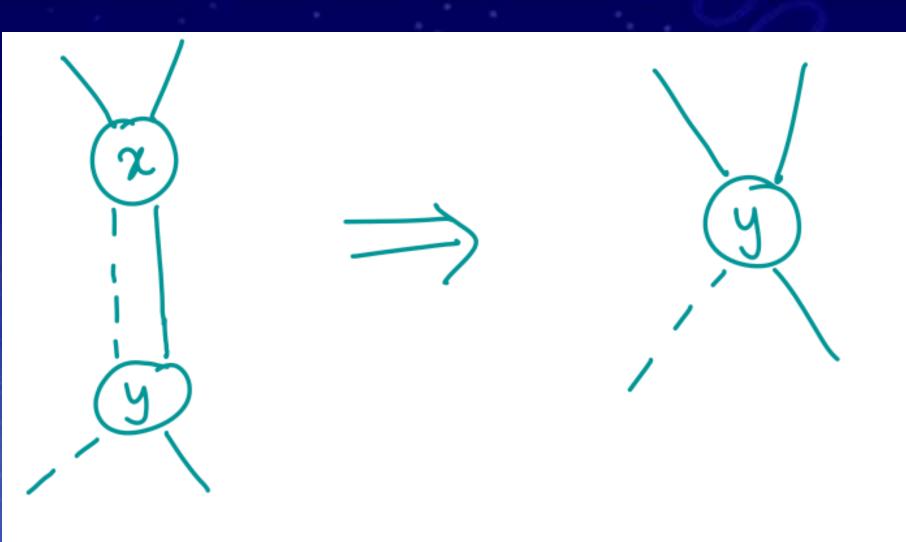
Merge equivalent leaves

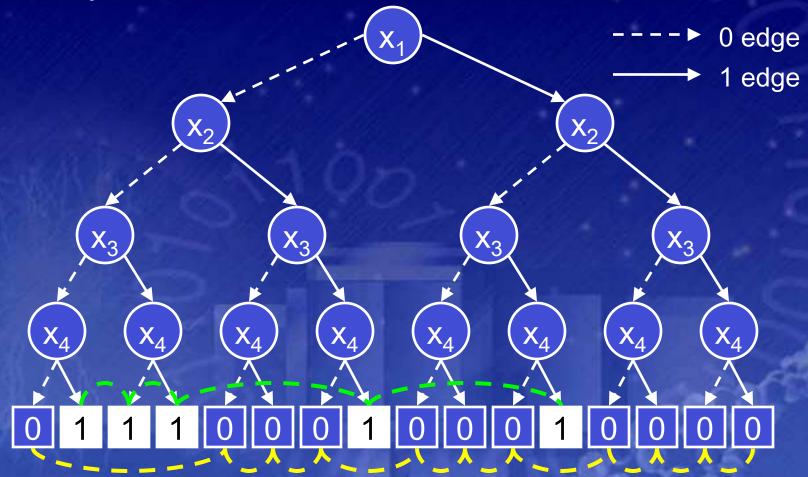


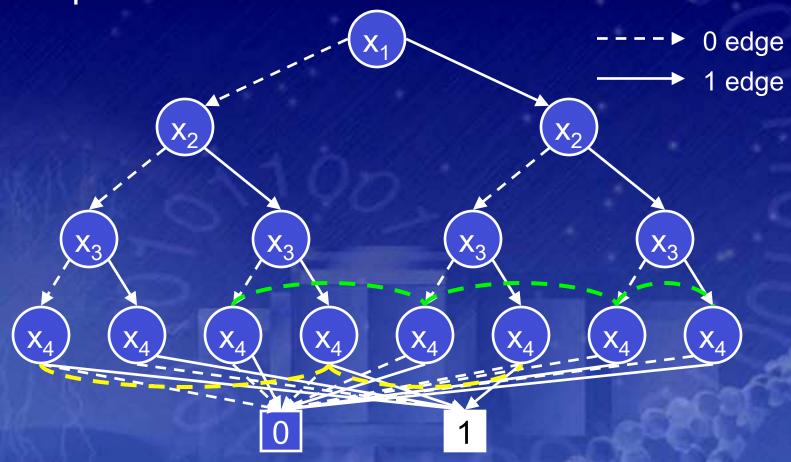
Merge isomorphic nodes

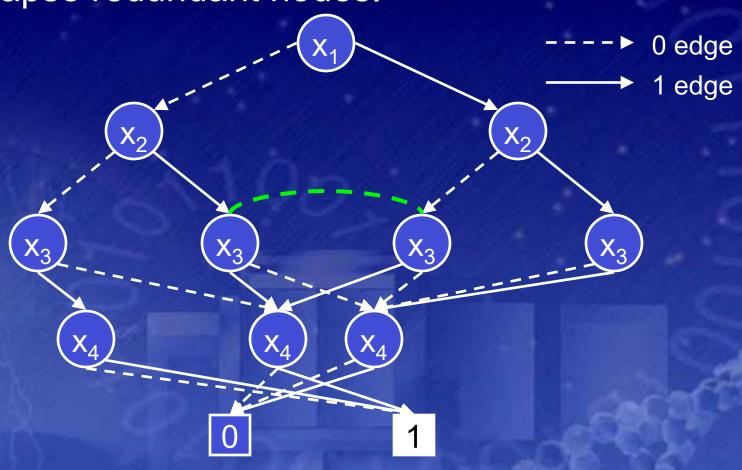


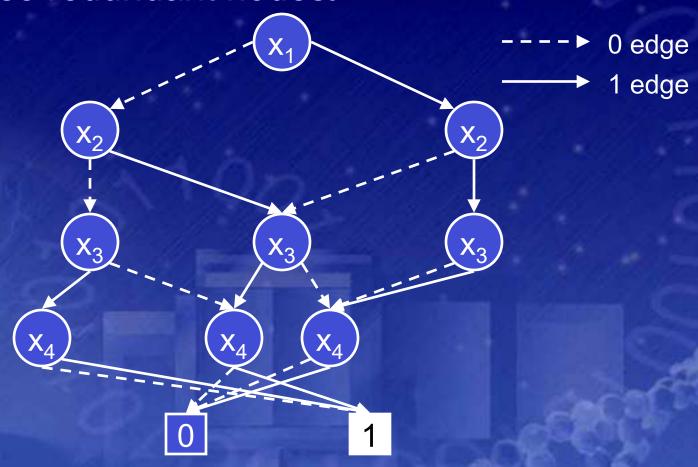
Eliminate redundant tests



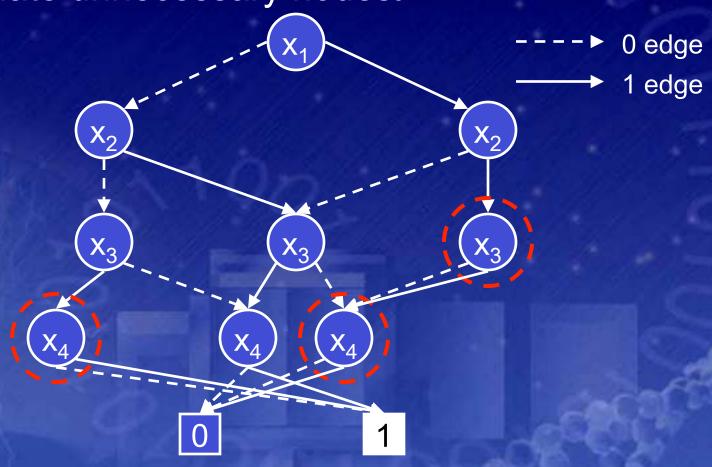




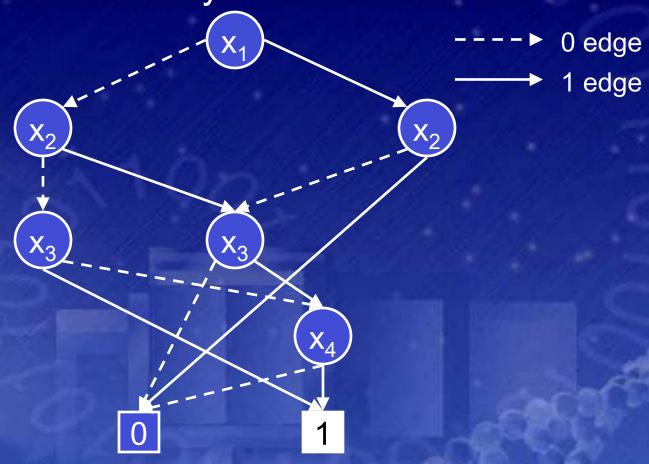




• Eliminate unnecessary nodes.



• Eliminate unnecessary nodes.

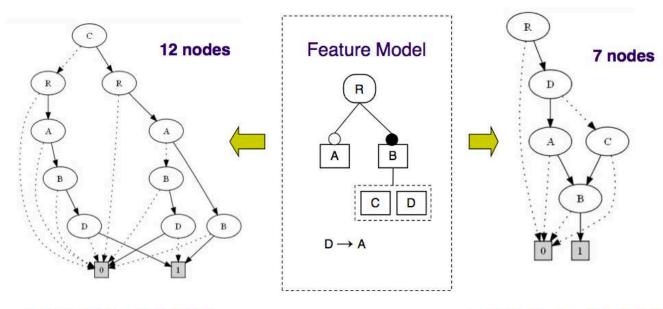


Binary Decision Diagrams (BDDs)

- Very efficient structure for most of the satisfiability operations
- Polynomial in time for checking satisfiability and determining equivalence between two BDDs
- Graph trasversal
- So great?

Binary Decision Diagrams (BDDs): Theoretical Problem

- The size of the BDD is very sensitive to the order of the BDD variables
 - e.g. two equivalent BDDs for the same feature

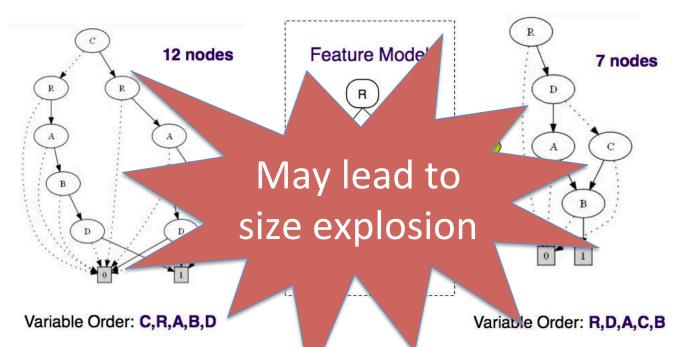


Variable Order: C,R,A,B,D

Variable Order: R,D,A,C,B

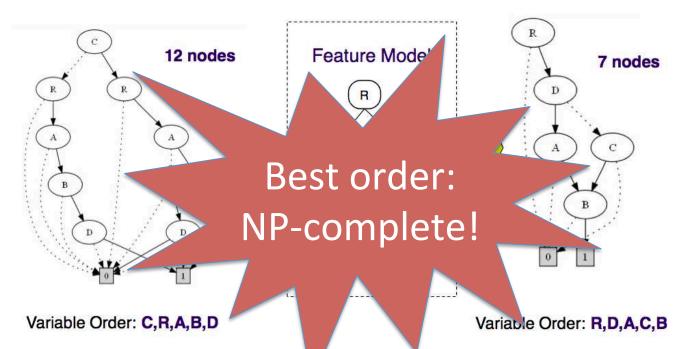
Binary Decision Diagrams (BDDs): Theoretical Problem

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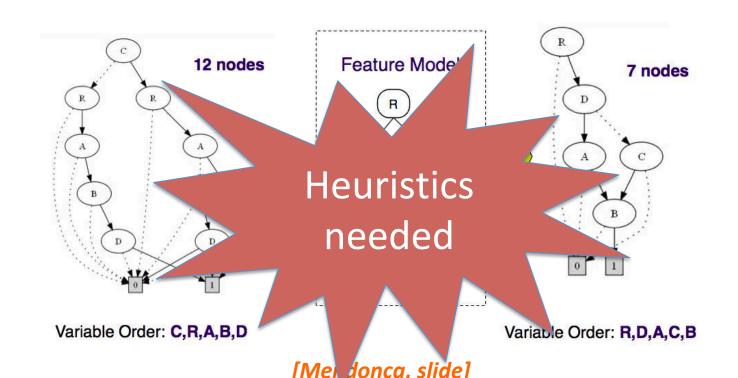
Binary Decision Diagrams (BDDs): Theoretical Problem

- The size of the BDD is very sensitive to the order of the BDD variables
 - e.g. two equivalent BDDs for the same feature



Binary Decision Diagrams (BDDs): Practical Problem

 The size of the BDD is very sensitive to the <u>order</u> of the BDD variables. In practice: <u>BDDs</u> cannot be build for feature models with 2000+ features

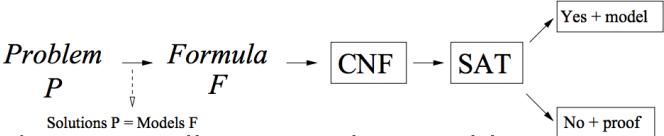


How to automate analysis of your feature models?

Let us try with SAT solvers

Satisfiability (SAT) solver

- A "SAT solver" is a program that automatically decides whether a propositional logic formula is satisfiable.
 - If it is satisfiable, a SAT solver will produce an example of a truth assignment that satisfies the formula.



- Basic idea: since all NP-complete problems are mutually reducible:
 - Write one really good solver for NP-complete problems (in fact, get lots of people to do it. Hold competitions.)
 - Translate your NP-complete problems to that problem.

SAT solver and **CNF**

- All current fast SAT solvers work on CNF
- Terminology:
 - A literal is a propositional variable or its negation (e.g., p or ¬q).
 - A clause is a disjunction of literals (e.g., (p $\vee \neg q \vee r$)). Since \vee is associative, we can represent clauses as lists of literals.
- A formula is in conjunctive normal form (CNF) if it is a conjunction of clauses
 - e.g., (p \vee q \vee \neg r) \wedge (\neg p \vee s \vee t \vee \neg u)

SAT solver and Unit Propagation

- Whenever all the literals in a clause are false except one, the remaining literal must be true in any satisfying assignment (such a clause is called a unit clause).
 - Therefore, the algorithm can assign it to true immediately. After choosing a variable there are often many unit clauses.
 - Setting a literal in a unit clause often creates other unit clauses, leading to a cascade.

$$\{ \neg p \lor q, \ \neg p \lor \neg q \lor r, \ p, \ \neg r \}. \quad \begin{array}{c|c} \neg p \lor q & q \\ \neg p \lor \neg q \lor r & \neg q \lor r \\ \nearrow r & \neg r \end{array} \quad \begin{array}{c|c} q \\ \neg q \lor r \\ \neg r & \neg r \end{array}$$

 A good SAT solver often spends 80-90% of its time in unit propagation.

$$\mathcal{F}_{\text{unit}} := (\neg x_1 \lor \neg x_3 \lor x_4) \land (\neg x_1 \lor \neg x_2 \lor x_3)$$
$$(\neg x_1 \lor x_2) \land (x_1 \lor x_3 \lor x_6) \land (\neg x_1 \lor x_4 \lor \neg x_5)$$
$$(x_1 \lor \neg x_6) \land (x_4 \lor x_5 \lor x_6) \land (x_5 \lor \neg x_6)$$

$$\mathcal{F}_{\text{unit}} := (\neg x_1 \lor \neg x_3 \lor x_4) \land (\neg x_1 \lor \neg x_2 \lor x_3)$$

$$(\neg x_1 \lor x_2) \land (x_1 \lor x_3 \lor x_6) \land (\neg x_1 \lor x_4 \lor \neg x_5)$$

$$(x_1 \lor \neg x_6) \land (x_4 \lor x_5 \lor x_6) \land (x_5 \lor \neg x_6)$$

$$\varphi = \{x_1 = 1\}$$

$$\mathcal{F}_{\text{unit}} := (\neg x_1 \lor \neg x_3 \lor x_4) \land (\neg x_1 \lor \neg x_2 \lor x_3)$$

$$(\neg x_1 \lor x_2) \land (x_1 \lor x_3 \lor x_6) \land (\neg x_1 \lor x_4 \lor \neg x_5)$$

$$(x_1 \lor \neg x_6) \land (x_4 \lor x_5 \lor x_6) \land (x_5 \lor \neg x_6)$$

 $\varphi = \{x_1 = 1, x_2 = 1\}$

$$\mathcal{F}_{\text{unit}} := (\neg x_1 \lor \neg x_3 \lor x_4) \land (\neg x_1 \lor \neg x_2 \lor x_3)$$

$$(\neg x_1 \lor x_2) \land (x_1 \lor x_3 \lor x_6) \land (\neg x_1 \lor x_4 \lor \neg x_5)$$

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$$\varphi = \{x_1 = 1, x_2 = 1, x_3 = 1\}$$

$$\mathcal{F}_{\text{unit}} := (\neg x_1 \lor \neg x_3 \lor x_4) \land (\neg x_1 \lor \neg x_2 \lor x_3)$$

$$(\neg x_1 \lor x_2) \land (x_1 \lor x_3 \lor x_6) \land (\neg x_1 \lor x_4 \lor \neg x_5)$$

$$(x_1 \lor \neg x_6) \land (x_4 \lor x_5 \lor x_6) \land (x_5 \lor \neg x_6)$$

$$\varphi = \{x_1 = 1, x_2 = 1, x_3 = 1, x_4 = 1\}$$

SAT solver and Unit Propagation

```
BCP():
  Repeatedly search for unit clauses, and
     set unassigned literal to required value.
  If a literal is assigned conflicting values, return F
     else return T;
satisfy(\phi) {
  if every clause of \phi has a true literal, return T;
  if BCP() == F, return F;
  assign appropriate values to all pure literals;
  choose an x \in V that is unassigned in A,
     and choose v \in \{T, F\}.
  A(x) = v;
  if satisfy(\phi) return T;
  A(x) = \neg v;
  if satisfy(\phi) return T;
  unassign A(x); // undo assignment for backtracking.
  return F; }
```

How to automate analysis of your feature models?

Let us use BDDs and SAT solvers

A ^ C => A ^ D => A

FAMiliAR



```
fm1bis = FM ("foo3.dimacs")
fm1bisbis = FM ("foo3.constraints")
```

```
fml> c1 = cores fm1

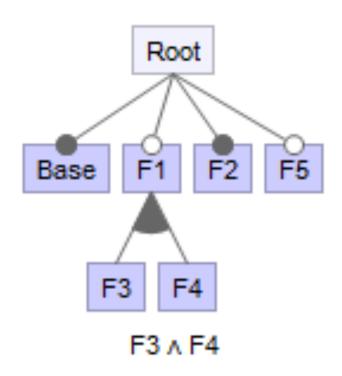
fml> s1: (SET) {B;A}
s1: (SE fml> c1bis = cores fm1bis
fml> fml> compare fm1 fm1bis
s1bis
fml> compare fm1bis fm1bisbis
s1bis
fml> compare fm1bis fm1bisbis
s1bis
fml> compare fm1bis fm1bisbis
fml> compare fm1bis fm1bisbis
fml> c1 eq c1bisbis
fml> s1
res3: (fml> c1 eq c1bisbis
fml> s1
res6: (B00LEAN) true
res4: (B00LEAN) true
```

```
B C Optional Xor-Group Or-Group
```

```
fml> fm1 = FM ("output/fm1.tvl")
root A {
    group [ 3..3 ] {
       opt D {
       },
       B {
       },
       opt C {
       }
    }
}
fm1: (FEATURE_MODEL) A: [D] B [C];
```

Consistency

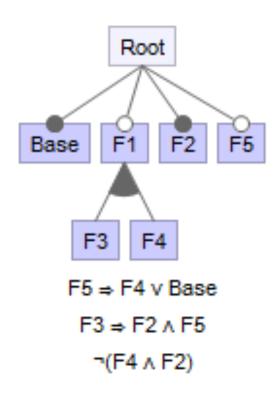
- SAT-Solver
 - -SAT(FM)



Core and dead features

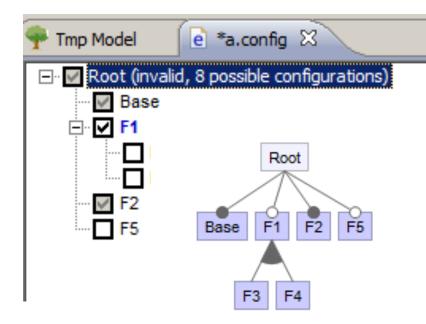
Dead : SAT(FM ^ F)

Core: SAT(FM ^ not(F))

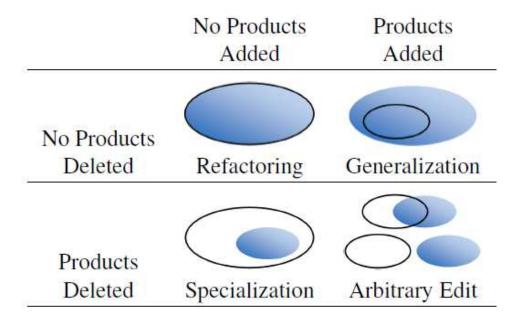


Partial configuration

- SAT(FM ^ PK ^ F)
- SAT(FM ^ PK ^ not(F))



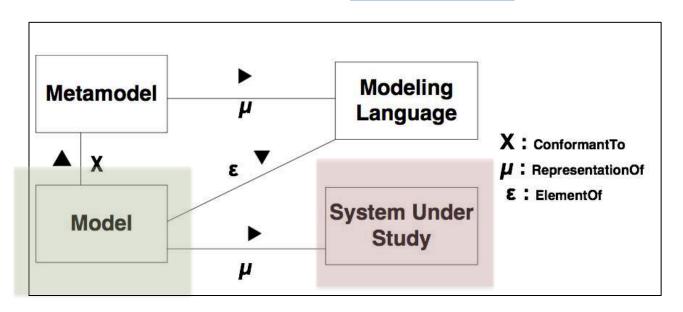
Relationship between feature models



- Refactoring
 - Tautology: (FM1 <=> FM2)
 - = not SAT(not (FM1 <=> FM2))

Recap

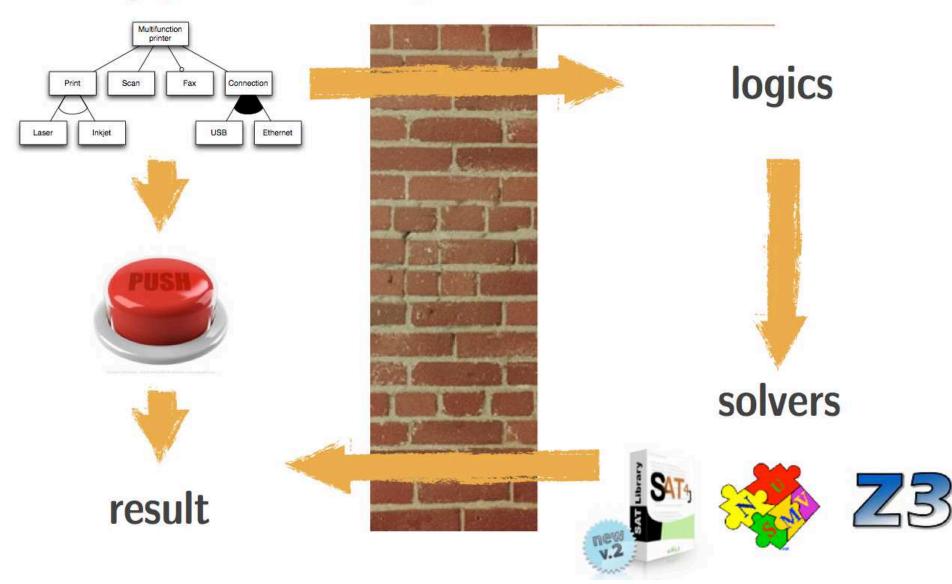
Feature Models







Typical implementations

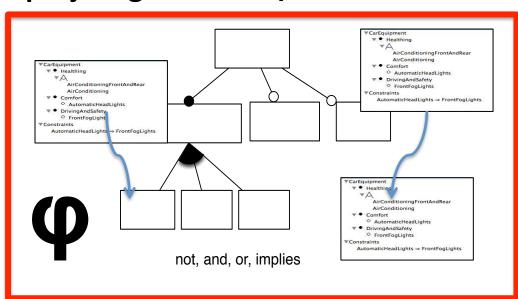


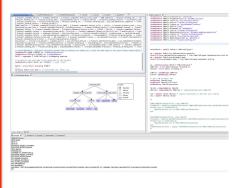


(FeAture Model script Language for manipulation and Automatic Reasoning)

http://familiar-project.github.com/

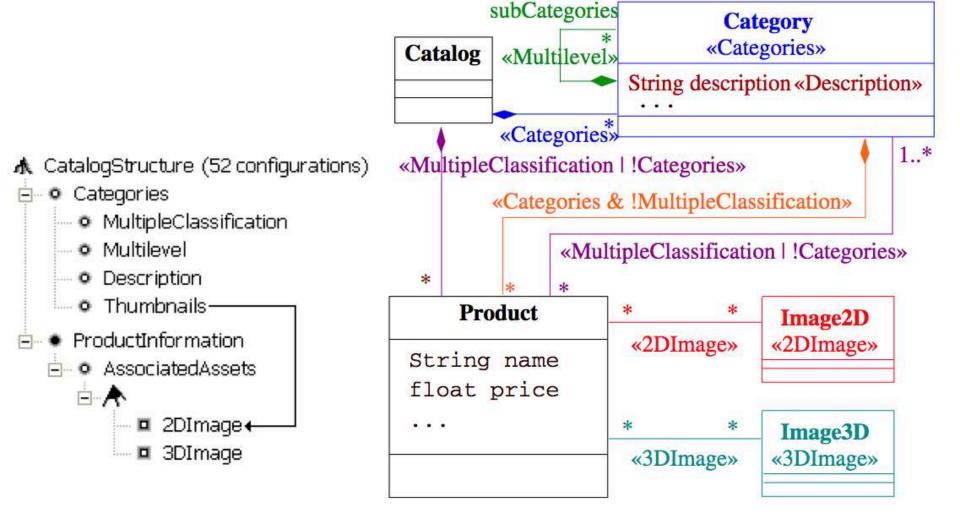


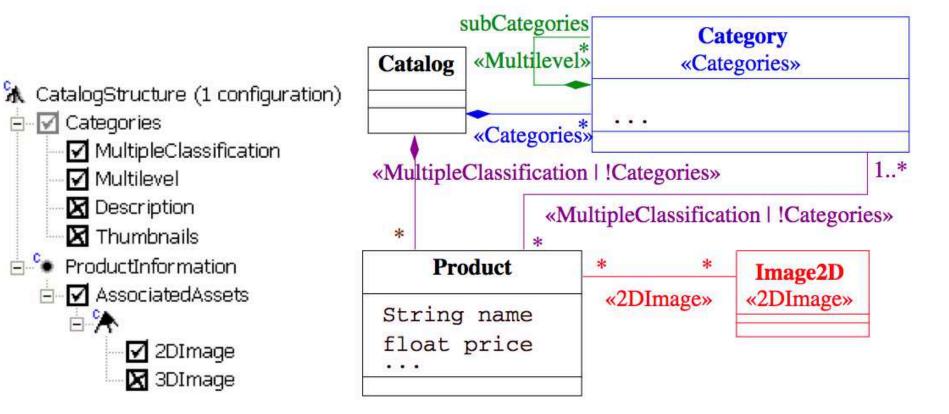




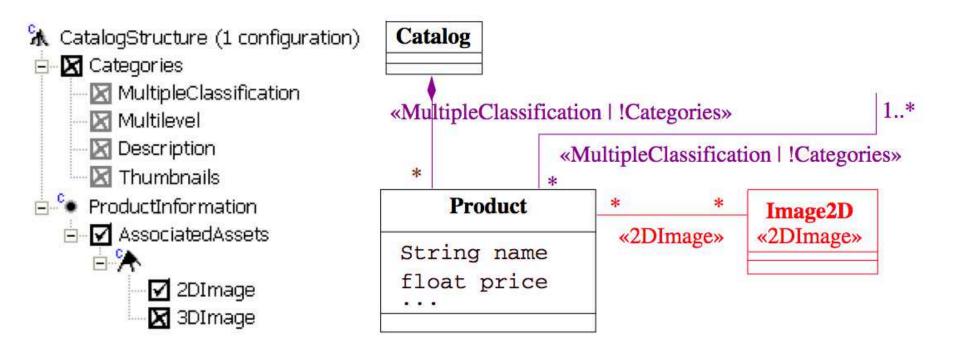
importing, exporting, composing, decomposing, editing, configuring, reverse engineering, computing "diffs", refactoring, testing, and reasoning about (multiple) variability models

Mathieu Acher, Philippe Collet, Philippe Lahire, Robert B. France « A Domain-Specific Language for Large-Scale Management of Feature Models » Science of Computer Programming (SCP), 2013

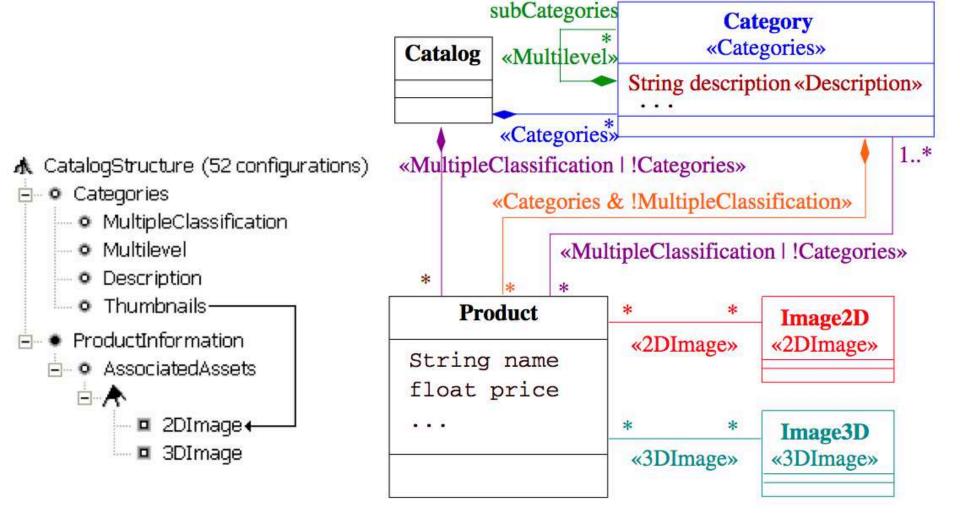




Ooops



Safe composition? No!



Product Derivation

feature model

Photo Print Scan Fax Connection

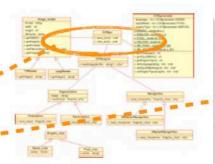
Connection
Laser Inkjet
USB Ethernet

{ MP, Photo, Print, Inkjet, Scan, Fax, Connection, USB, Ethernet }

configuration

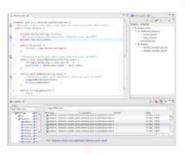
product spec

variable model and code assets





composition, weaving, transformation



product

Safe composition: how does it work?

