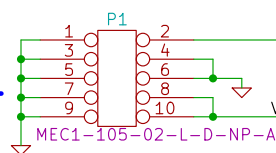


FOX-1 Maximum Power Point Tracker

ALL SOLAR PANELS INPUTS MED. CURRENT = 0.5A RATED

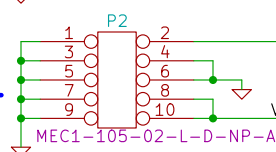
X+ PANEL

VIN MAX = 6.5V



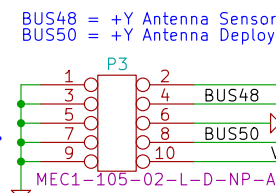
X- PANEL

VIN MAX = 6.5V



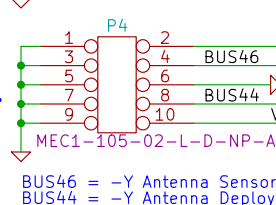
Y+ PANEL

VIN MAX = 6.5V



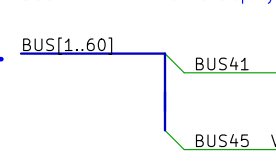
Y- PANEL

VIN MAX = 6.5V



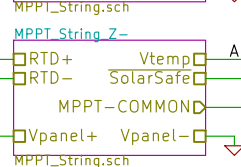
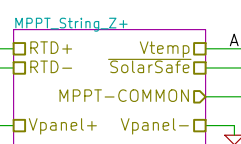
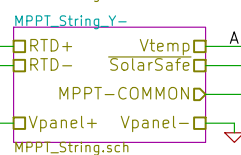
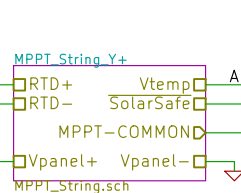
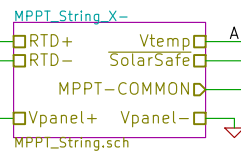
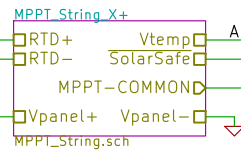
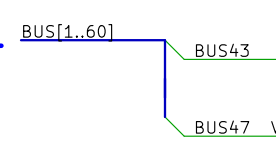
Z+ PANEL

VIN MAX = 6.5V



Z- PANEL

VIN MAX = 6.5V



BUS[1..60]

HIGH CURRENT = 1A RATED

VMPPT

BUS[1..60]

ADC_0 [0..7]

ADC_1 [0..7]

ADC_2 [0..7]

ADC_3 [0..7]

ADC_4 [0..7]

ADC_5 [0..7]

ADC_6 [0..7]

ADC_7 [0..7]

ADC_8 [0..7]

ADC_9 [0..7]

ADC_10 [0..7]

ADC_11 [0..7]

ADC_12 [0..7]

ADC_13 [0..7]

ADC_14 [0..7]

ADC_15 [0..7]

ADC_16 [0..7]

ADC_17 [0..7]

ADC_18 [0..7]

ADC_19 [0..7]

ADC_20 [0..7]

ADC_21 [0..7]

ADC_22 [0..7]

ADC_23 [0..7]

ADC_24 [0..7]

ADC_25 [0..7]

ADC_26 [0..7]

ADC_27 [0..7]

ADC_28 [0..7]

ADC_29 [0..7]

ADC_30 [0..7]

ADC_31 [0..7]

ADC_32 [0..7]

ADC_33 [0..7]

ADC_34 [0..7]

ADC_35 [0..7]

ADC_36 [0..7]

ADC_37 [0..7]

ADC_38 [0..7]

ADC_39 [0..7]

ADC_40 [0..7]

ADC_41 [0..7]

ADC_42 [0..7]

ADC_43 [0..7]

ADC_44 [0..7]

ADC_45 [0..7]

ADC_46 [0..7]

ADC_47 [0..7]

ADC_48 [0..7]

ADC_49 [0..7]

ADC_50 [0..7]

ADC_51 [0..7]

ADC_52 [0..7]

ADC_53 [0..7]

ADC_54 [0..7]

ADC_55 [0..7]

ADC_56 [0..7]

ADC_57 [0..7]

ADC_58 [0..7]

ADC_59 [0..7]

ADC_60 [0..7]

ADC_61 [0..7]

ADC_62 [0..7]

ADC_63 [0..7]

ADC_64 [0..7]

ADC_65 [0..7]

ADC_66 [0..7]

ADC_67 [0..7]

ADC_68 [0..7]

ADC_69 [0..7]

ADC_70 [0..7]

ADC_71 [0..7]

ADC_72 [0..7]

ADC_73 [0..7]

ADC_74 [0..7]

ADC_75 [0..7]

ADC_76 [0..7]

ADC_77 [0..7]

ADC_78 [0..7]

ADC_79 [0..7]

ADC_80 [0..7]

ADC_81 [0..7]

ADC_82 [0..7]

ADC_83 [0..7]

ADC_84 [0..7]

ADC_85 [0..7]

ADC_86 [0..7]

ADC_87 [0..7]

ADC_88 [0..7]

ADC_89 [0..7]

ADC_90 [0..7]

ADC_91 [0..7]

ADC_92 [0..7]

ADC_93 [0..7]

ADC_94 [0..7]

ADC_95 [0..7]

ADC_96 [0..7]

ADC_97 [0..7]

ADC_98 [0..7]

ADC_99 [0..7]

ADC_100 [0..7]

ADC_101 [0..7]

ADC_102 [0..7]

ADC_103 [0..7]

ADC_104 [0..7]

ADC_105 [0..7]

ADC_106 [0..7]

ADC_107 [0..7]

ADC_108 [0..7]

ADC_109 [0..7]

ADC_110 [0..7]

ADC_111 [0..7]

ADC_112 [0..7]

ADC_113 [0..7]

ADC_114 [0..7]

ADC_115 [0..7]

ADC_116 [0..7]

ADC_117 [0..7]

ADC_118 [0..7]

ADC_119 [0..7]

ADC_120 [0..7]

ADC_121 [0..7]

ADC_122 [0..7]

ADC_123 [0..7]

ADC_124 [0..7]

ADC_125 [0..7]

ADC_126 [0..7]

ADC_127 [0..7]

ADC_128 [0..7]

ADC_129 [0..7]

ADC_130 [0..7]

ADC_131 [0..7]

ADC_132 [0..7]

ADC_133 [0..7]

ADC_134 [0..7]

ADC_135 [0..7]

ADC_136 [0..7]

ADC_137 [0..7]

ADC_138 [0..7]

ADC_139 [0..7]

ADC_140 [0..7]

ADC_141 [0..7]

ADC_142 [0..7]

ADC_143 [0..7]

ADC_144 [0..7]

ADC_145 [0..7]

ADC_146 [0..7]

ADC_147 [0..7]

ADC_148 [0..7]

ADC_149 [0..7]

ADC_150 [0..7]

ADC_151 [0..7]

ADC_152 [0..7]

ADC_153 [0..7]

ADC_154 [0..7]

ADC_155 [0..7]

ADC_156 [0..7]

ADC_157 [0..7]

ADC_158 [0..7]

ADC_159 [0..7]

ADC_160 [0..7]

ADC_161 [0..7]

ADC_162 [0..7]

ADC_163 [0..7]

ADC_164 [0..7]

ADC_165 [0..7]

ADC_166 [0..7]

ADC_167 [0..7]

ADC_168 [0..7]

ADC_169 [0..7]

ADC_170 [0..7]

ADC_171 [0..7]

ADC_172 [0..7]

ADC_173 [0..7]

ADC_174 [0..7]

ADC_175 [0..7]

ADC_176 [0..7]

ADC_177 [0..7]

ADC_178 [0..7]

ADC_179 [0..7]

ADC_180 [0..7]

ADC_181 [0..7]

ADC_182 [0..7]

ADC_183 [0..7]

ADC_184 [0..7]

ADC_185 [0..7]

ADC_186 [0..7]

ADC_187 [0..7]

ADC_188 [0..7]

ADC_189 [0..7]

ADC_190 [0..7]

ADC_191 [0..7]

ADC_192 [0..7]

ADC_193 [0..7]

ADC_194 [0..7]

ADC_195 [0..7]

ADC_196 [0..7]

ADC_197 [0..7]

ADC_198 [0..7]

ADC_199 [0..7]

ADC_200 [0..7]

ADC_201 [0..7]

ADC_202 [0..7]

ADC_203 [0..7]

ADC_204 [0..7]

ADC_205 [0..7]

ADC_206 [0..7]

ADC_207 [0..7]

ADC_208 [0..7]

ADC_209 [0..7]

ADC_210 [0..7]

ADC_211 [0..7]

ADC_212 [0..7]

ADC_213 [0..7]

ADC_214 [0..7]

ADC_215 [0..7]

ADC_216 [0..7]

ADC_217 [0..7]

ADC_218 [0..7]

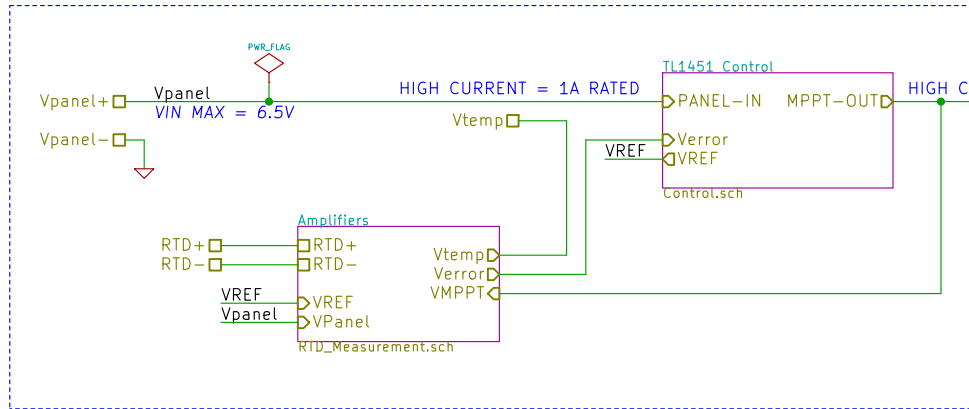
ADC_219 [0..7]

ADC_220 [0..7]

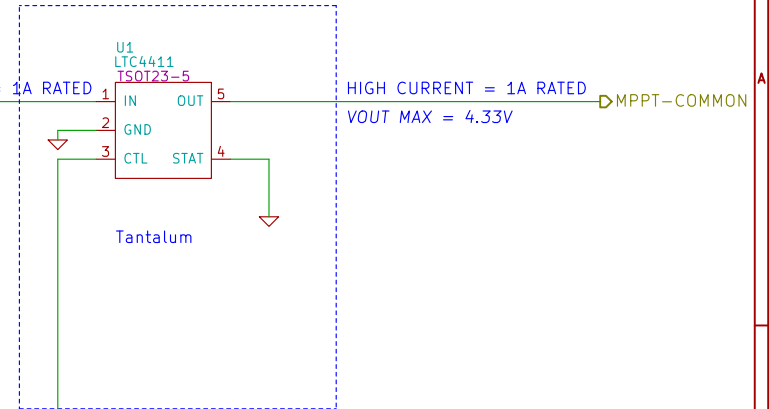
ADC_221 [0..7]

ADC_222 [0..7]

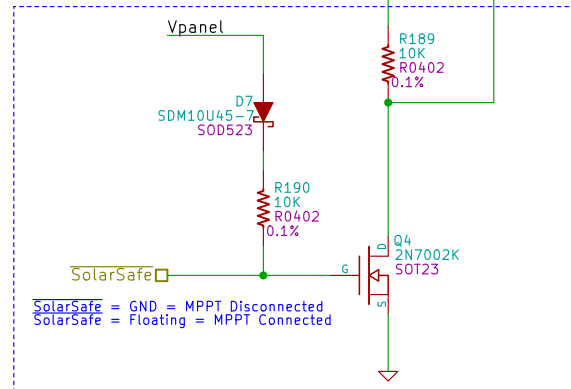
SWITCH MODE CONVERTER



Ideal Diode



Solar Safe Inverter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corrieo.
- * NASA derating taken into account, not gauranteed

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Bryce Salmi, KB1LQC

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File: MPPT_String.sch

Sheet: /MPPT_String_X+ /

Title: Fox-1 Maximum Power Point Tracker

Size: A

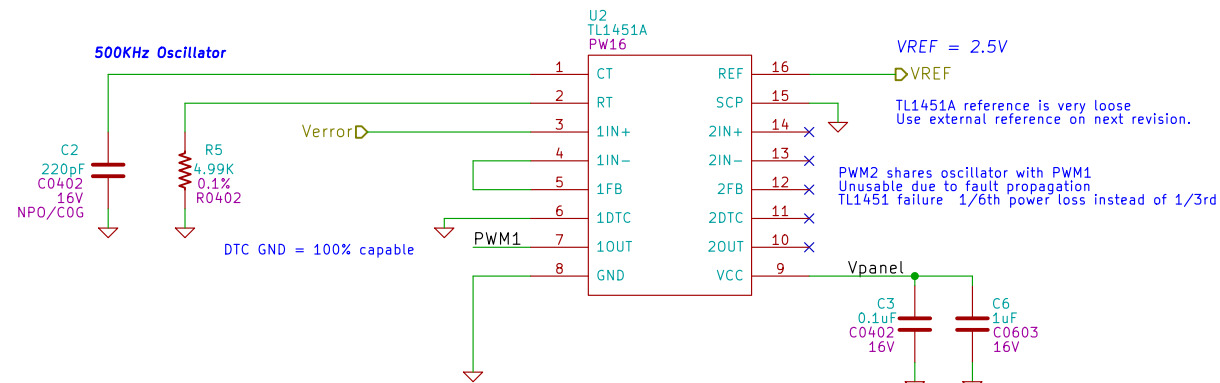
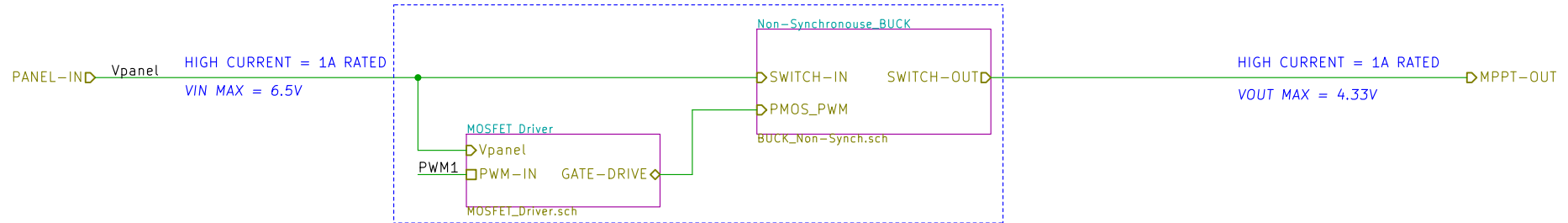
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 2/37

Switch Mode Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not gauranteed

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Bryce Salmi, KB1LQC
The Radio Amateur Satellite Corporation

File: Control.sch

Sheet: /MPPT_String_X+/TL1451 Control/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

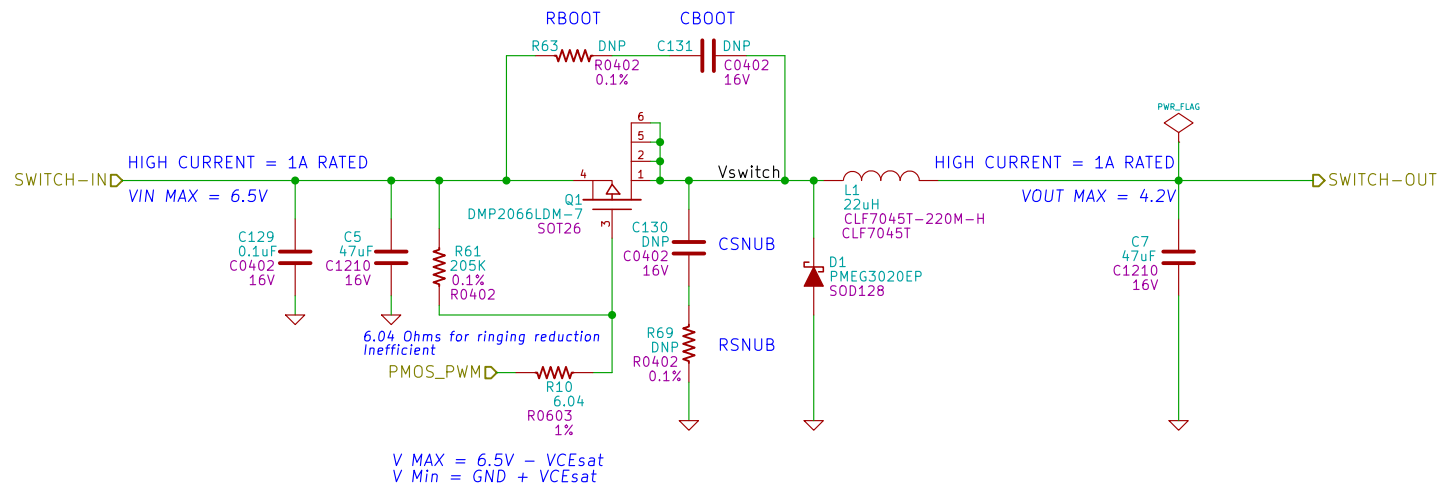
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 3/37

500 KHz Step-Down Buck Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

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File: BUCK_Non-Synch.sch

Sheet: /MPPT_String_X+/TL1451 Control/Non-Synchronous_BUCK/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

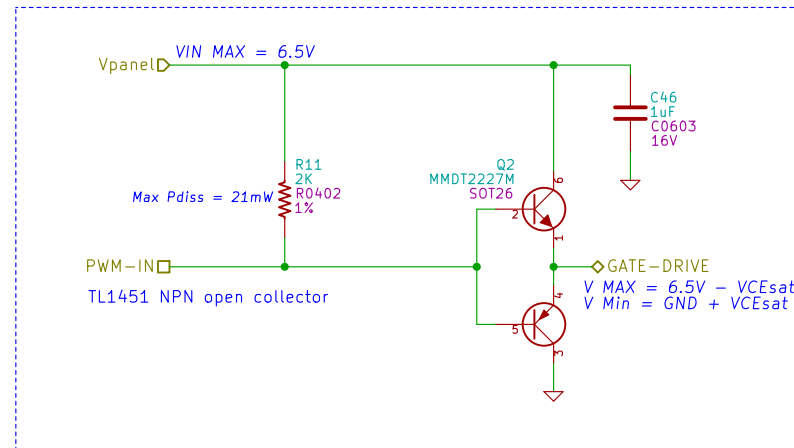
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 4/37

TOTEM POLE MOSFET DRIVER



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Bryce Salmi, KB1LQC

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File: MOSFET_Driver.sch

Sheet: /MPPT_String_X+/TL1451 Control/MOSFET Driver/

Title: Fox-1 Maximum Power Point Tracker

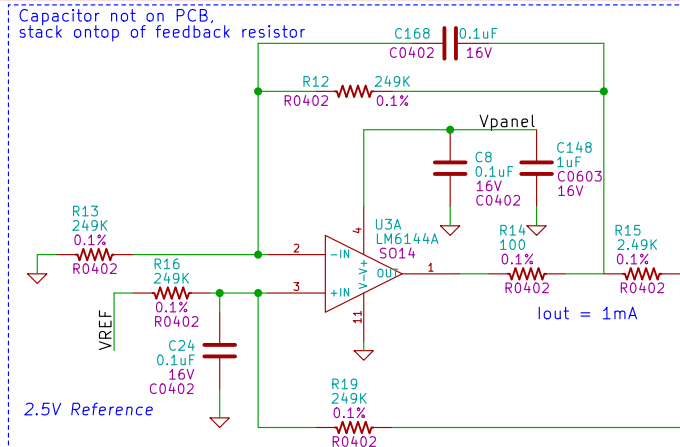
Size: A4

Date: 30 nov 2015

Rev: 2.0

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Id: 5/37



Constant 1mA Current Driver

Highest Voltage Wins = Lowest Duty Cycle Wins

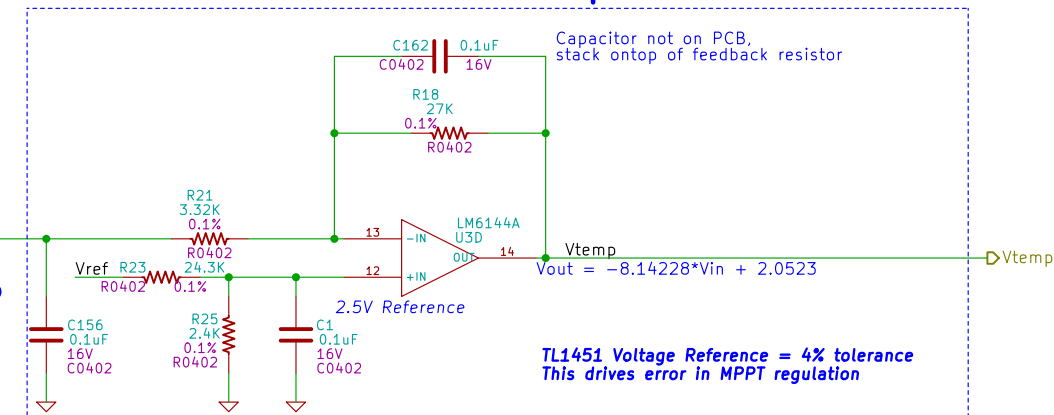
MPPT = V_{out} 3.3V to 4.33V

VregError → Increasing Duty Cycle = Decrease voltage

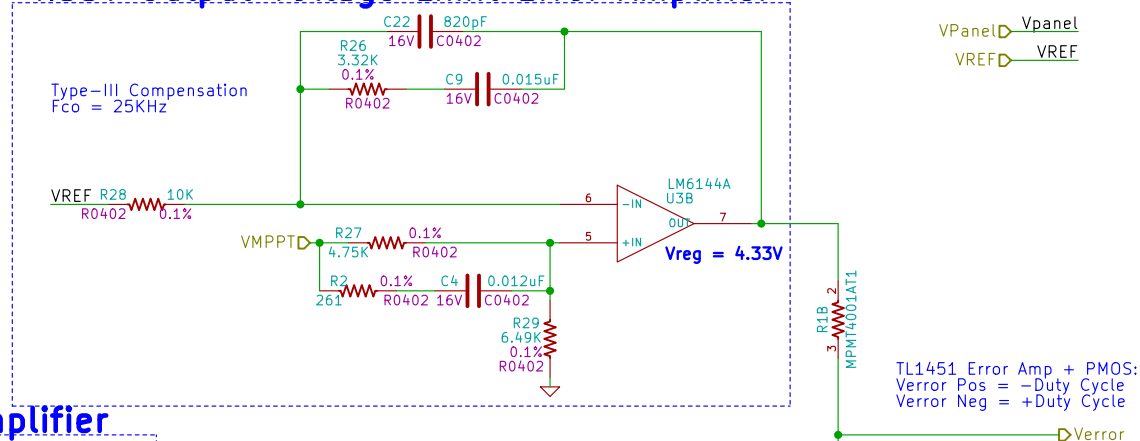
Regulation = V_{out} 4.33V, V_{panel} increasing

Vmppt Error → increases duty cycle (to load panel) so it looses

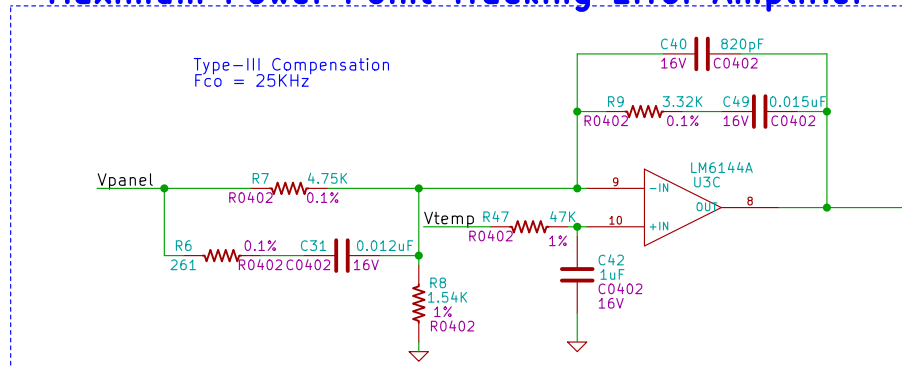
$Y = -mX + b$ Amplifier



4.33V Output Voltage Limit Error Amplifier



Maximum Power Point Tracking Error Amplifier



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Bryce Salmi, KB1LQC

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File: RTD_Measurement.sch

Sheet: /MPPT_String_X+/Amplifiers/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

Date: 30 nov 2015

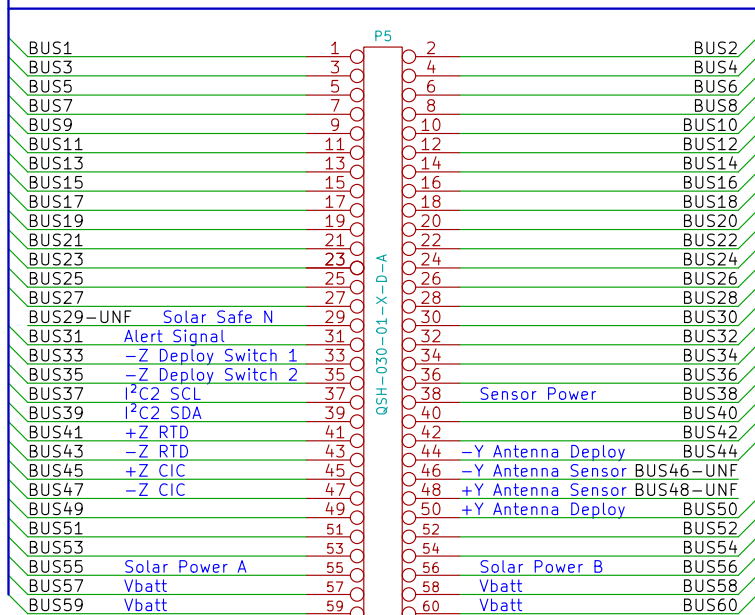
Rev: 2.0

KiCad E.D.A.

Id: 6/37

+Y Antenna Deploy & -Y Antenna Deploy
Deploy resistors = 6.98 Ohms
Imax = 4.2V/6.98V = 602mA
Pmax = 2.53W

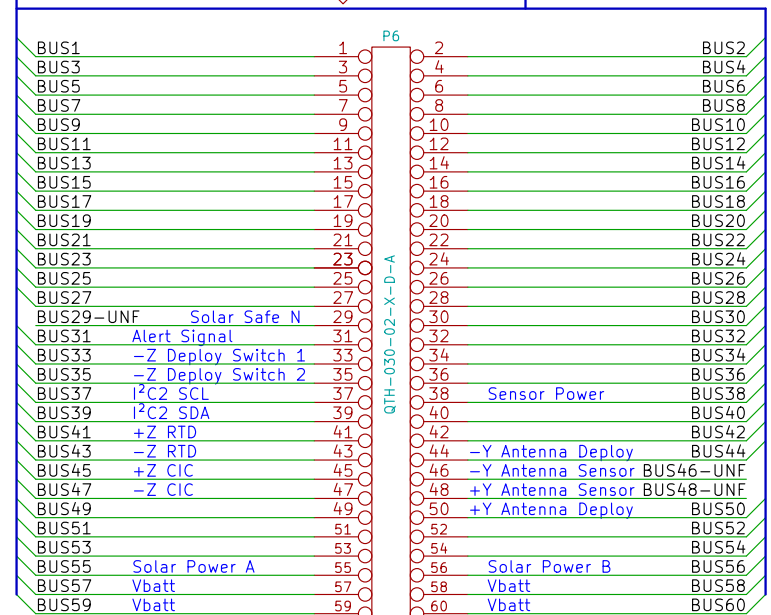
BUS[1..60]



BUS38 Sensor Power is driven by the IHU PCB

BUS38

BUS[1..60]



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not gauranteed

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Bryce Salmi, KB1LQC

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File: Connectors.sch

Sheet: /Connectors/

Title: Fox-1 Maximum Power Point Tracker

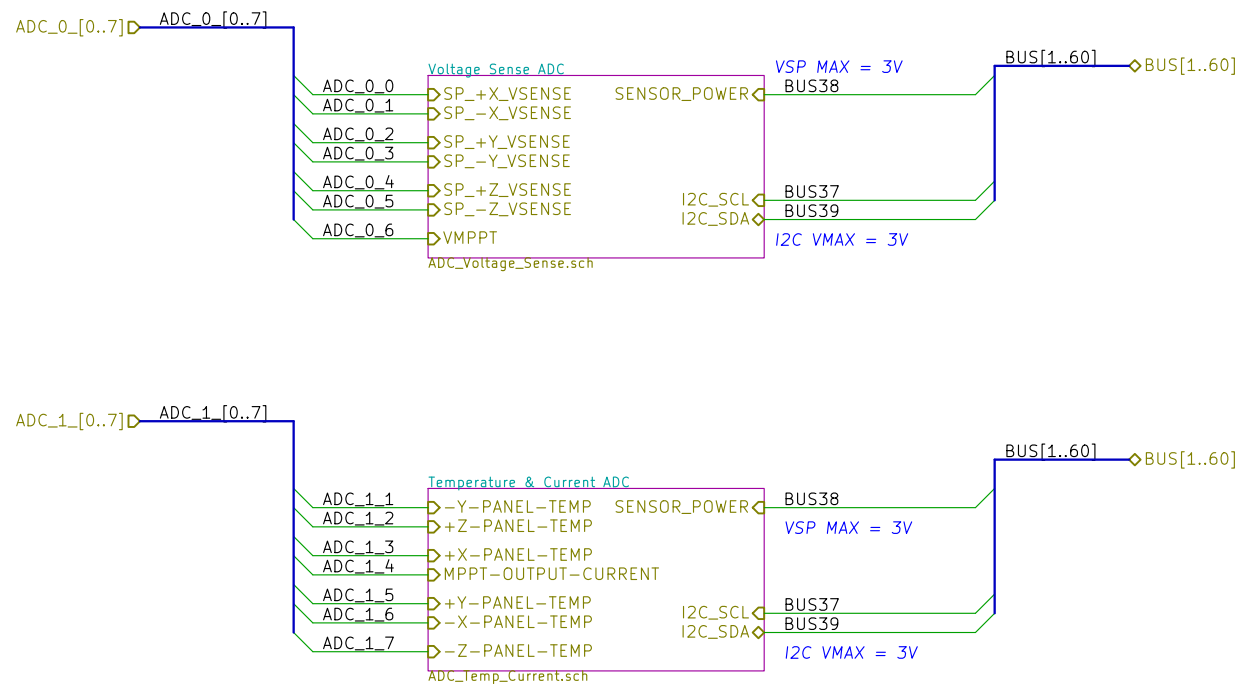
Size: A4

Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 7/37



NOTES

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- * NASA derating taken into account, not gauranteed

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File: ADC_Channels.sch

Sheet: /ADC Channels/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

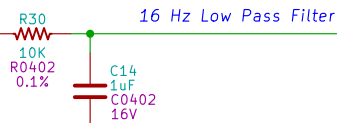
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

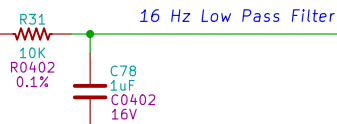
Id: 8/37

VIN MAX = 2.42V @ 6.54V
SP_+X_VSENSE

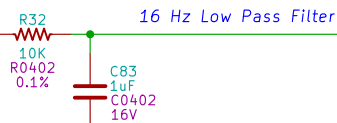


FAULT CURRENT = 6.5V LIMITED TO 320uA/CHANNEL WHEN ON

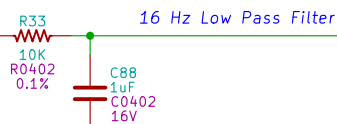
VIN MAX = 2.42V @ 6.54V
SP_-X_VSENSE



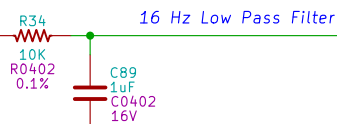
VIN MAX = 2.42V @ 6.54V
SP_+Y_VSENSE



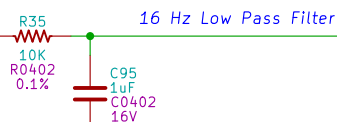
VIN MAX = 2.42V @ 6.54V
SP_-Y_VSENSE



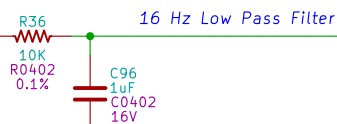
VIN MAX = 2.42V @ 6.54V
SP_+Z_VSENSE



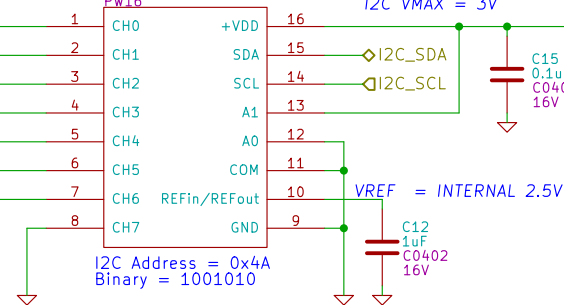
VIN MAX = 2.42V @ 6.54V
SP_-Z_VSENSE



VIN MAX = 2.402V @ 4.2V
VMPPT



U5
ADS7828
PW16



SEL PROTECTED TO 50mA
FPF2001 ON IHU CARD
SENSOR_POWER
VIN MAX = 3.0V

NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Bryce Salmi, KB1LQC
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File: ADC_Voltage_Sense.sch

Sheet: /ADC Channels/Voltage Sense ADC/

Title: Fox-1 Maximum Power Point Tracker

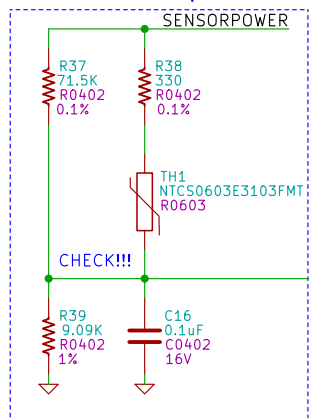
Size: A4 Date: 30 nov 2015

KiCad E.D.A.

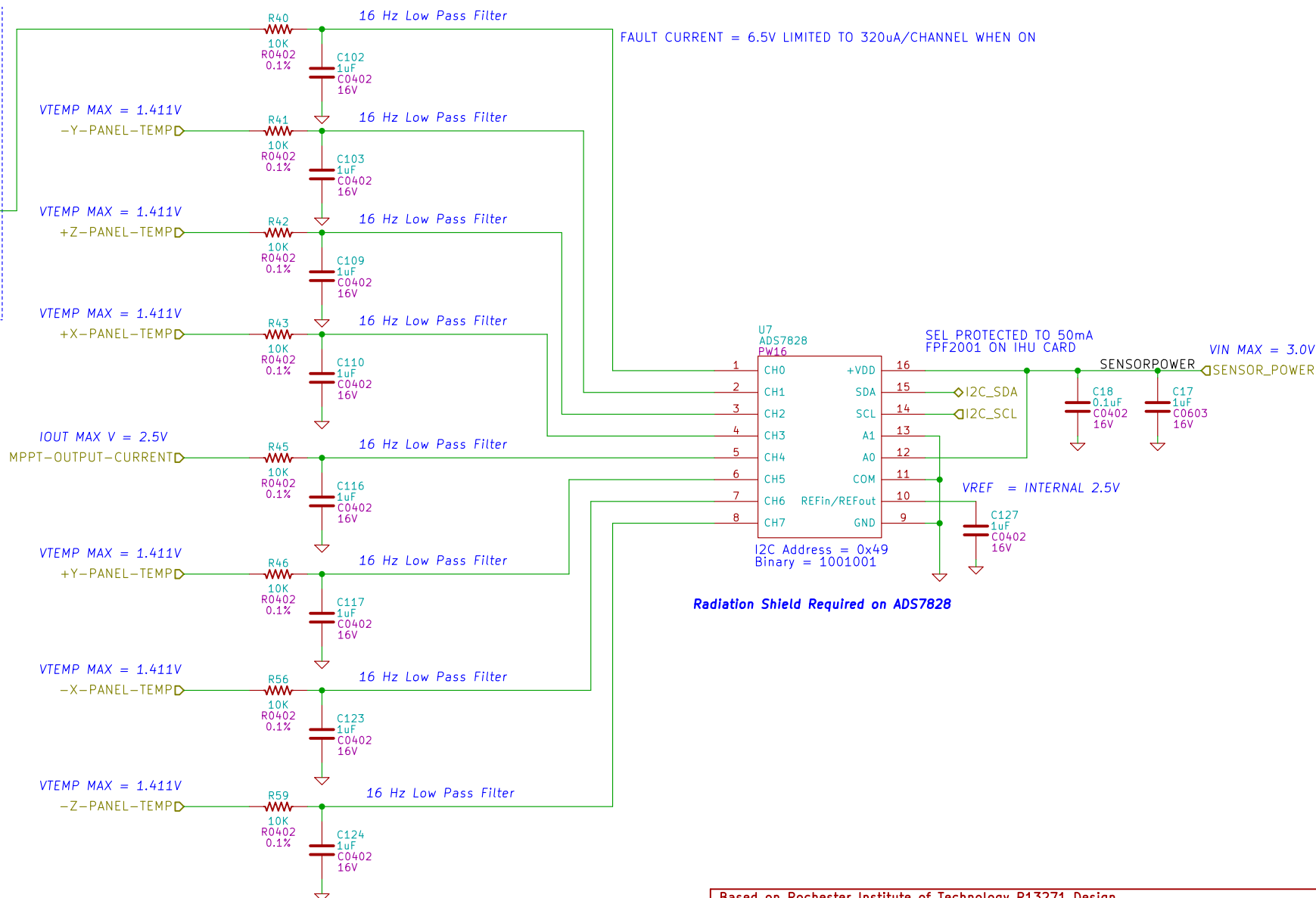
Rev: 2.0

Id: 9/37

MPPT PCB Temperature



Mount near center of PCB



NOTES

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- * NASA derating taken into account, not gauranteed

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File: ADC_Temp_Current.sch

Sheet: /ADC Channels/Temperature & Current ADC/

Title: Fox-1 Maximum Power Point Tracker

Size: A4 Date: 30 nov 2015

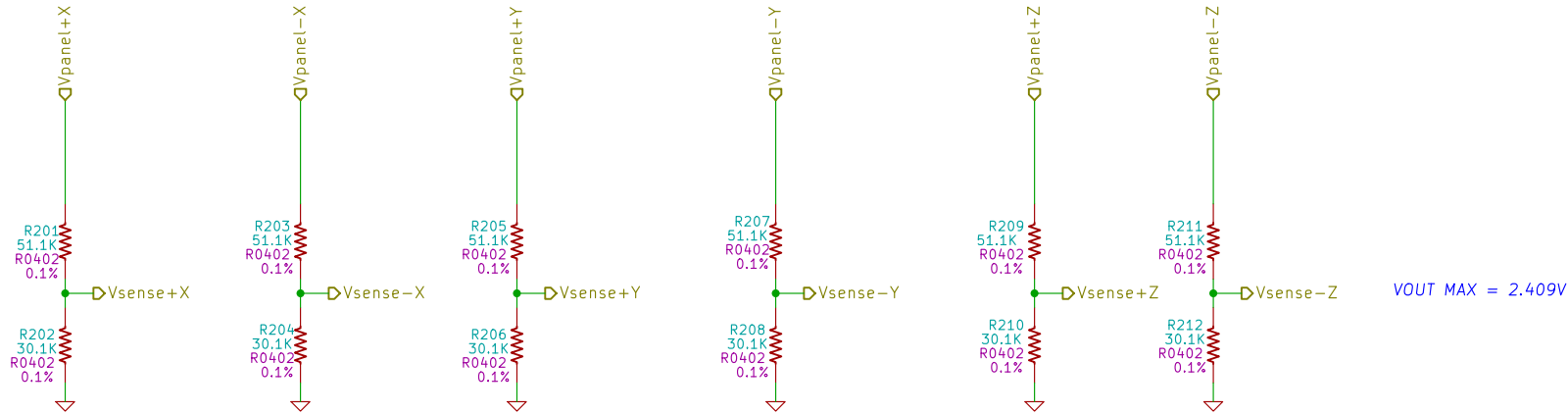
KiCad E.D.A.

Rev: 2.0

Id: 10/37

PANEL VOLTAGE TELEMETRY ADC SCALING

VIN MAX = 6.5V

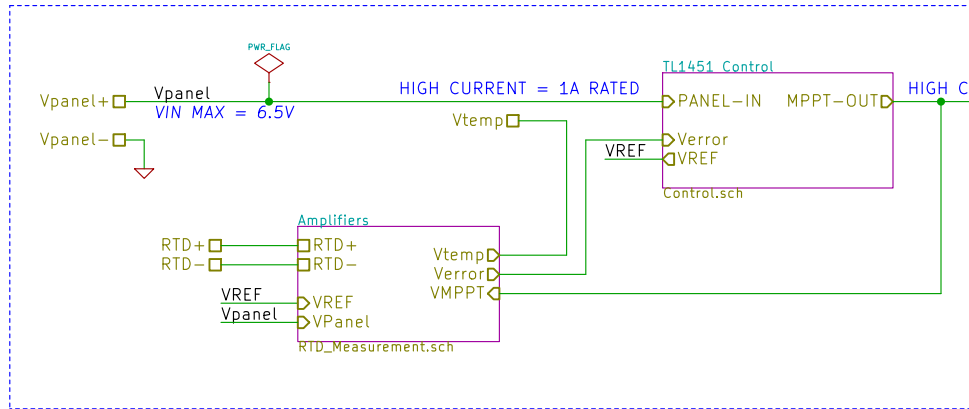


NOTES

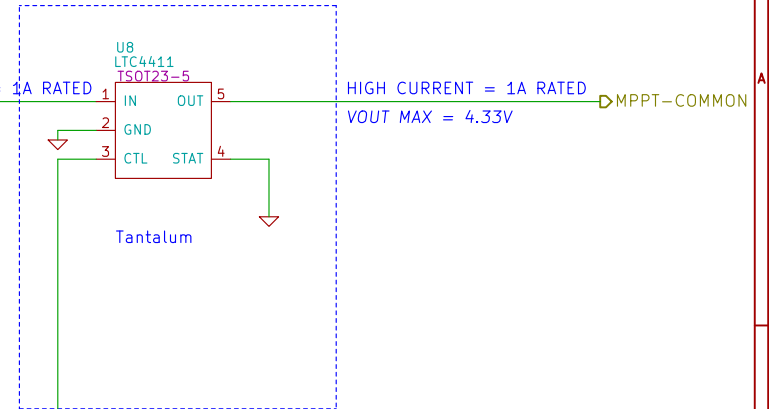
- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not gauranteed

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Brent Salmi, KB1LQD		
Bryce Salmi, KB1LQC		
The Radio Amateur Satellite Corporation		
File: VpanelScaling.sch		
Sheet: /Panel Voltage Scaling/		
Title:		
Size: A4	Date: 30 nov 2015	Rev: 2.0
KiCad E.D.A.		Id: 11/37

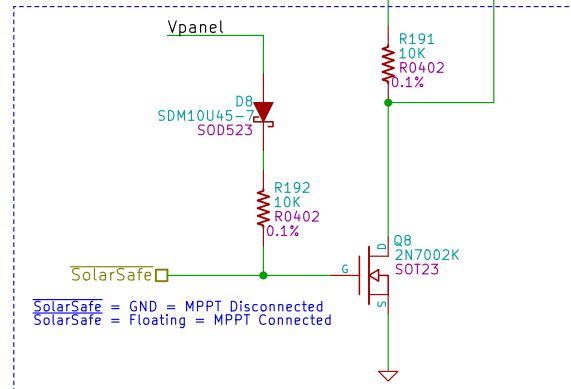
SWITCH MODE CONVERTER



Ideal Diode



Solar Safe Inverter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corrieo.
- * NASA derating taken into account, not gauranteed

Based on Rochester Institute of Technology P13271 Design

Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: MPPT_String.sch

Sheet: /MPPT_String_X-/

Title: Fox-1 Maximum Power Point Tracker

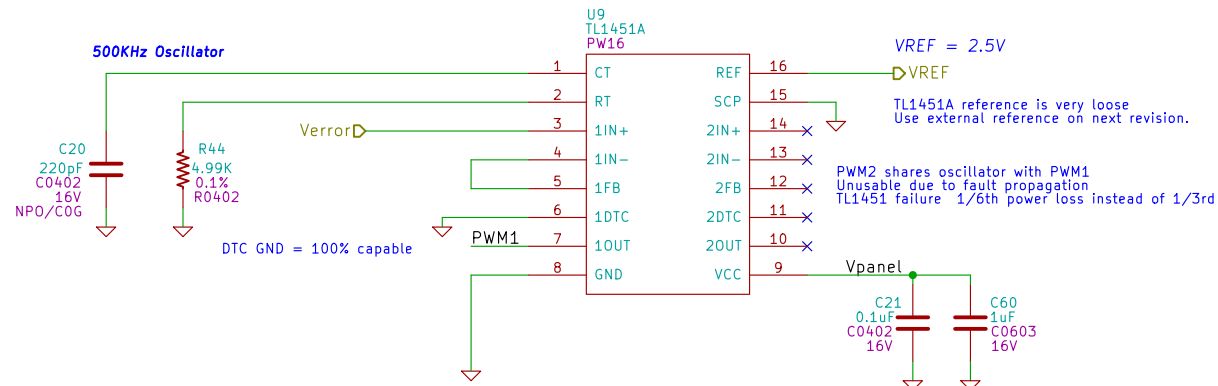
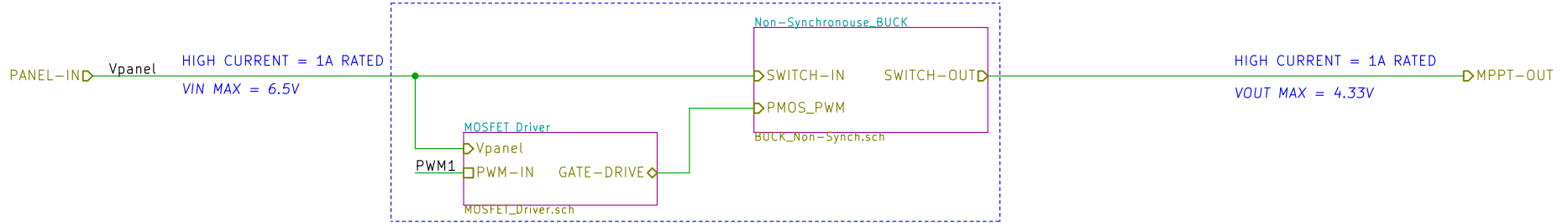
Size: A Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 12/37

Switch Mode Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

Based on Rochester Institute of Technology P13271 Design
Brent Salmi, KB1LQD
Bryce Salmi, KB1LQC
The Radio Amateur Satellite Corporation

File: Control.sch

Sheet: /MPPT_String_X-/TL1451 Control/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

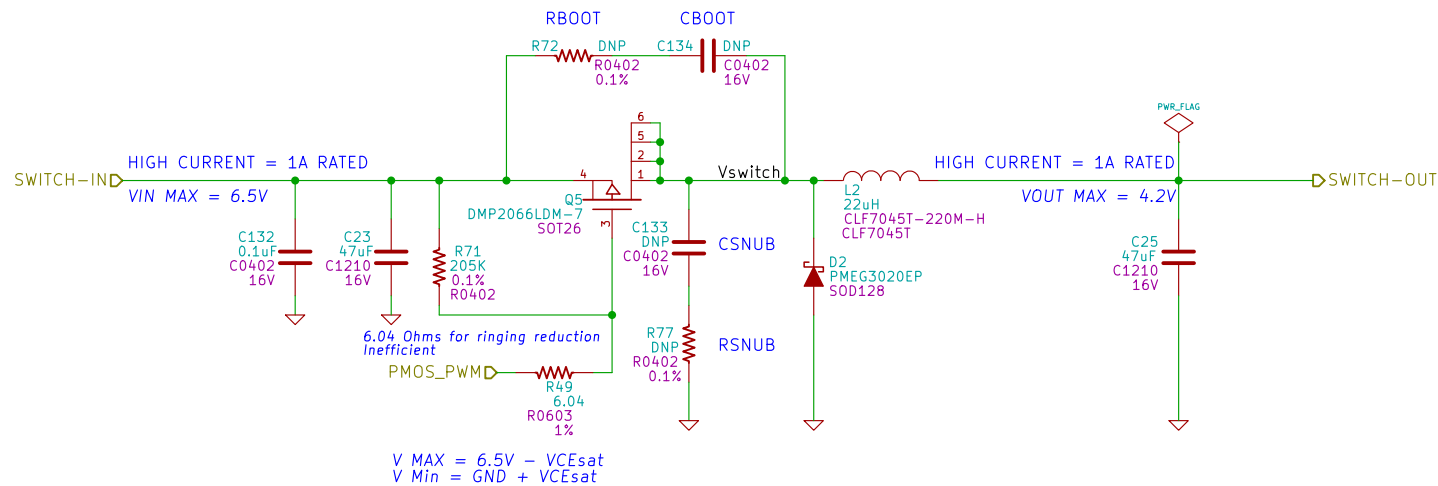
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 13/37

500 KHz Step-Down Buck Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

Based on Rochester Institute of Technology P13271 Design

Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: BUCK_Non-Synch.sch

Sheet: /MPPT_String_X-/TL1451 Control/Non-Synchronous_BUCK/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

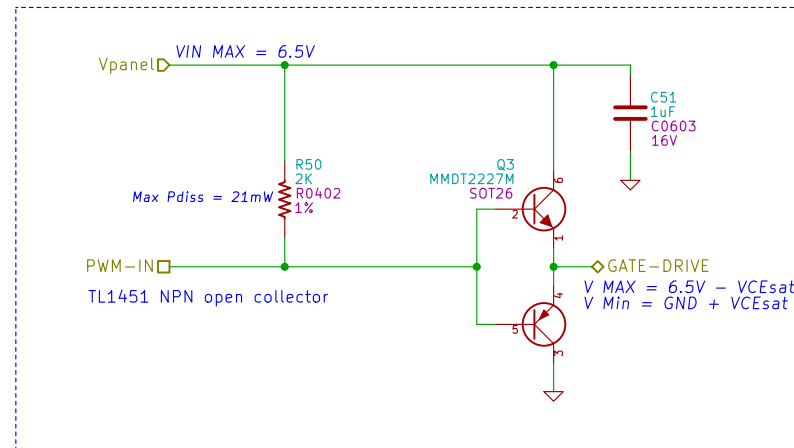
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 14/37

TOTEM POLE MOSFET DRIVER



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: MOSFET_Driver.sch

Sheet: /MPPT_String_X-/TL1451 Control/MOSFET Driver/

Title: Fox-1 Maximum Power Point Tracker

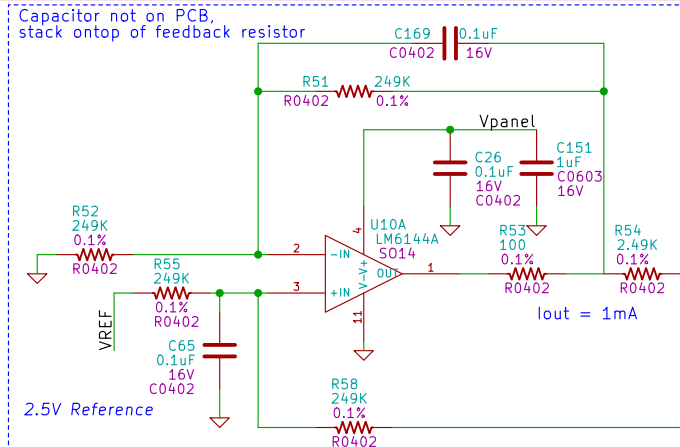
Size: A4

Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 15/37



Constant 1mA Current Driver

Highest Voltage Wins = Lowest Duty Cycle Wins

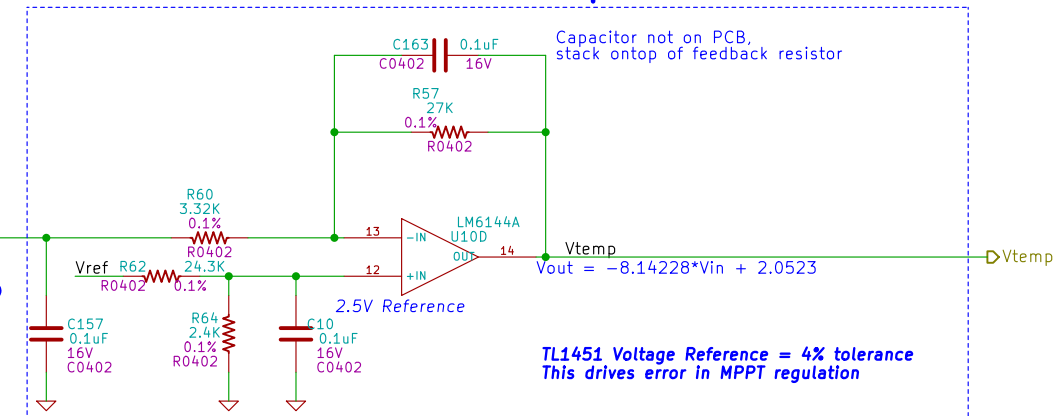
MPPT = V_{out} 3.3V to 4.33V

VregError → Increasing Duty Cycle = Decrease voltage

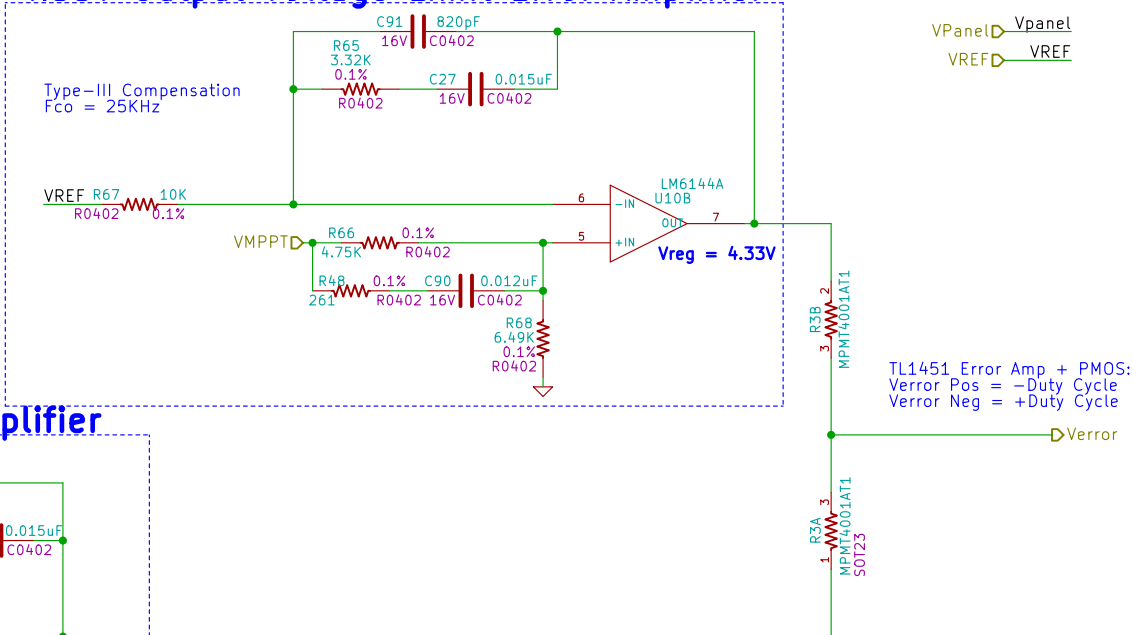
Regulation = V_{out} 4.33V, Vpanel increasing

Vmppt Error → increases duty cycle (to load panel) so it looses

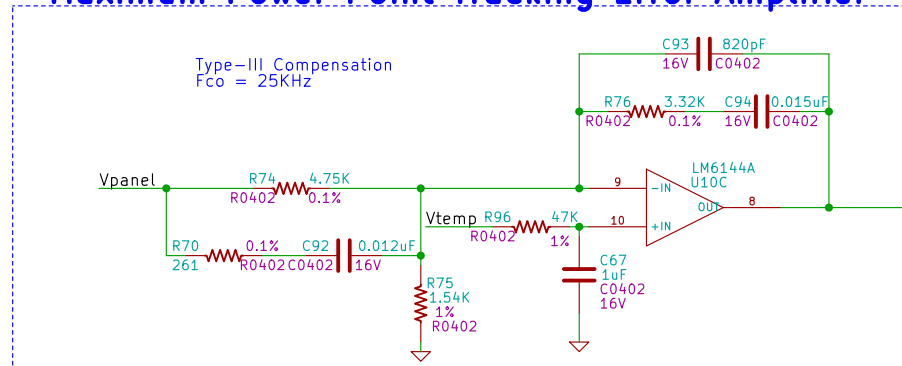
$Y = -mX + b$ Amplifier



4.33V Output Voltage Limit Error Amplifier



Maximum Power Point Tracking Error Amplifier



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

Based on Rochester Institute of Technology P13271 Design

Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: RTD_Measurement.sch

Sheet: /MPPT_String_X-/Amplifiers/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

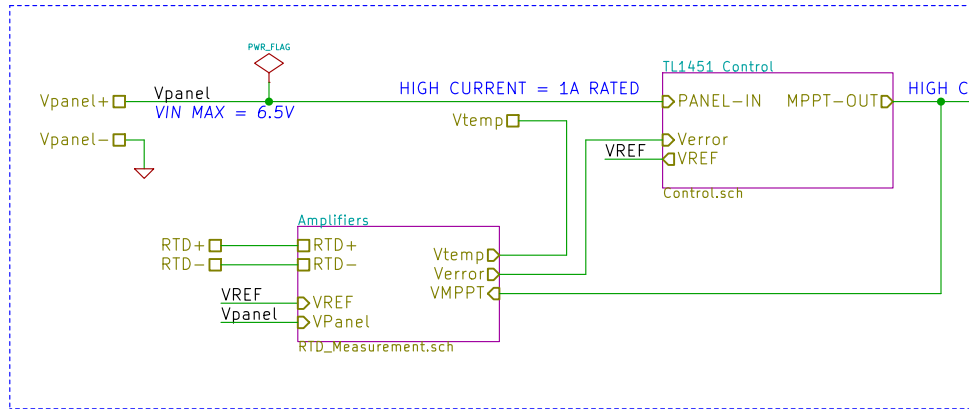
Date: 30 nov 2015

Rev: 2.0

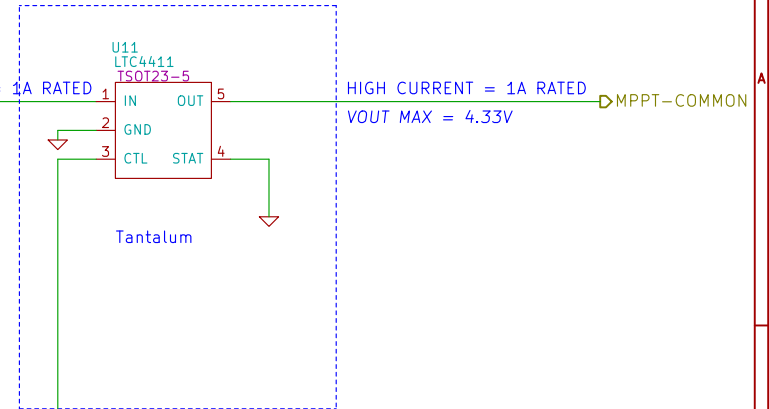
KiCad E.D.A.

Id: 16/37

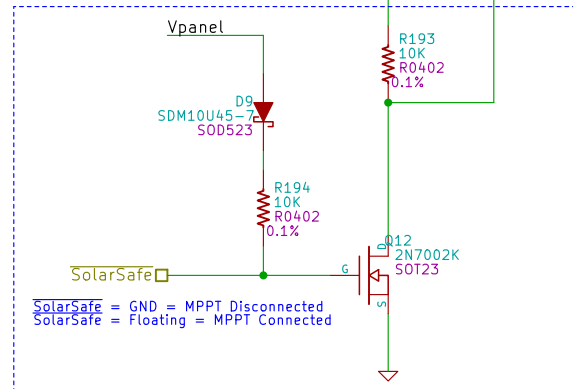
SWITCH MODE CONVERTER



Ideal Diode



Solar Safe Inverter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corrieo.
- * NASA derating taken into account, not gauranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: MPPT_String.sch

Sheet: /MPPT_String_Y+/
 Title: Fox-1 Maximum Power Point Tracker

Size: A

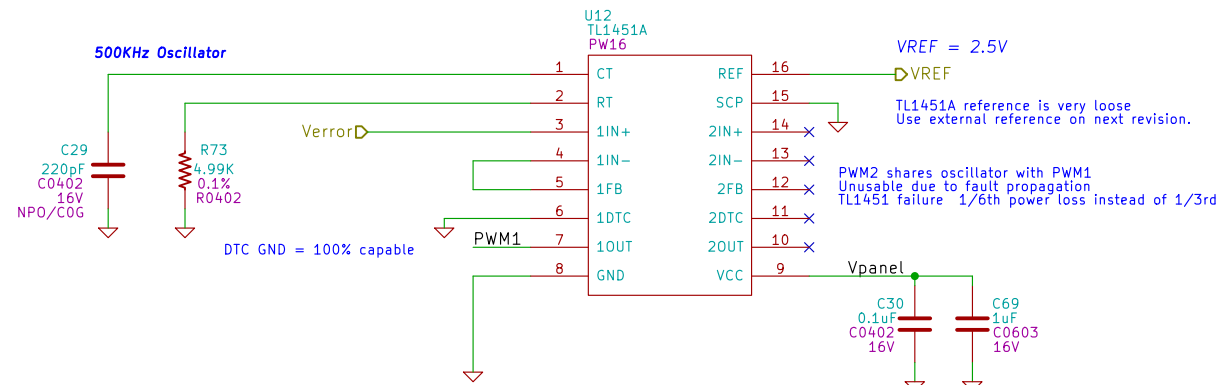
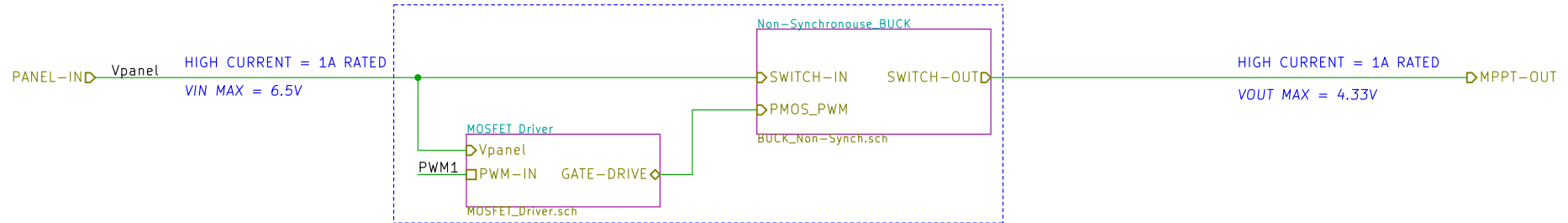
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 17/37

Switch Mode Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not gauranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: Control.sch

Sheet: /MPPT_String_Y+/TL1451 Control/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

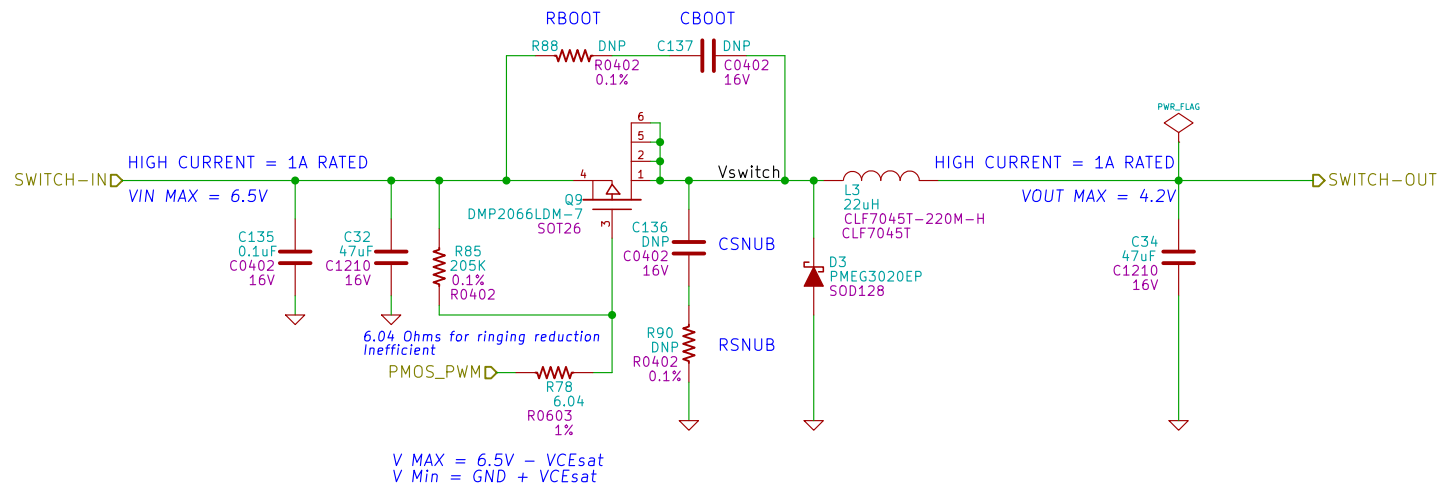
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 18/37

500 KHz Step-Down Buck Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

Based on Rochester Institute of Technology P13271 Design

Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: BUCK_Non-Synch.sch

Sheet: /MPPT_String_Y+/TL1451 Control/Non-Synchronous_BUCK/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

Date: 30 nov 2015

Rev: 2.0

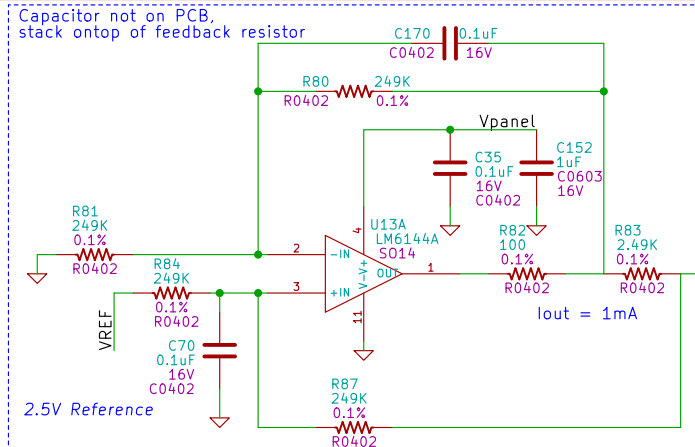
KiCad E.D.A.

Id: 19/37

$V_{in\ MAX} = 6.5V$
 $R_{79} = 2K$
 R_{0402}
 1%
 $Max\ P_{diss} = 21mW$
 $TL1451\ NPN\ open\ collector$
 $C_{55} = 1\mu F$
 C_{0603}
 $16V$
 $Q_6 = MMDT2227M$
 $SOT26$
 $GATE-DRIVE$
 $V_{MAX} = 6.5V - V_{CEsat}$
 $V_{Min} = GND + V_{CEsat}$

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

Id: 20/37



Constant 1mA Current Driver

Highest Voltage Wins = Lowest Duty Cycle Wins

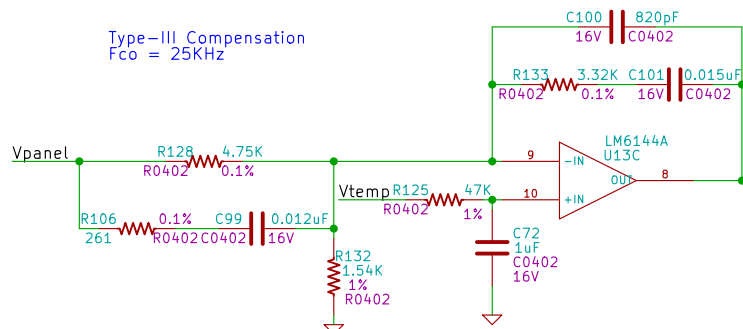
MPPT = V_{out} 3.3V to 4.33V

VregError → Increasing Duty Cycle = Decrease voltage

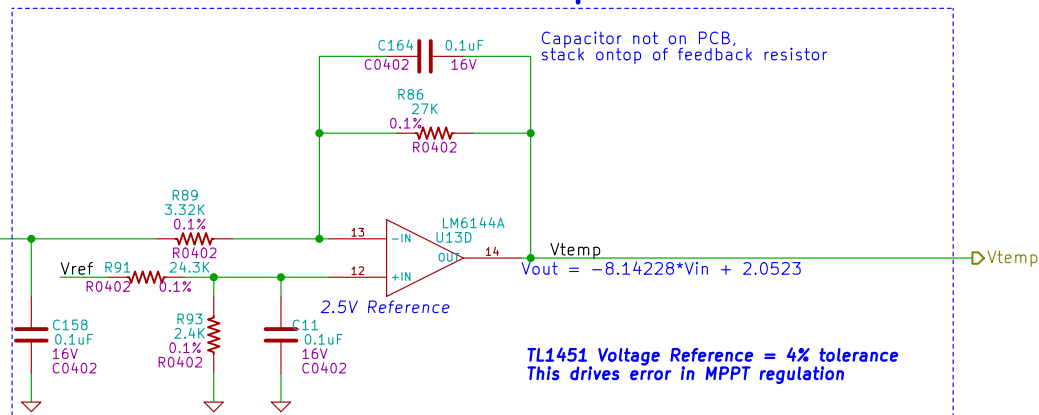
Regulation = V_{out} 4.33V, V_{panel} increasing

Vmppt Error → increases duty cycle (to load panel) so it looses

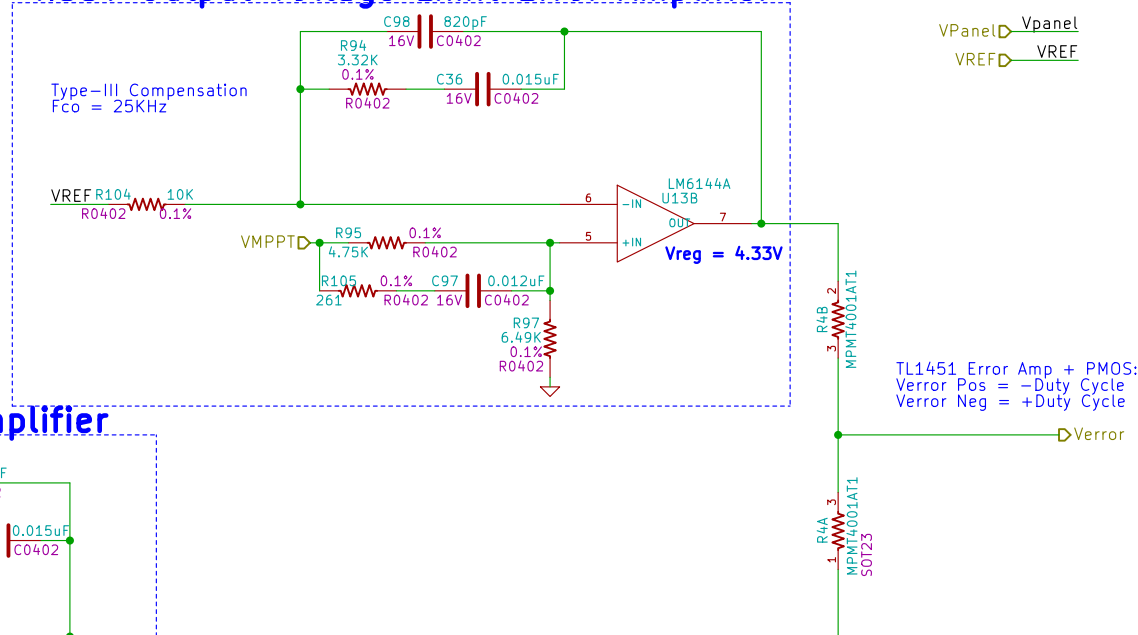
Maximum Power Point Tracking Error Amplifier



$Y = -mX + b$ Amplifier



4.33V Output Voltage Limit Error Amplifier



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: RTD_Measurement.sch

Sheet: /MPPT_String_Y+/Amplifiers/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

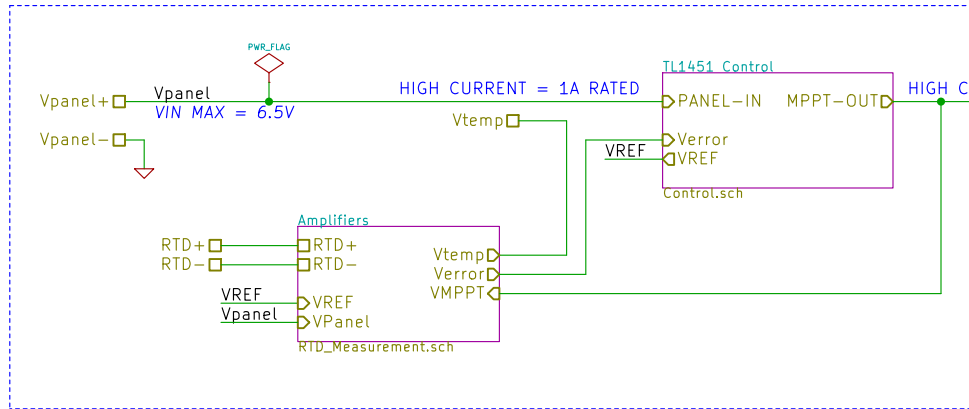
Date: 30 nov 2015

Rev: 2.0

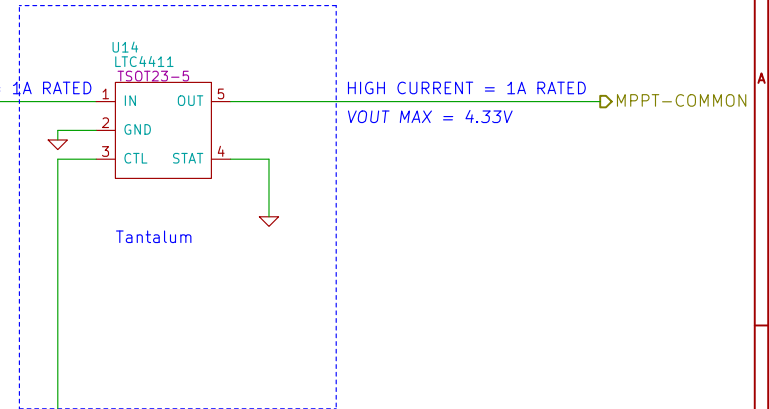
KiCad E.D.A.

Id: 21/37

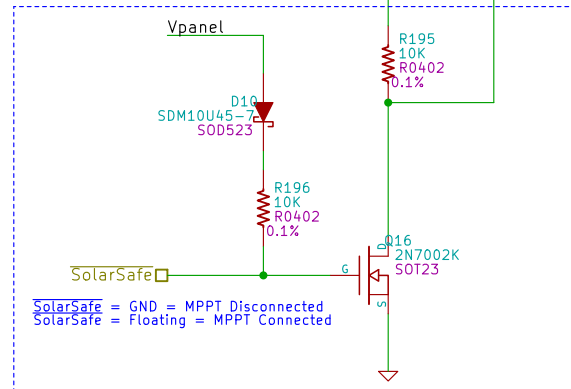
SWITCH MODE CONVERTER



Ideal Diode



Solar Safe Inverter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corrie.
- * NASA derating taken into account, not gauranteed

Based on Rochester Institute of Technology P13271 Design

Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: MPPT_String.sch

Sheet: /MPPT_String_Y-/

Title: Fox-1 Maximum Power Point Tracker

Size: A

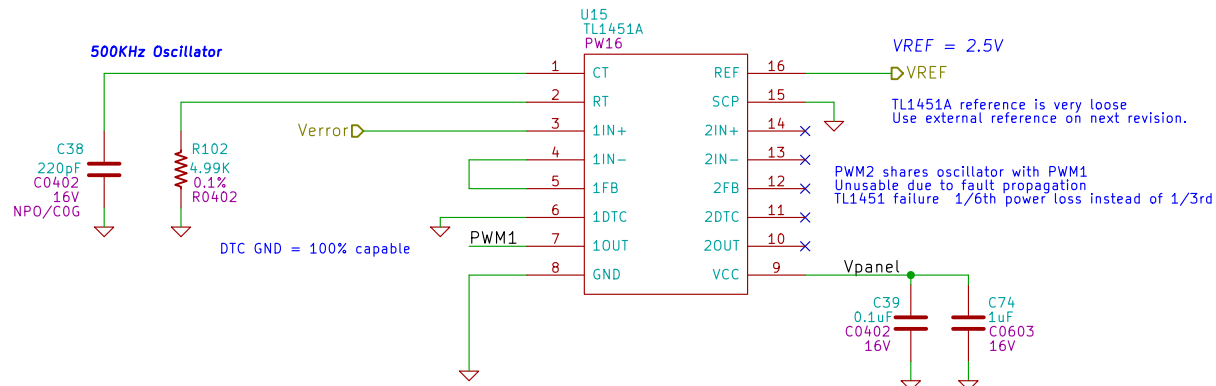
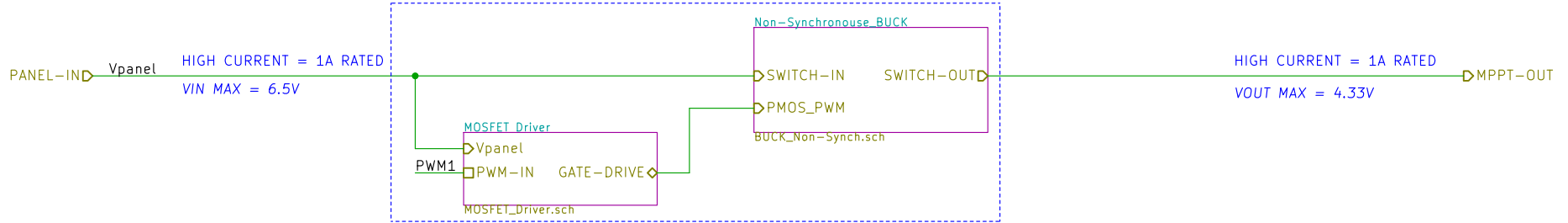
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 22/37

Switch Mode Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Brent Salmi, KB1LQD
Bryce Salmi, KB1LQC
The Radio Amateur Satellite Corporation

File: Control.sch

Sheet: /MPPT_String_Y-/TL1451 Control/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

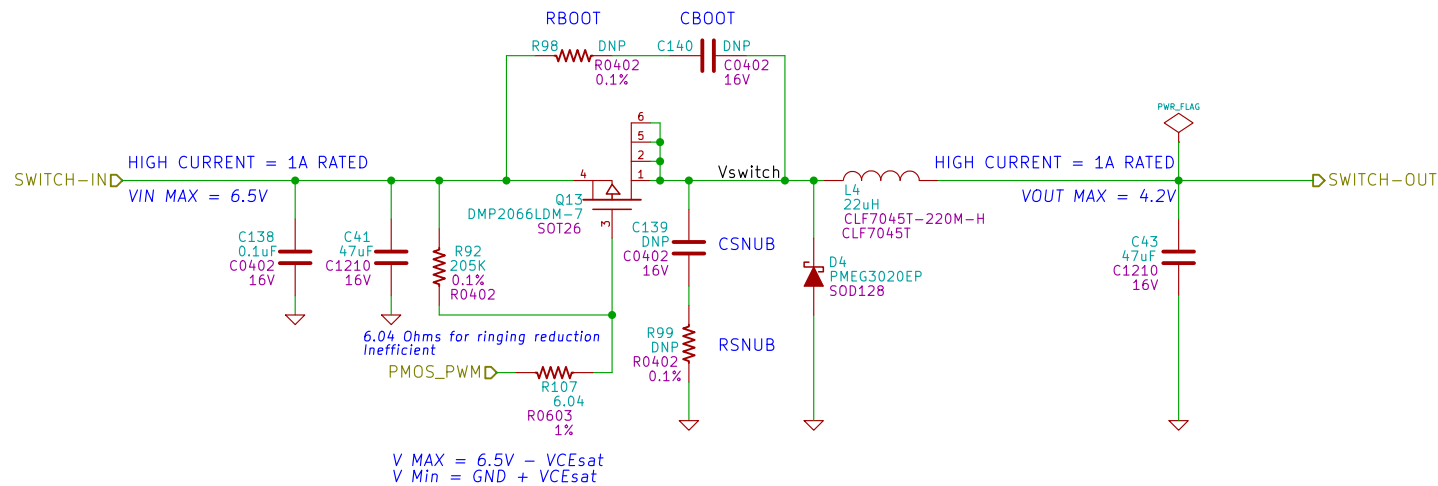
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 23/37

500 KHz Step-Down Buck Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: BUCK_Non-Synch.sch

Sheet: /MPPT_String_Y-/TL1451 Control/Non-Synchronous_BUCK/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

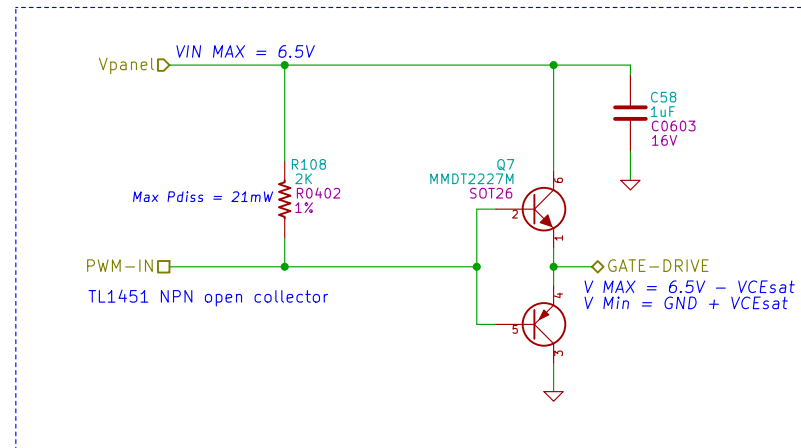
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 24/37

TOTEM POLE MOSFET DRIVER



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

Based on Rochester Institute of Technology P13271 Design

Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: MOSFET_Driver.sch

Sheet: /MPPT_String_Y-/TL1451 Control/MOSFET Driver/

Title: Fox-1 Maximum Power Point Tracker

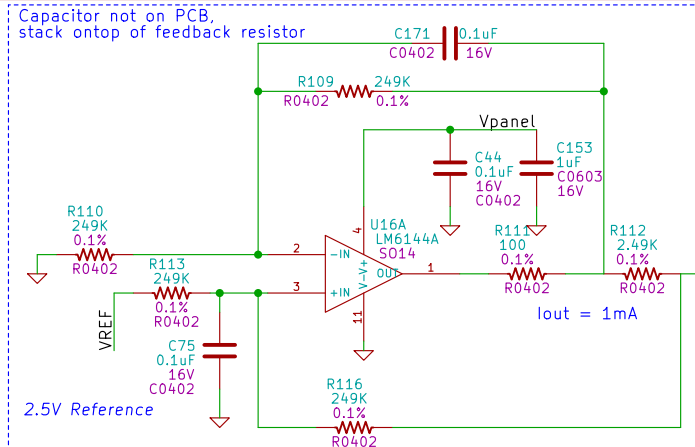
Size: A4

Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 25/37



Constant 1mA Current Driver

Highest Voltage Wins = Lowest Duty Cycle Wins

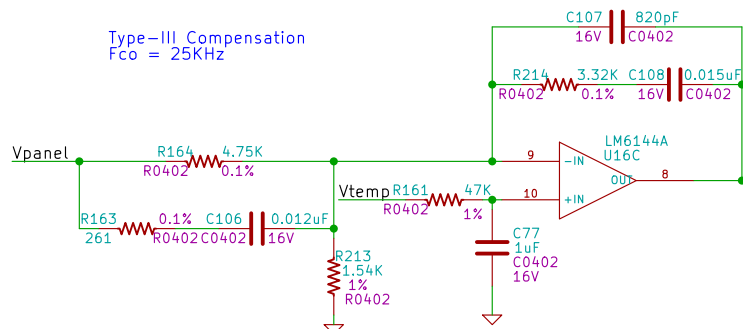
MPPT = Vout 3.3V to 4.33V

VregError -> Increasing Duty Cycle = Decrease voltage

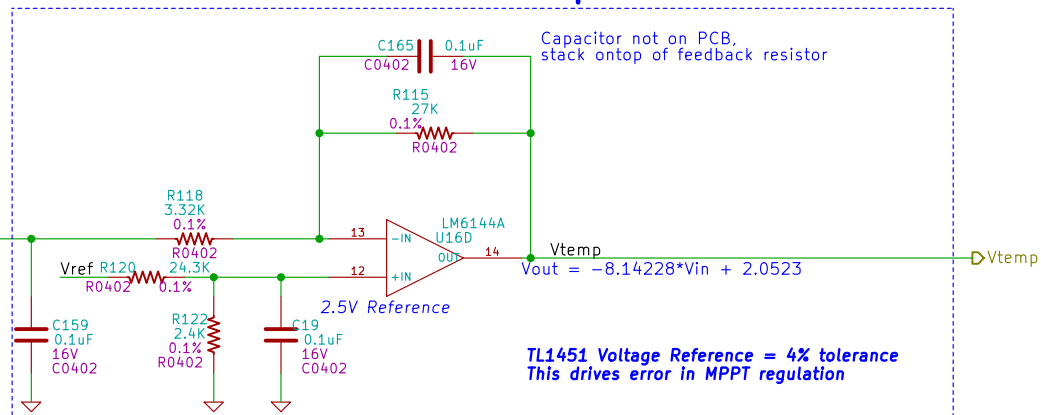
Regulation = Vout 4.33V, Vpanel increasing

Vmppt Error -> increases duty cycle (to load panel) so it looses

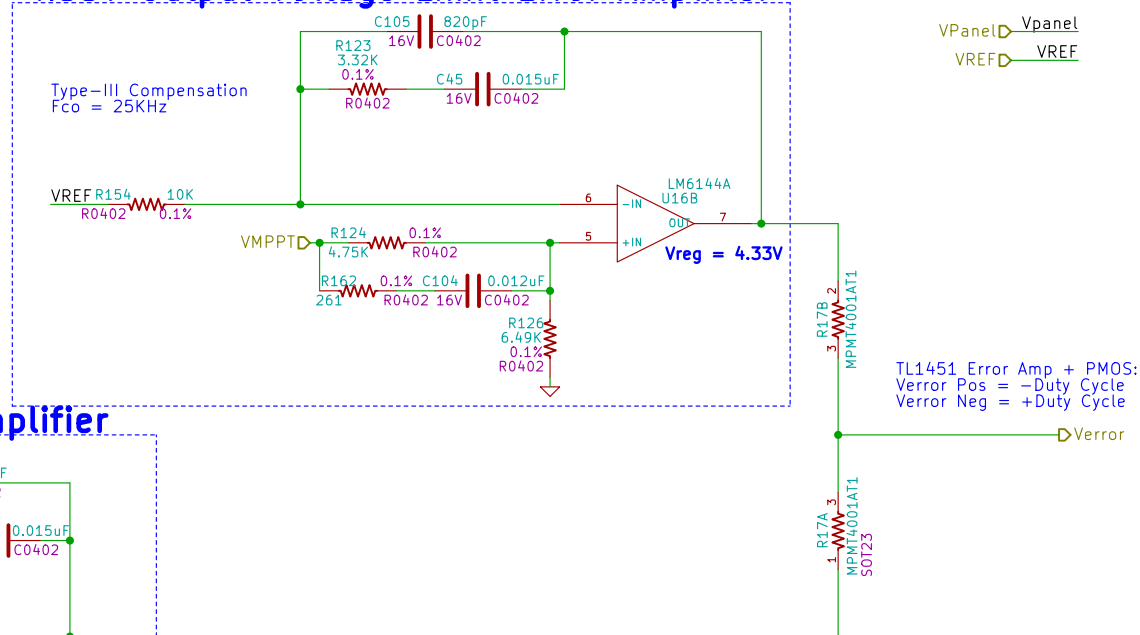
Maximum Power Point Tracking Error Amplifier



Y = -mX + b Amplifier



4.33V Output Voltage Limit Error Amplifier



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: RTD_Measurement.sch

Sheet: /MPPT_String_Y-/Amplifiers/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

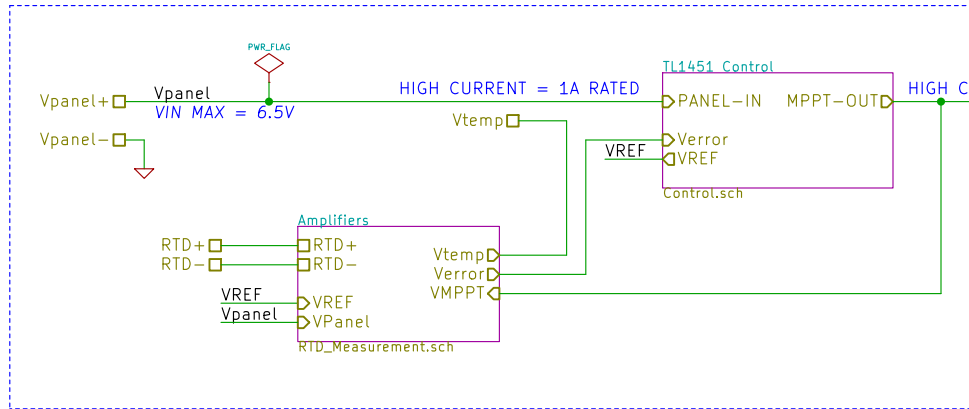
Date: 30 nov 2015

Rev: 2.0

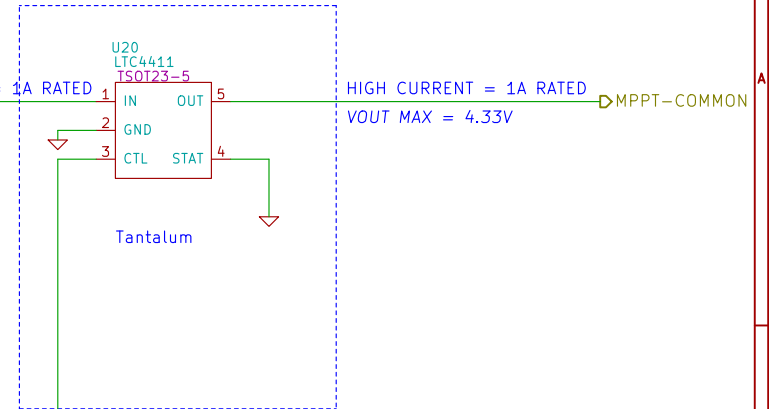
KiCad E.D.A.

Id: 26/37

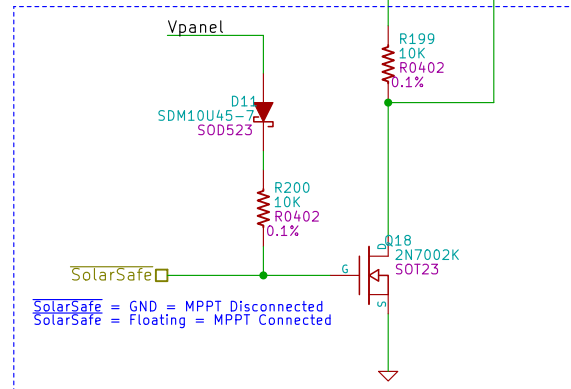
SWITCH MODE CONVERTER



Ideal Diode



Solar Safe Inverter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corrieo.
- * NASA derating taken into account, not gauranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: MPPT_String.sch

Sheet: /MPPT_String_Z-/

Title: Fox-1 Maximum Power Point Tracker

Size: A

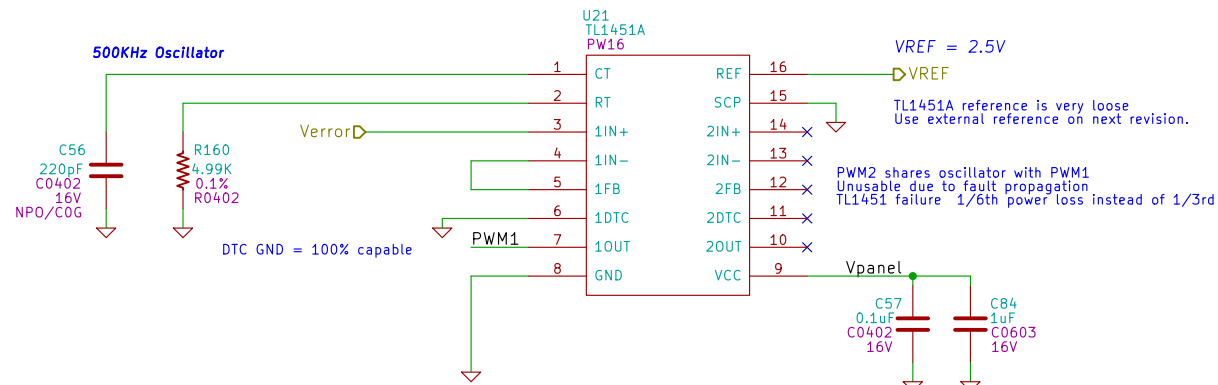
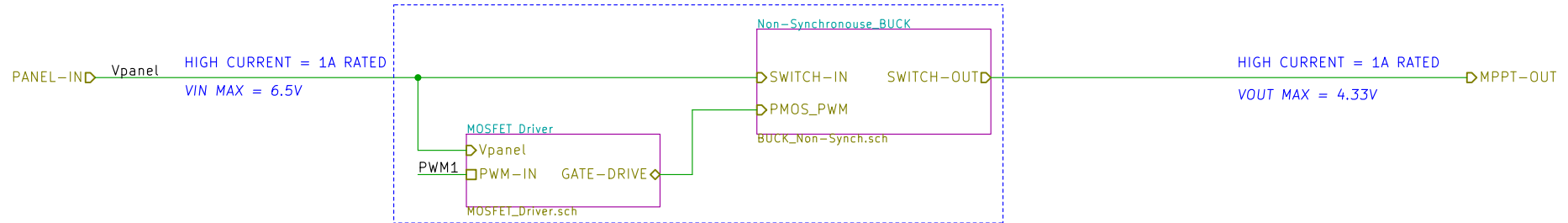
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 27/37

Switch Mode Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not gauranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: Control.sch

Sheet: /MPPT_String_Z-/TL1451 Control/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

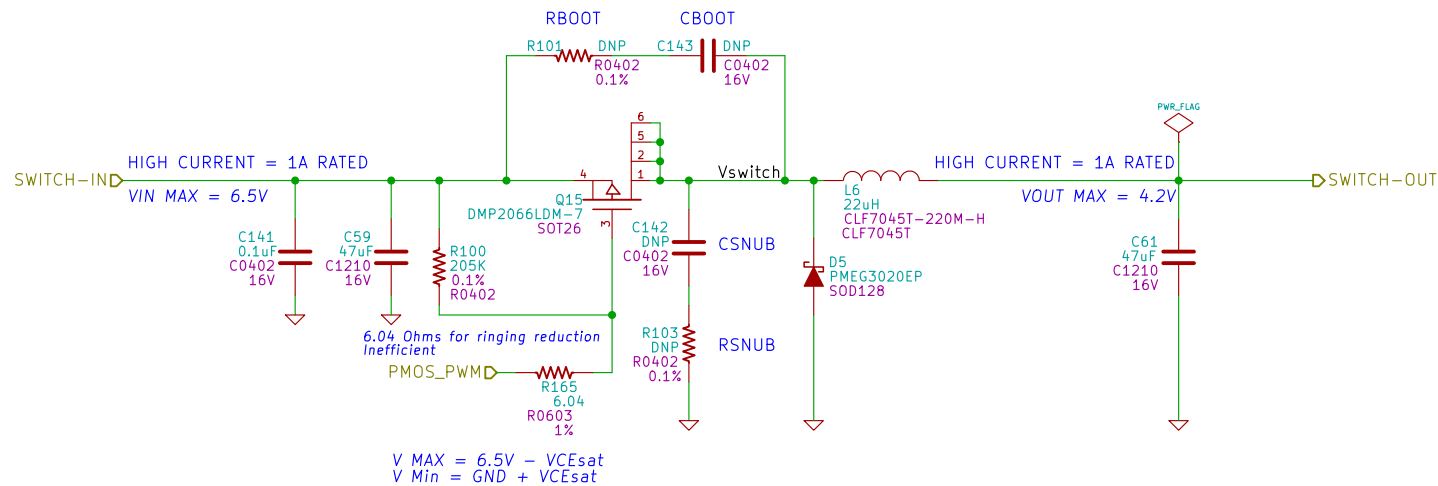
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 28/37

500 KHz Step-Down Buck Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: BUCK_Non-Synch.sch

Sheet: /MPPT_String_Z-/TL1451 Control/Non-Synchronous_BUCK/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

Date: 30 nov 2015

Rev: 2.0

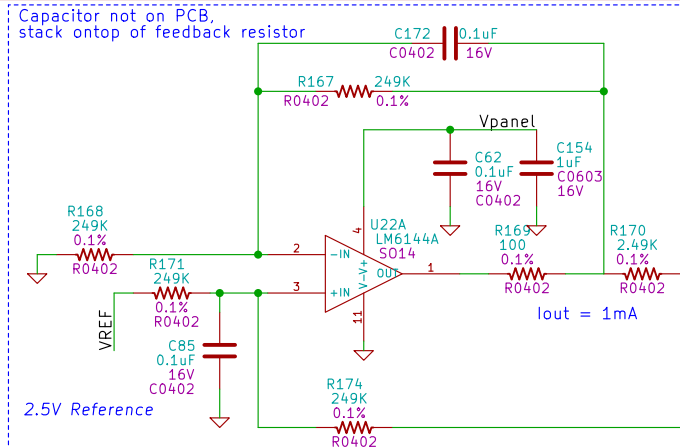
KiCad E.D.A.

Id: 29/37

$V_{IN\ MAX} = 6.5V$
 R_{166} 2K $R0402$ 1% $Max\ P_{dis} = 21mW$
 TL1451 NPN open collector
 C_{68} 1uF $C0603$ 16V
 Q_{11} MMDT2227M SOT26
 GATE-DRIVE
 $V_{MAX} = 6.5V - V_{CEsat}$
 $V_{Min} = GND + V_{CEsat}$

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

Id: 30/37



Constant 1mA Current Driver

Highest Voltage Wins = Lowest Duty Cycle Wins

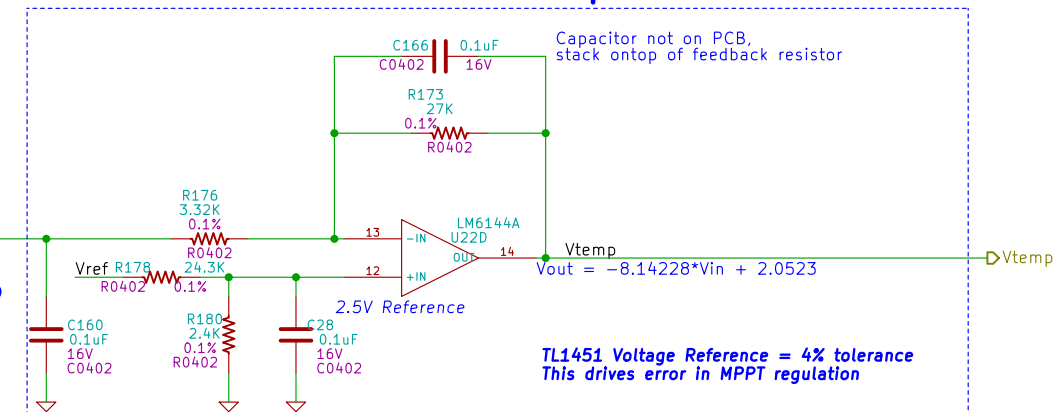
MPPT = V_{out} 3.3V to 4.33V

VregError → Increasing Duty Cycle = Decrease voltage

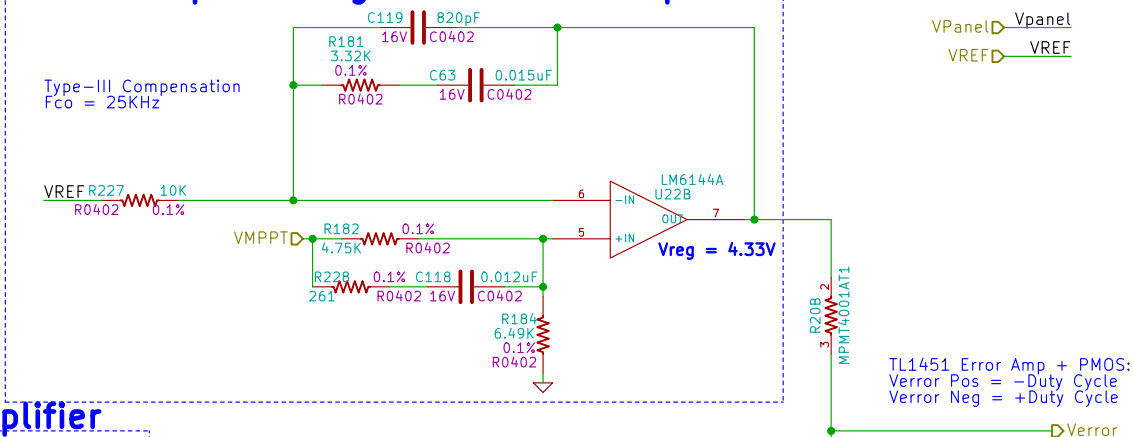
Regulation = V_{out} 4.33V, V_{panel} increasing

Vmppt Error → increases duty cycle (to load panel) so it looses

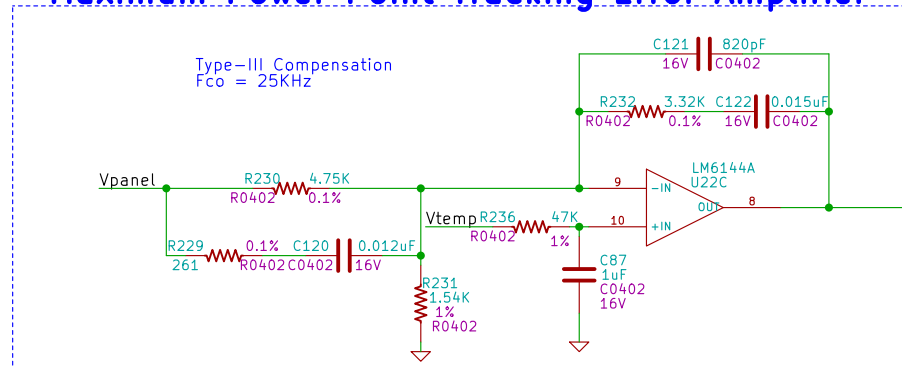
$Y = -mX + b$ Amplifier



4.33V Output Voltage Limit Error Amplifier



Maximum Power Point Tracking Error Amplifier



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

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File: RTD_Measurement.sch

Sheet: /MPPT_String_Z-/Amplifiers/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

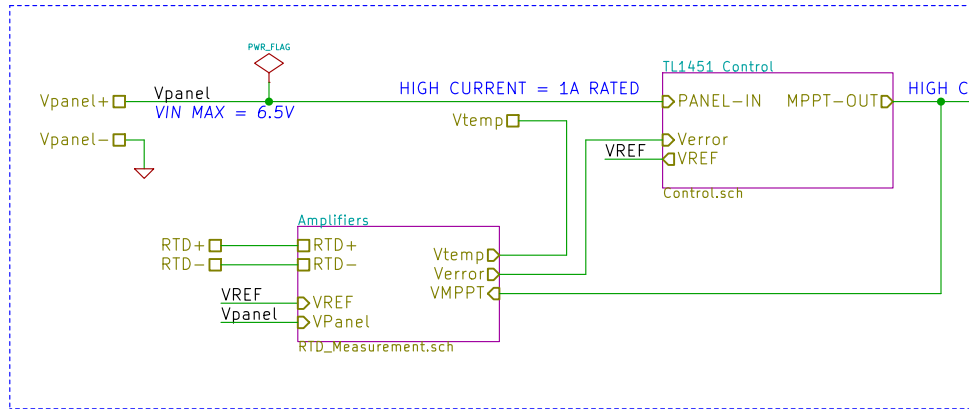
Date: 30 nov 2015

Rev: 2.0

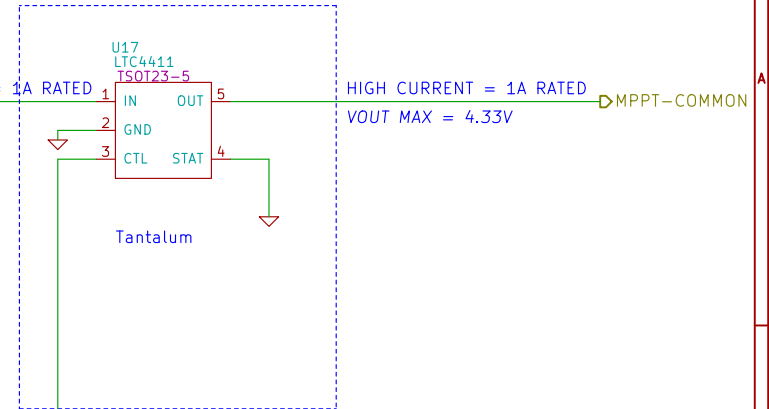
KiCad E.D.A.

Id: 31/37

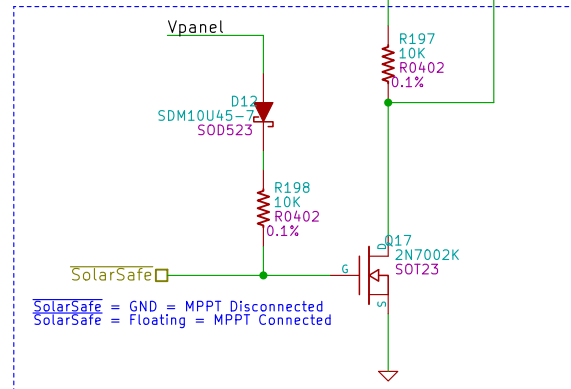
SWITCH MODE CONVERTER



Ideal Diode



Solar Safe Inverter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriere.
- * NASA derating taken into account, not gauranteed

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Brent Salmi, KB1LQD

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The Radio Amateur Satellite Corporation

File: MPPT_String.sch

Sheet: /MPPT_String_Z+/
 Title: Fox-1 Maximum Power Point Tracker

Size: A

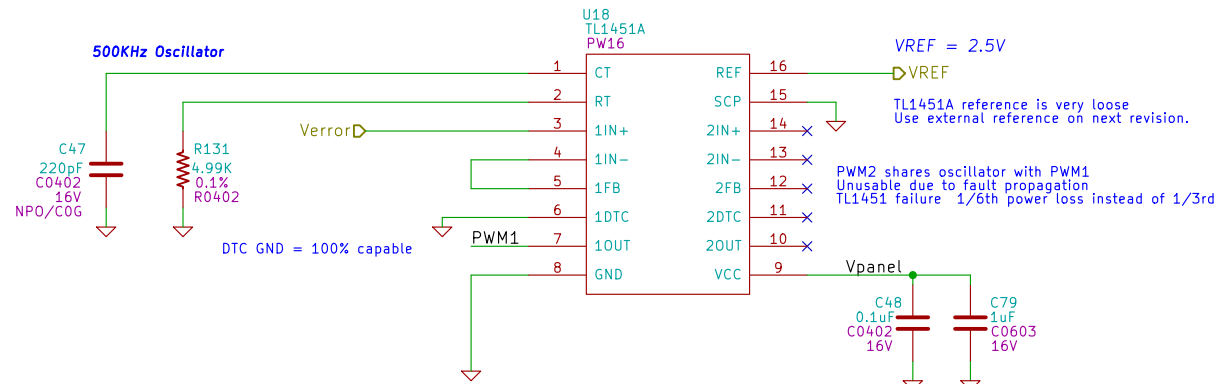
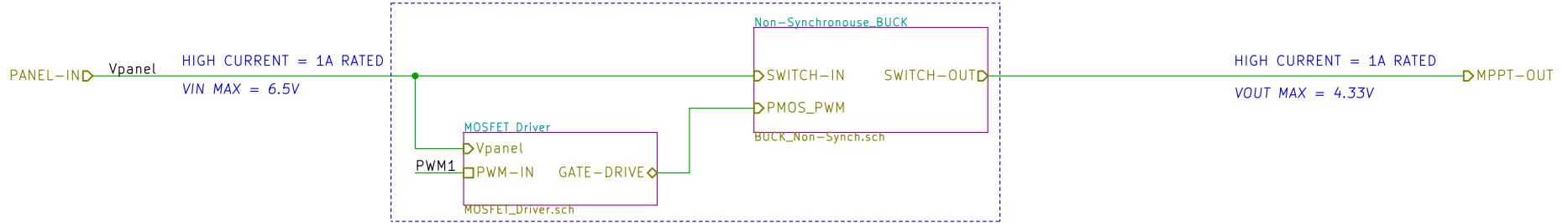
Date: 30 nov 2015

Rev: 2.0

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Id: 33/37

Switch Mode Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Brent Salmi, KB1LQD
Bryce Salmi, KB1LQC
The Radio Amateur Satellite Corporation

File: Control.sch

Sheet: /MPPT_String_Z+/TL1451 Control/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

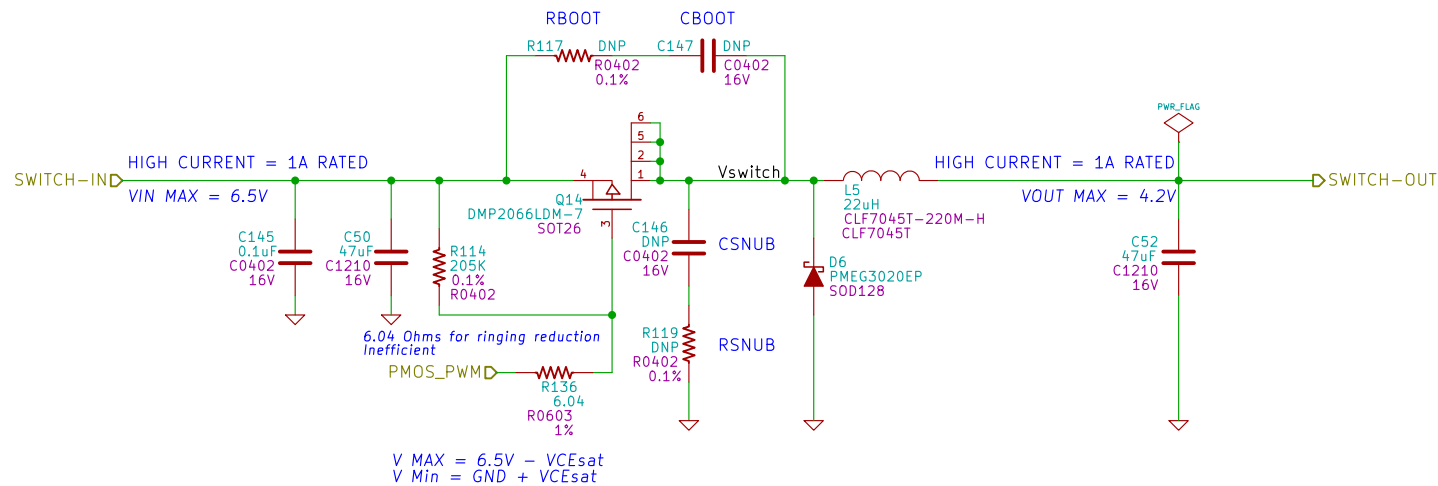
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 34/37

500 KHz Step-Down Buck Converter



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

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File: BUCK_Non-Synch.sch

Sheet: /MPPT_String_Z+/TL1451 Control/Non-Synchronous_BUCK/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

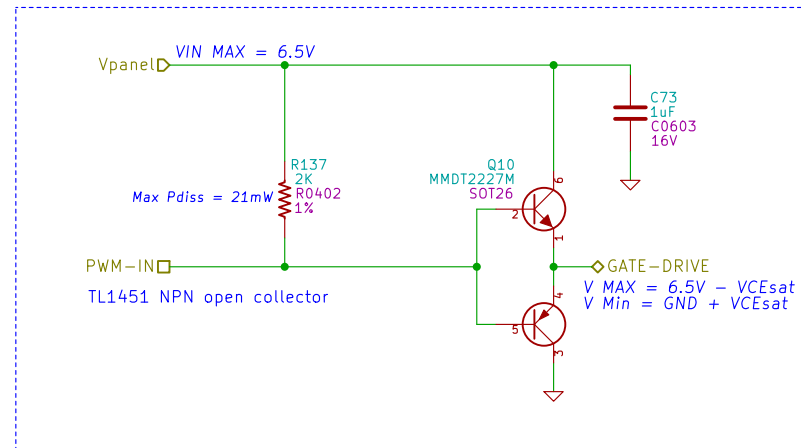
Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 35/37

TOTEM POLE MOSFET DRIVER



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * Parts not yet NASA derated.

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

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File: MOSFET_Driver.sch

Sheet: /MPPT_String_Z+/TL1451 Control/MOSFET Driver/

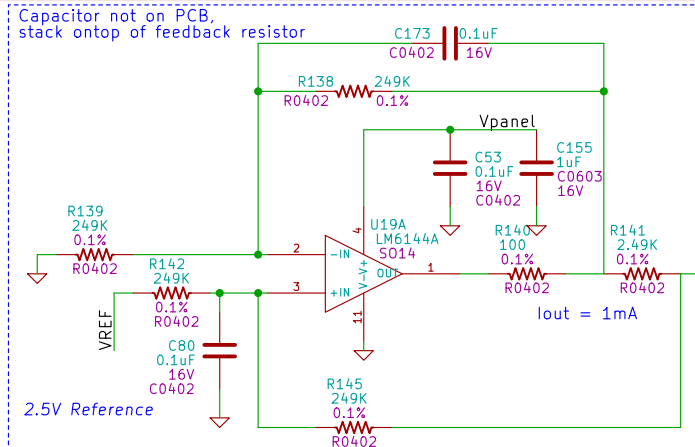
Title: Fox-1 Maximum Power Point Tracker

Size: A4 Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 36/37



Constant 1mA Current Driver

Highest Voltage Wins = Lowest Duty Cycle Wins

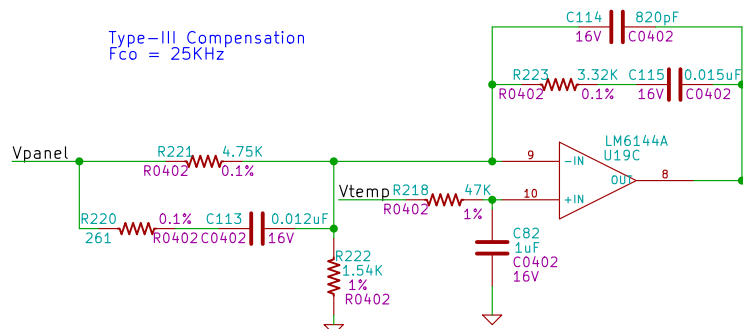
MPPT = Vout 3.3V to 4.33V

VregError -> Increasing Duty Cycle = Decrease voltage

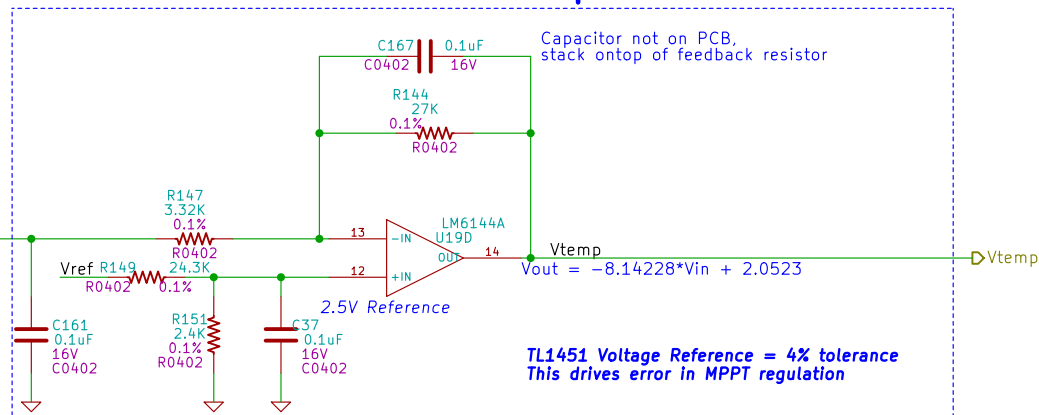
Regulation = Vout 4.33V, Vpanel increasing

Vmppt Error -> increases duty cycle (to load panel) so it looses

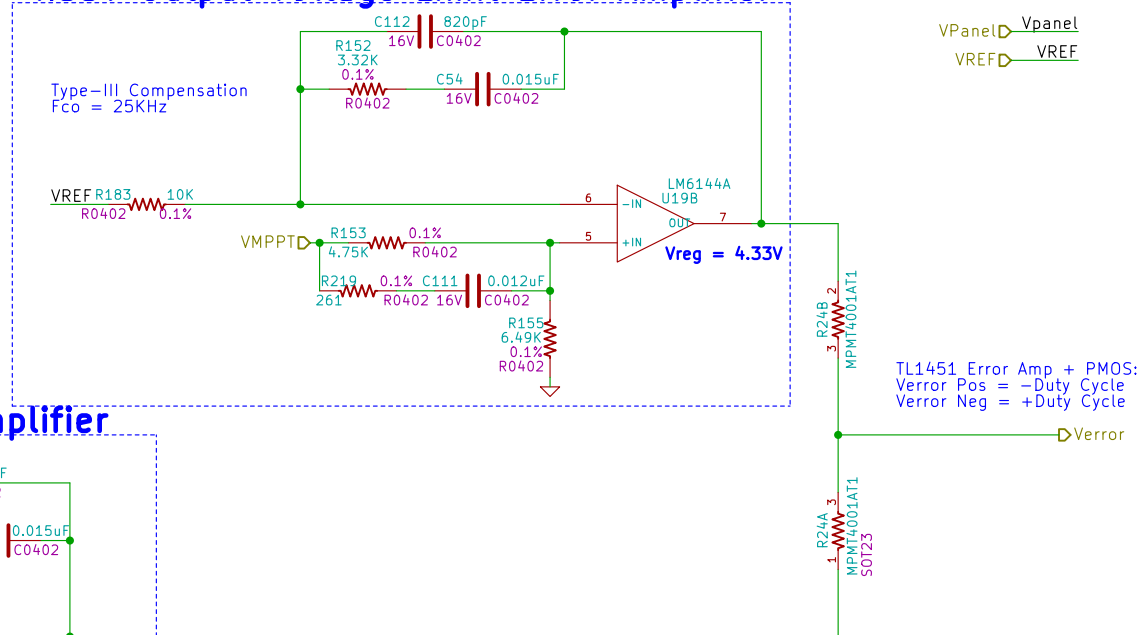
Maximum Power Point Tracking Error Amplifier



Y = -mX + b Amplifier



4.33V Output Voltage Limit Error Amplifier



NOTES

- * This MPPT implements a set-point constant voltage tracking algorithm based on panel temperature.
- * RIT MPPT Team: Brenton Salmi (KB1LQD), Bryce Salmi (KB1LQC), Ian MacKenzie (KB3OCF), Daniel Corriero.
- * NASA derating taken into account, not guaranteed

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Brent Salmi, KB1LQD

Bryce Salmi, KB1LQC

The Radio Amateur Satellite Corporation

File: RTD_Measurement.sch

Sheet: /MPPT_String_Z+/Amplifiers/

Title: Fox-1 Maximum Power Point Tracker

Size: A4

Date: 30 nov 2015

Rev: 2.0

KiCad E.D.A.

Id: 37/37