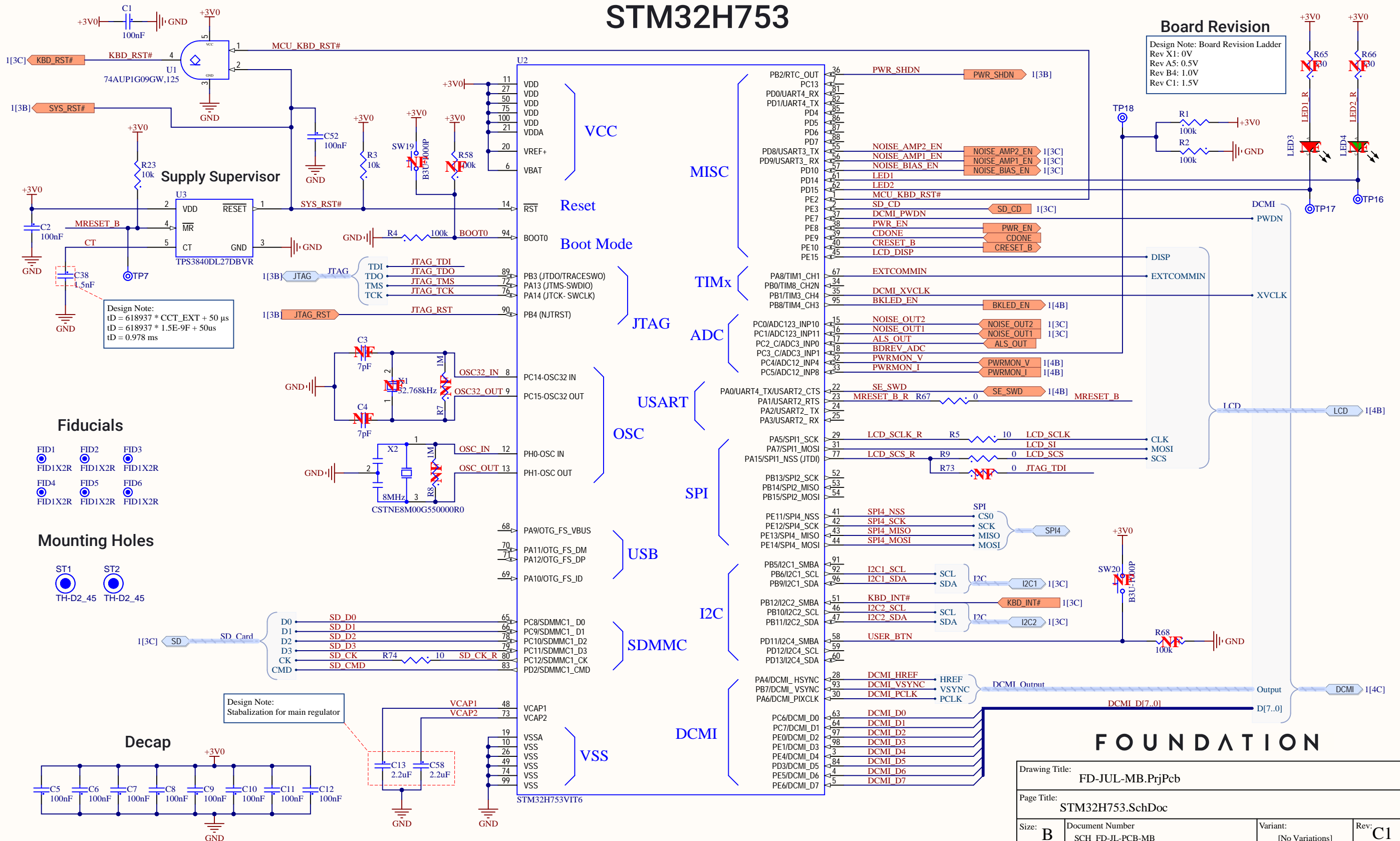


STM32H753



FOUNDATION

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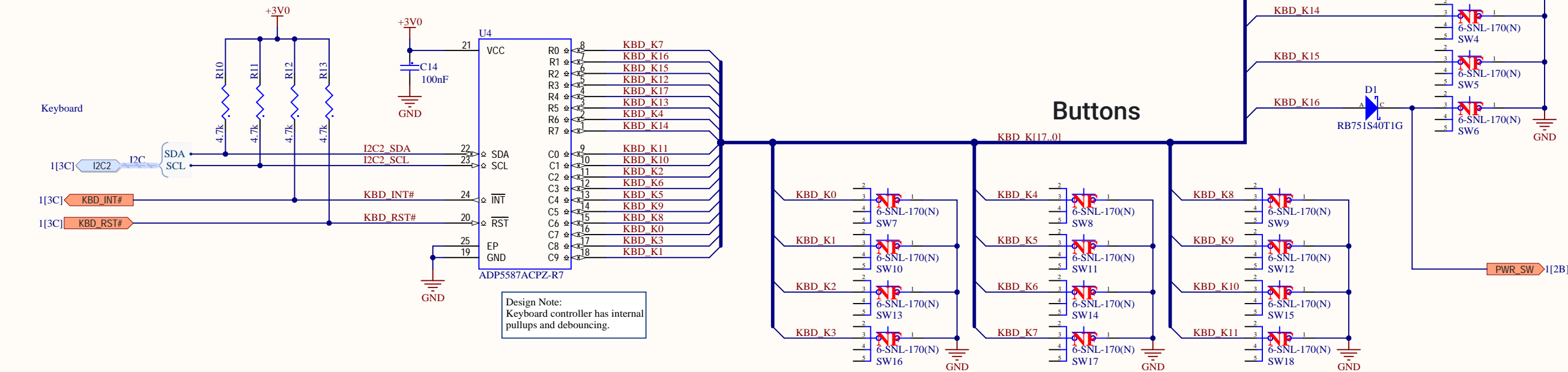
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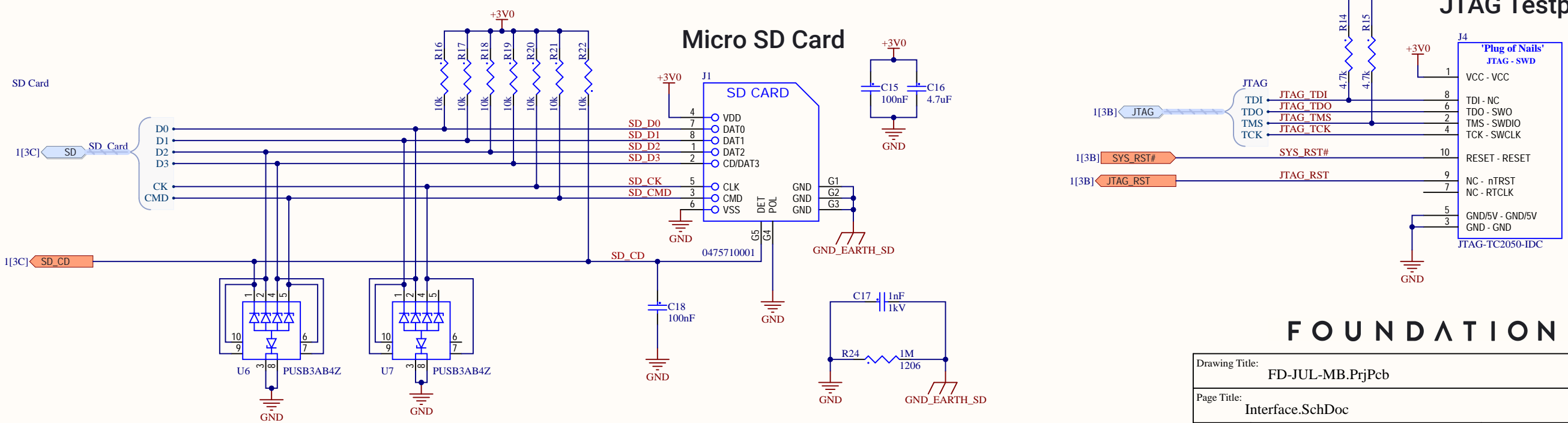
Interfaces

Keypad Controller

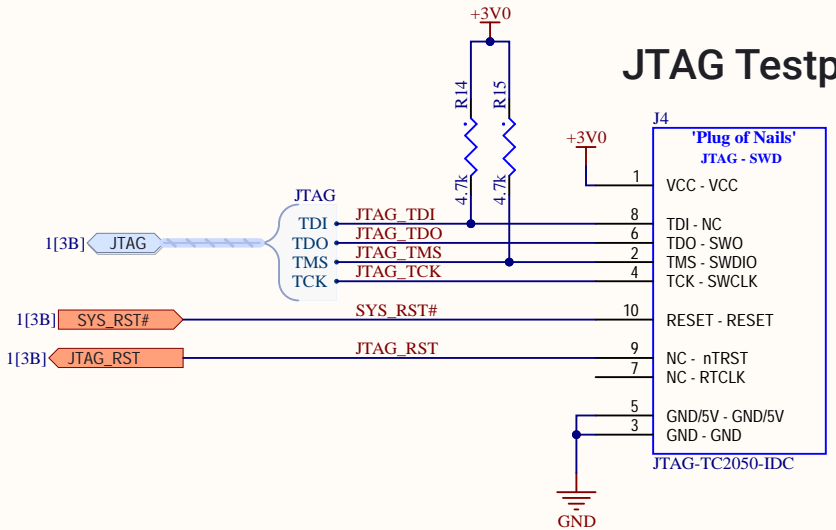
Buttons



Micro SD Card



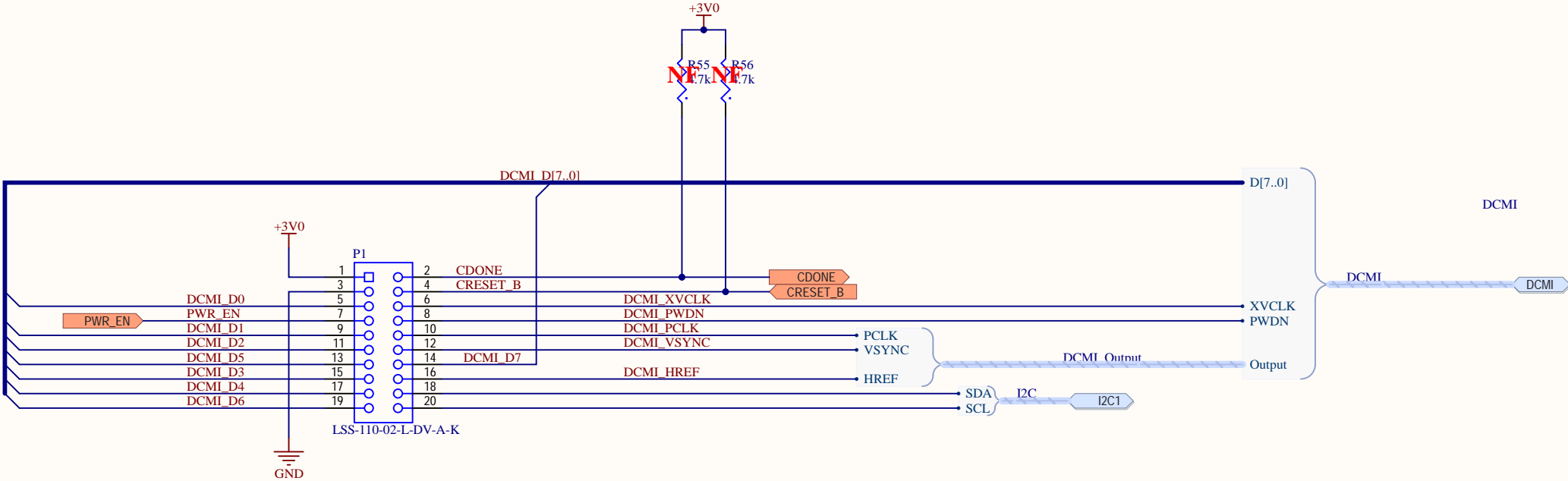
JTAG Testpoints



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Camera Connector



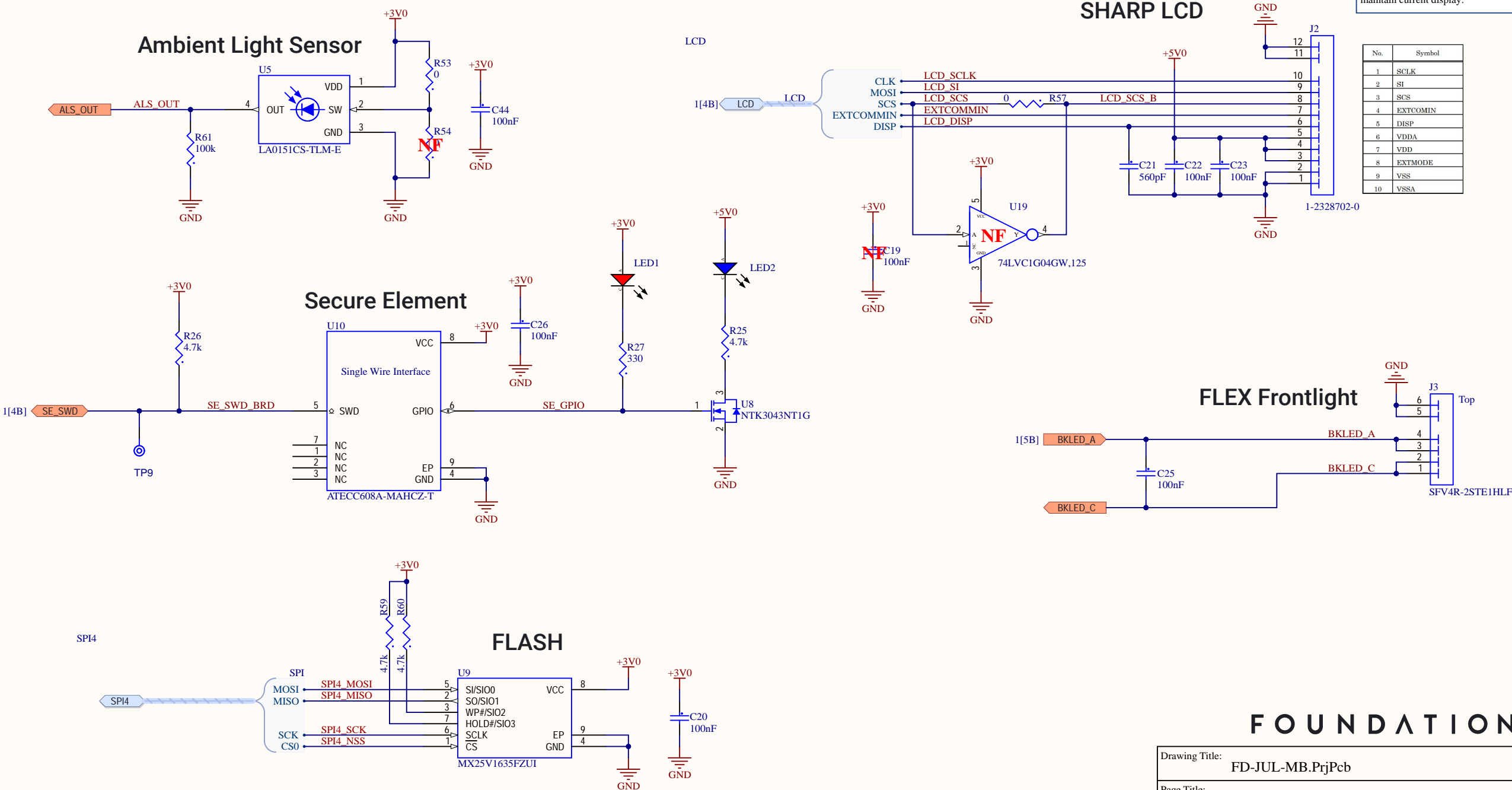
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Size: B	Document Number: SCH_FD-JL-PCB-MB	Variant: [No Variations]	Rev: C1
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LCD, MEMORY

Design Note:
Signal Vih: 2.7V < Vih < 3.3V.
Applies to SCLK, SI, SCS, DISP, EXTCOMIN

Design Note:
Supply EXTCOMIN with a 1HZ pulse to
maintain current display.



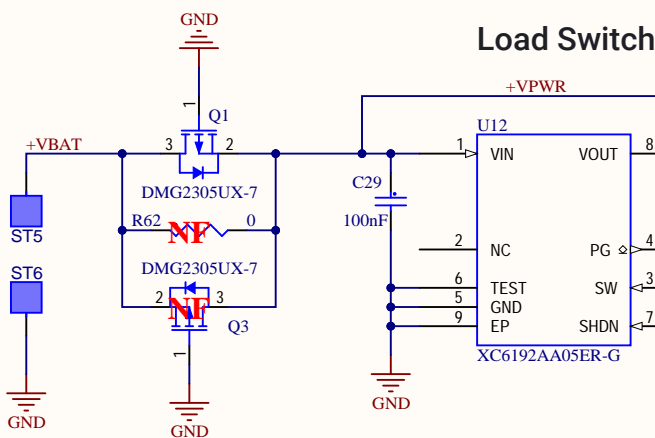
No.	Symbol
1	SCLK
2	SI
3	SCS
4	EXTCOMIN
5	DISP
6	VDDA
7	VDD
8	EXTMODE
9	VSS
10	VSSA

FOUNDATION

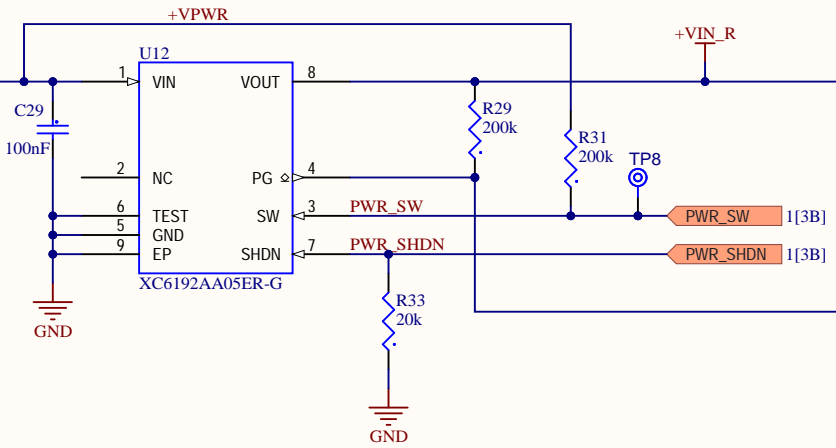
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Size: B	Document Number: SCH_FD-JL-PCB-MB	Variant: [No Variations]	Rev: C1
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Power Regulators

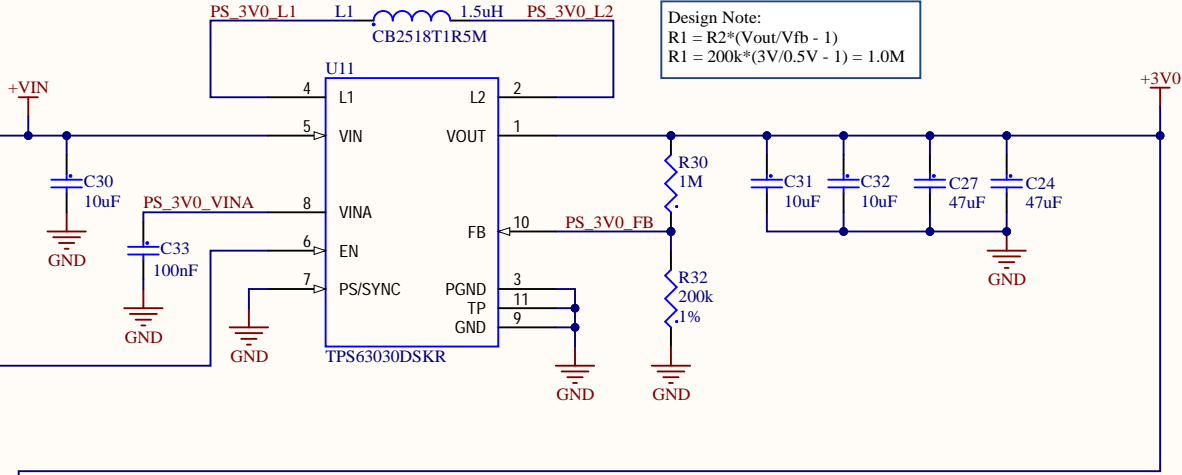
Reverse Current Protection



Load Switch

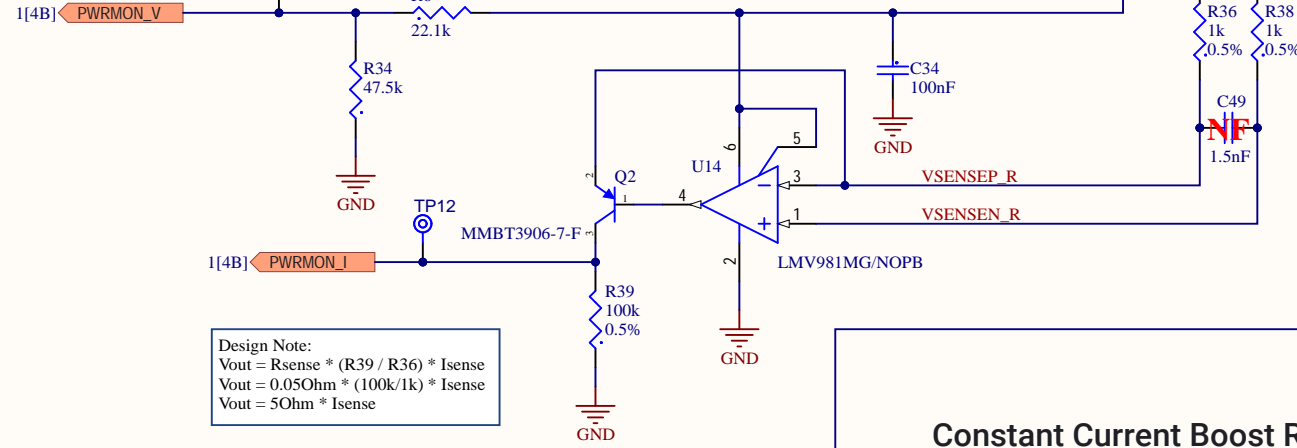


Boost-Buck Regulator: 3V0 (1A max)



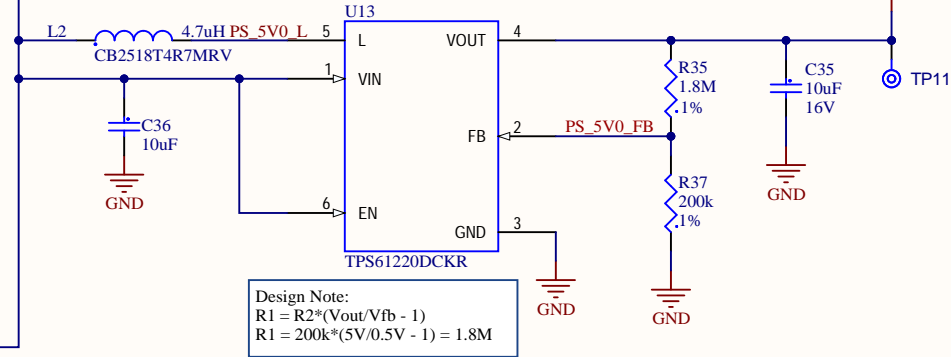
Design Note:
 $R1 = R2 * (V_{out} / V_{fb} - 1)$
 $R1 = 200k * (3V / 0.5V - 1) = 1.0M$

Current Monitor



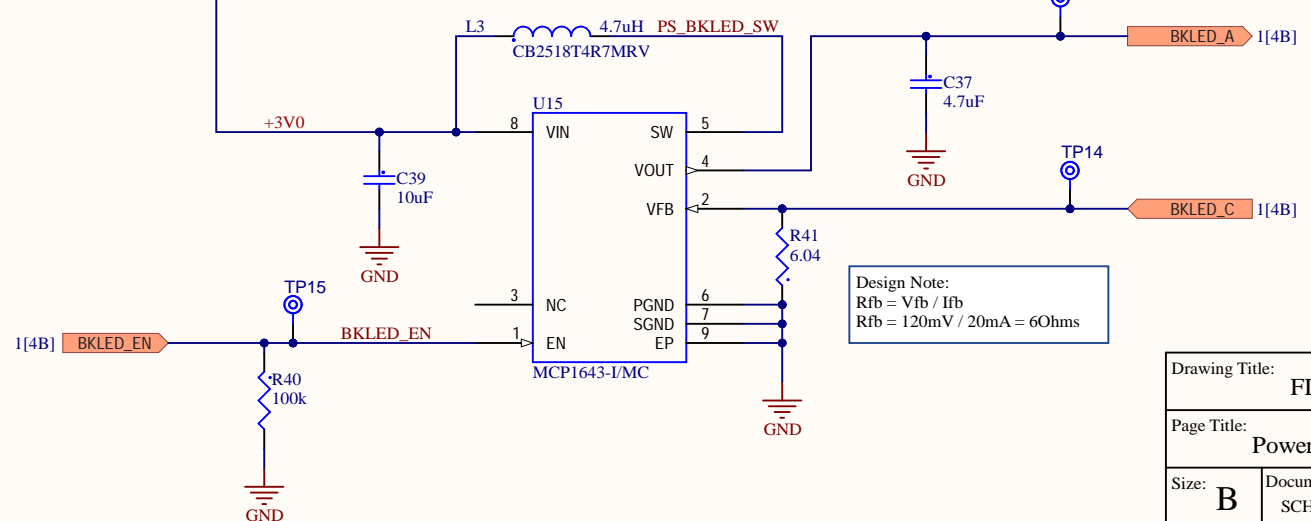
Design Note:
 $V_{out} = R_{sense} * (R39 / R36) * I_{sense}$
 $V_{out} = 0.05Ohm * (100k / 1k) * I_{sense}$
 $V_{out} = 50Ohm * I_{sense}$

Boost Regulator: 5V0 (150mA max)



Design Note:
 $R1 = R2 * (V_{out} / V_{fb} - 1)$
 $R1 = 200k * (5V / 0.5V - 1) = 1.8M$

Constant Current Boost Regulator @ 20mA

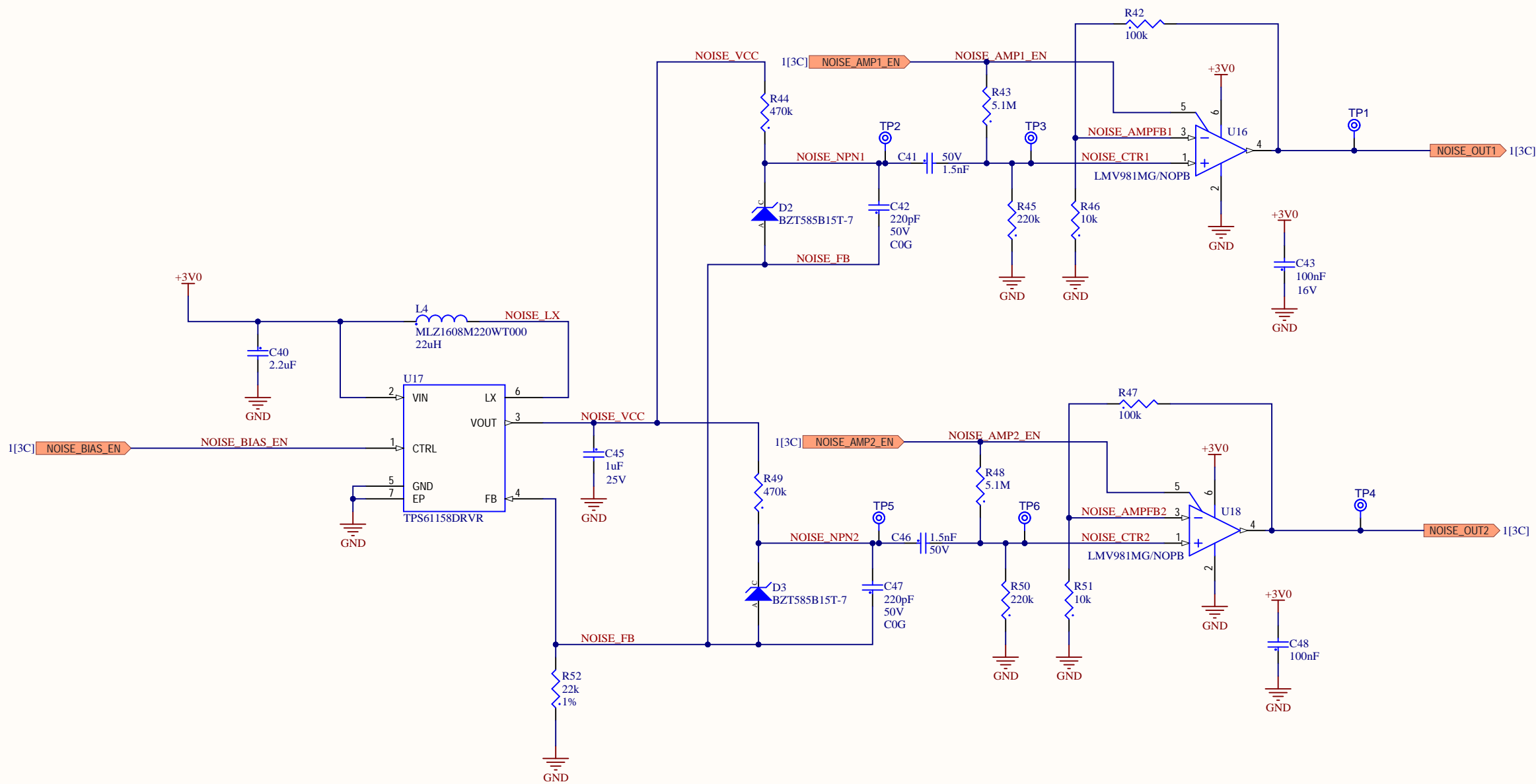


Design Note:
 $R_{fb} = V_{fb} / I_{fb}$
 $R_{fb} = 120mV / 20mA = 60Ohms$

FOUNDATION

Drawing Title: FD-JUL-MB.PrjPcb			
Page Title: Power.SchDoc			
Size: B	Document Number: SCH_FD-JL-PCB-MB	Variant: [No Variations]	Rev: C1
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Avalanche Breakdown Noise Source



Circuit designed by:
Andrew 'bunnie' Huang
@bunniestudios
betrusted.io

FOUNDATION

Drawing Title: FD-JUL-MB.PrjPcb			
Page Title: Noise.SchDoc			
Size: B	Document Number: SCH_FD-JL-PCB-MB	Variant: [No Variations]	Rev: C1
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