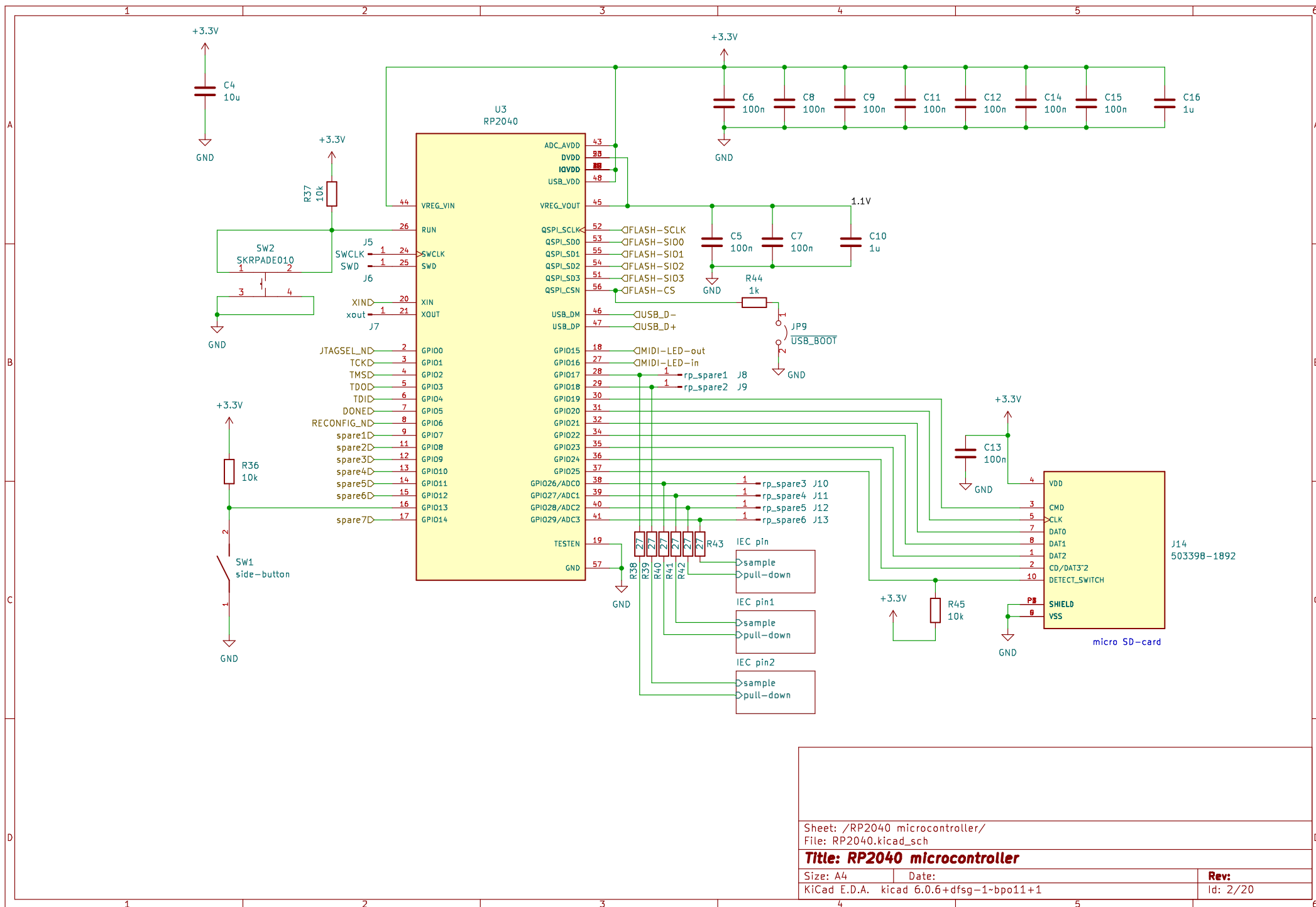


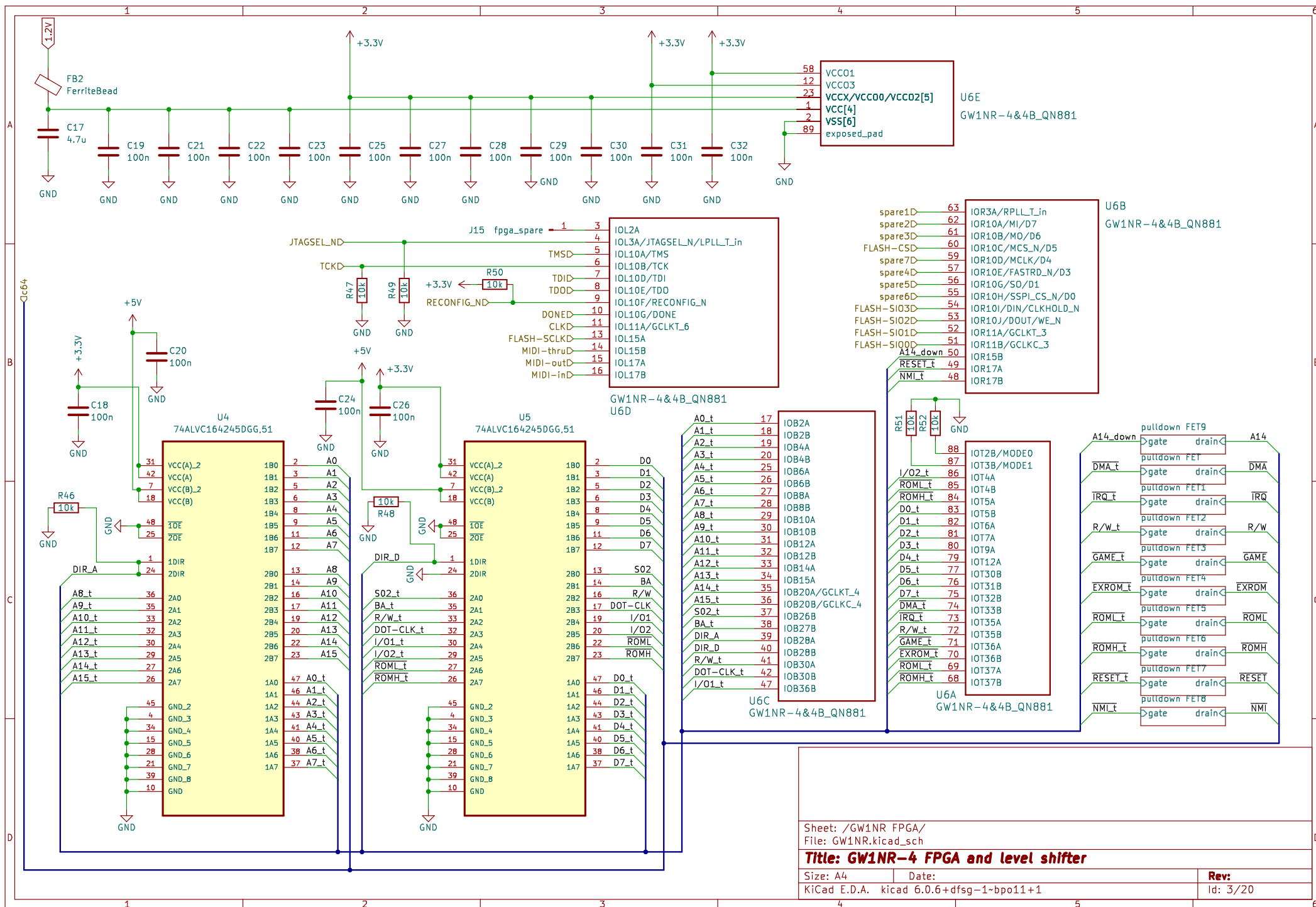
Sheet: /
File: kerberos2.kicad_sch

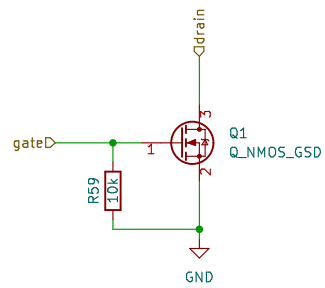
Title: Kerberos 2

Size: A4 Date:
KiCad E.D.A. kicad 6.0.6+dfsg-1-bpo11+1

Rev:
Id: 1/20



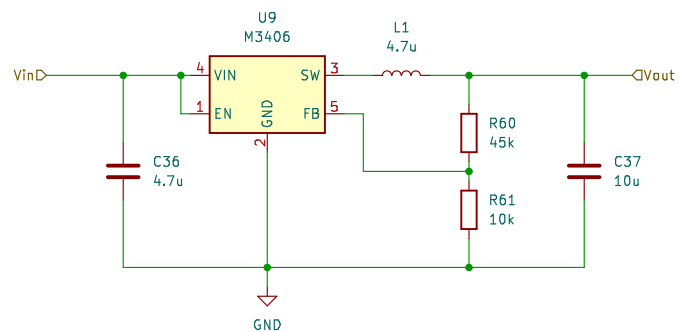




Sheet: /GW1NR FPGA/pulldown FET1/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1		Id: 5/20



Sheet: /3.3V power supply/
File: power-supply.kicad_sch

Title: step-down voltage regulator

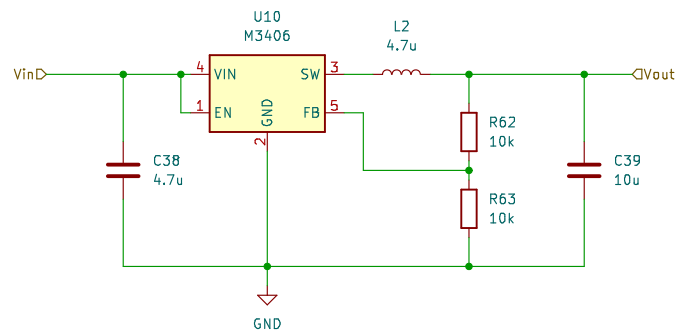
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Date:

KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1

Rev:

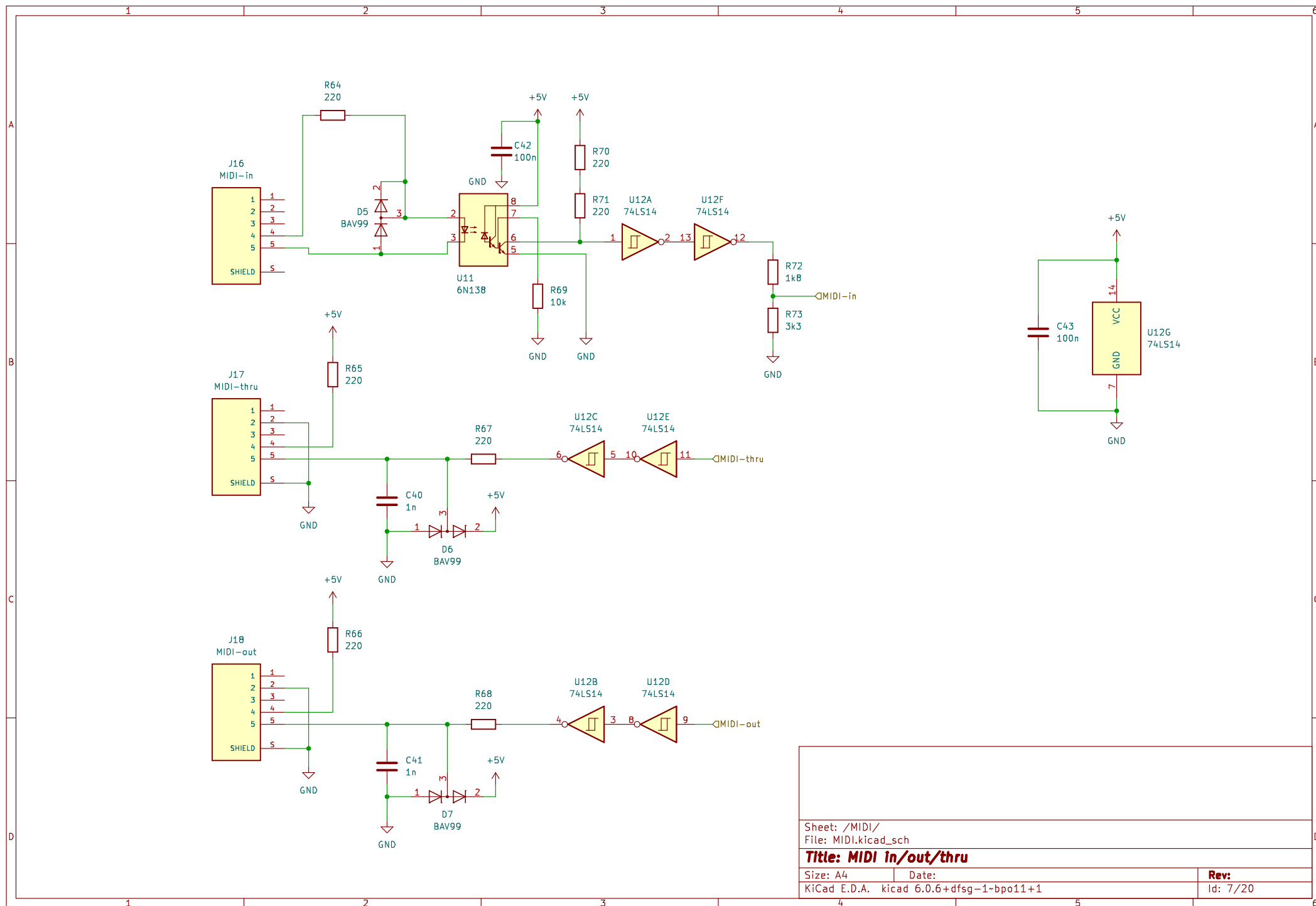
Id: 6/20

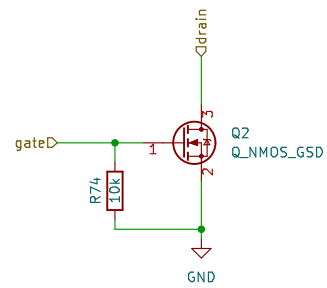


Sheet: /1.2V power supply/
File: power-supply.kicad_sch

Title: step-down voltage regulator

Size: A4	Date:	Rev:
KiCad E.D.A.	kicad 6.0.6+dfsg-1~bpo11+1	Id: 7/20





Sheet: /GW1NR FPGA/pulldown FET/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

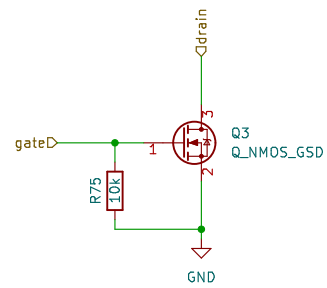
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Date:

KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1

Rev:

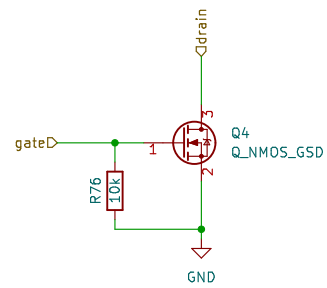
Id: 8/20



Sheet: /GW1NR FPGA/pulldown FET2/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

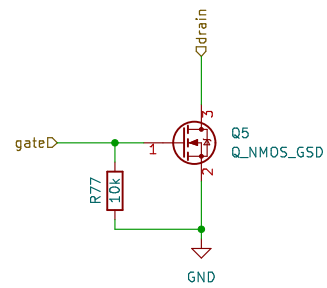
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1		Id: 9/20



Sheet: /GW1NR FPGA/pulldown FET3/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

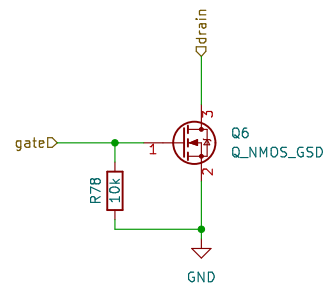
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1		Id: 10/20



Sheet: /GW1NR FPGA/pulldown FET4/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

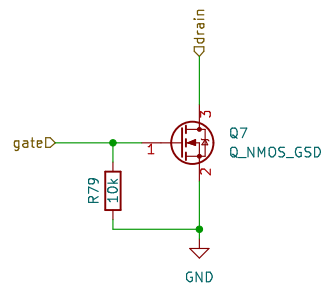
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1		Id: 11/20



Sheet: /GW1NR FPGA/pulldown FET5/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

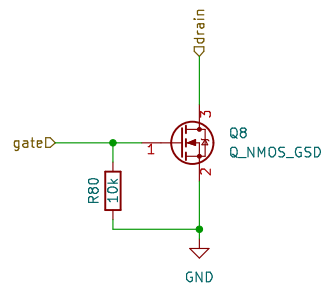
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Sheet: /GW1NR FPGA/pulldown FET6/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

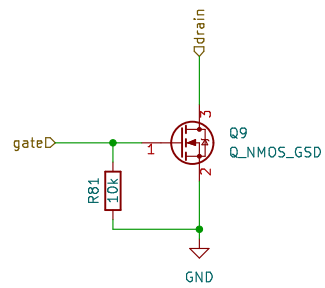
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KiCad E.D.A.	kicad 6.0.6+dfsg-1~bpo11+1	Id: 13/20



Sheet: /GW1NR FPGA/pulldown FET7/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

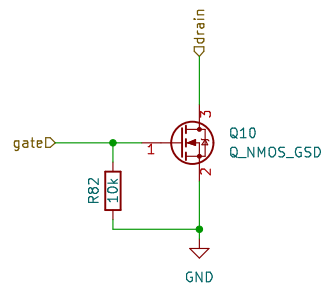
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Sheet: /GW1NR FPGA/pulldown FETB/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1		Id: 15/20



Sheet: /GW1NR FPGA/pulldown FET9/
File: pulldown-fet.kicad_sch

Title: pulldown circuit

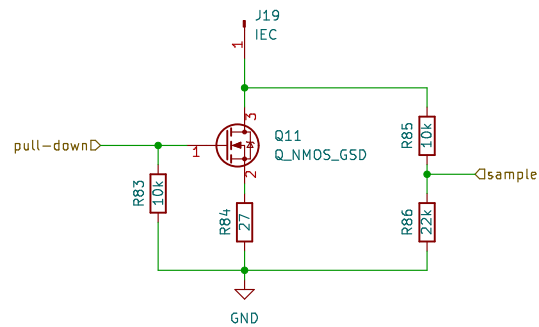
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Date:

KiCad E.D.A. kicad 6.0.6+dfsg-1~bpo11+1

Rev:

Id: 16/20



Sheet: /RP2040 microcontroller/IEC pin1/
File: iec-pin.kicad_sch

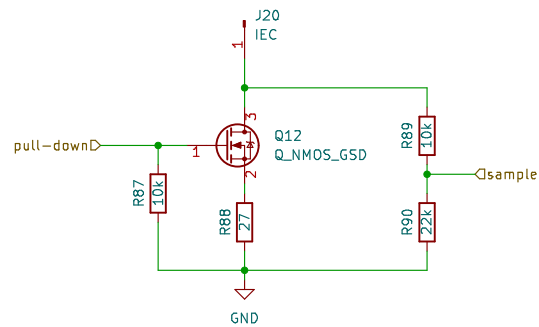
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Size: A4
KiCad E.D.A. kicad 6.0.6+dfsg-1-bpo11+1

Date:

Rev:

Id: 17/20



Sheet: /RP2040 microcontroller/IEC pin/
File: iec-pin.kicad_sch

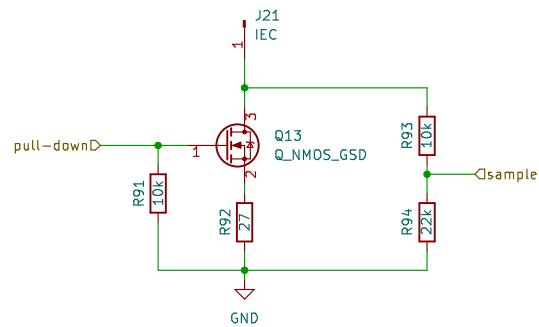
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Size: A4
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Date:

Rev:

Id: 18/20



Sheet: /RP2040 microcontroller/IEC pin2/
File: iec-pin.kicad_sch

Title:

Size: A4
KiCad E.D.A. kicad 6.0.6+dfsg-1-bpo11+1

Date:

Rev:

Id: 19/20