

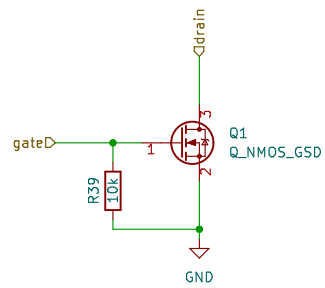
Sheet: /current sense/  
File: current\_sense.kicad\_sch

**Title: hall effect sensor current measurment**

Size: A4  
KiCad E.D.A. kicad 6.0.5+dfsg-1~bpo11+1

Date:  
Id: 4/17

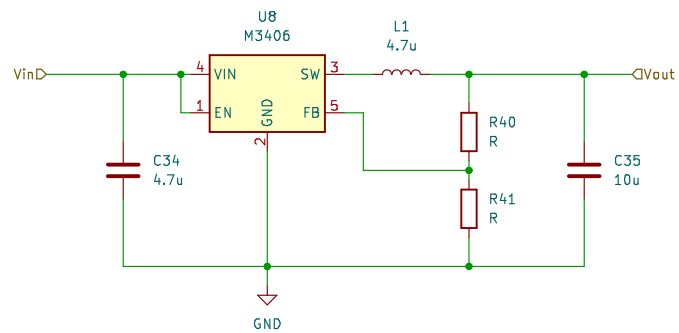
Rev:



Sheet: /GW1NR FPGA/pulldown FET1/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

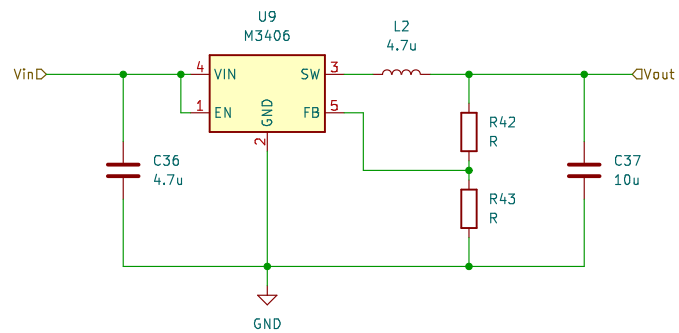
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Sheet: /3.3V power supply/  
File: power-supply.kicad\_sch

**Title: step-down voltage regulator**

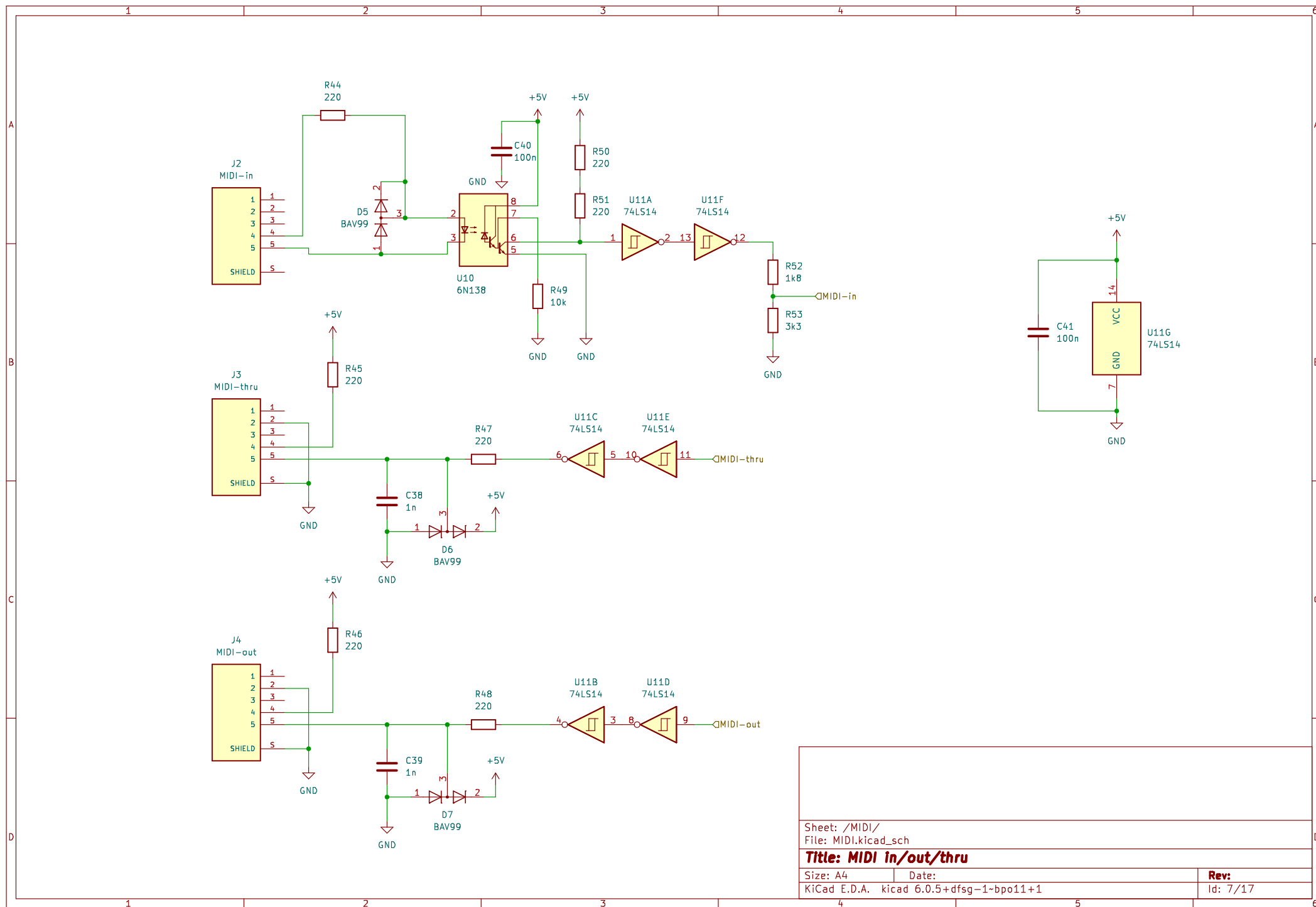
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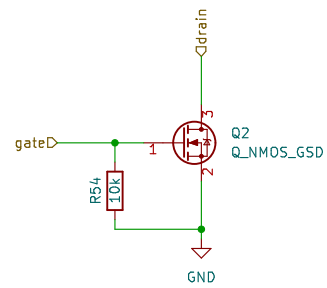
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File: power-supply.kicad\_sch

**Title: step-down voltage regulator**

Size: A4	Date:	Rev:
KiCad E.D.A.	kicad 6.0.5+dfsg-1~bpo11+1	Id: 7/17



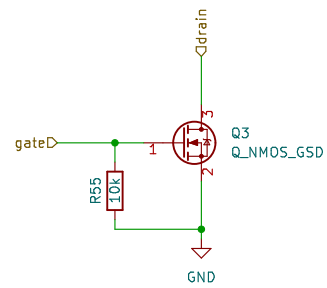




Sheet: /GW1NR FPGA/pulldown FET/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

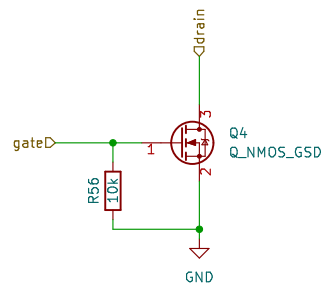
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Sheet: /GW1NR FPGA/pulldown FET2/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

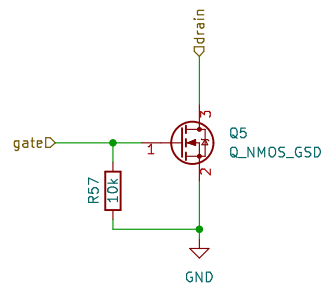
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Sheet: /GW1NR FPGA/pulldown FET3/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

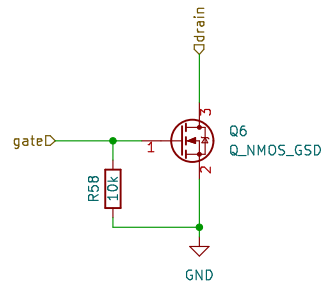
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Sheet: /GW1NR FPGA/pulldown FET4/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.5+dfsg-1~bpo11+1		Id: 11/17



Sheet: /GW1NR FPGA/pulldown FET5/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

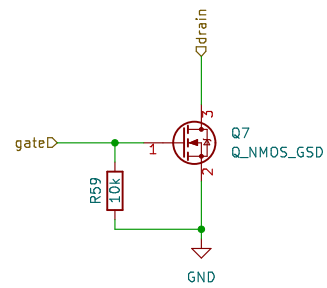
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Rev:

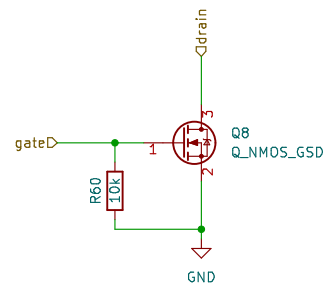
Id: 12/17



Sheet: /GW1NR FPGA/pulldown FET6/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

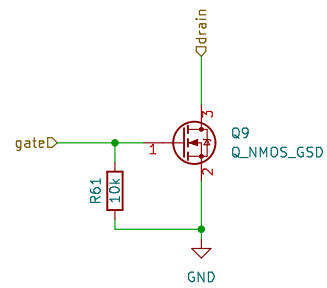
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KiCad E.D.A. kicad 6.0.5+dfsg-1~bpo11+1		Id: 13/17



Sheet: /GW1NR FPGA/pulldown FET7/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.5+dfsg-1~bpo11+1		Id: 14/17

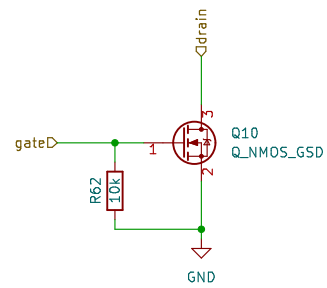


Sheet: /GW1NR FPGA/pulldown FETB/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.5+dfsg-1~bpo11+1		Id: 15/17





Sheet: /GW1NR FPGA/pulldown FET9/  
File: pulldown-fet.kicad\_sch

**Title: pulldown circuit**

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 6.0.5+dfsg-1~bpo11+1		Id: 16/17