Sample length

ADC data

wrap page N | wrap page N

last addr | first addr

Stop 1 | Stop 2 | Stop 3 | Stop 4

delayed Stop | Next sample addr

Saved data in wrap page N
of SIS3302/3320 Memory

first addr

Next sample addr
(wrap_read_addr_offset)

last addr
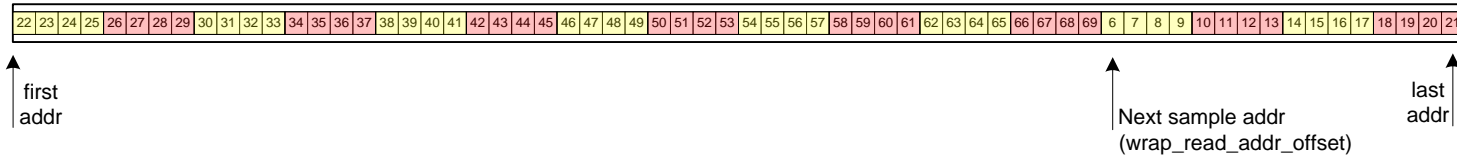
Simple rearranged data in the PC memory **regardless of** the Stop Correction bits

```
for (i=0;i<dma_request_no_of_Lwords;i++) {
    gl_uint_adc1_data_array[i] = (gl_dma_rd_buffer[(wrap_read_addr_offset + i) & wrap_read_addr_mask] ) & 0xffffffff;
}
```

first addr

last addr