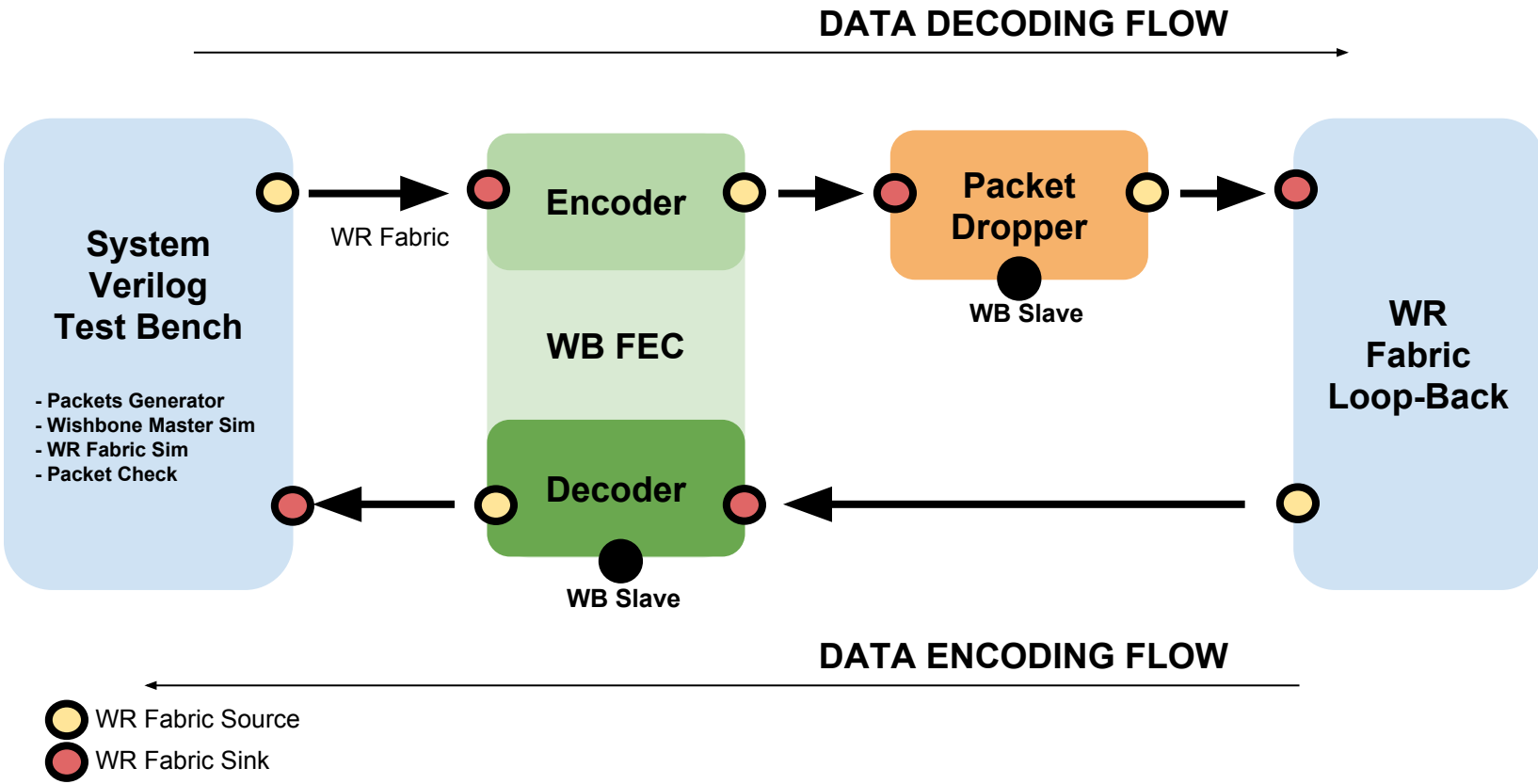
DATA DECODING FLOW
System Verilog Test Bench
- Packets Generator
- Wishbone Master Sim
- WR Fabric Sim
- Packet Check
WR Fabric
Encoder
WB FEC
Packet Dropper
WB Slave
WR Fabric Loop-Back
Decoder
WB Slave
DATA ENCODING FLOW
WR Fabric Source
WR Fabric Sink