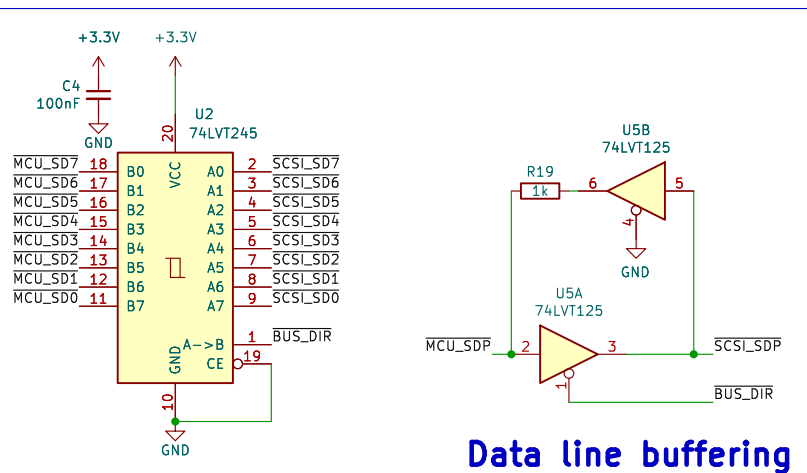
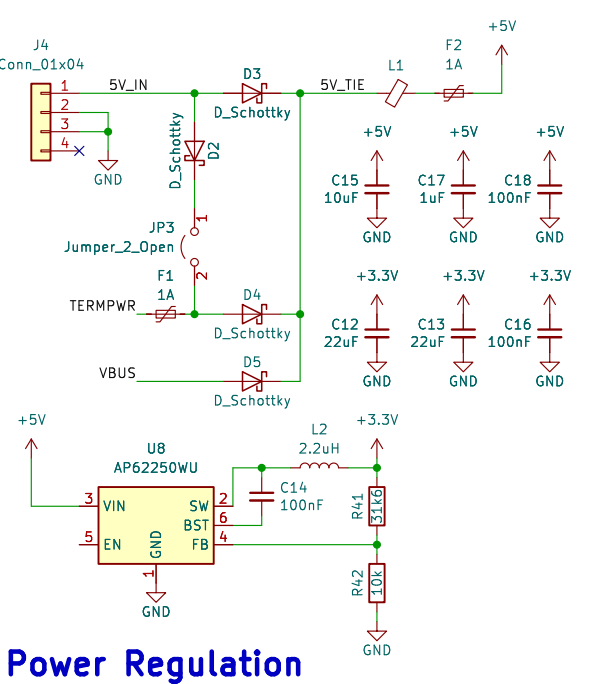
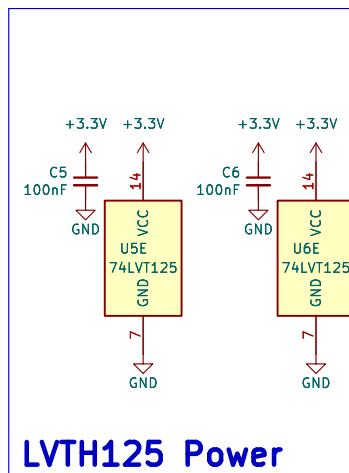


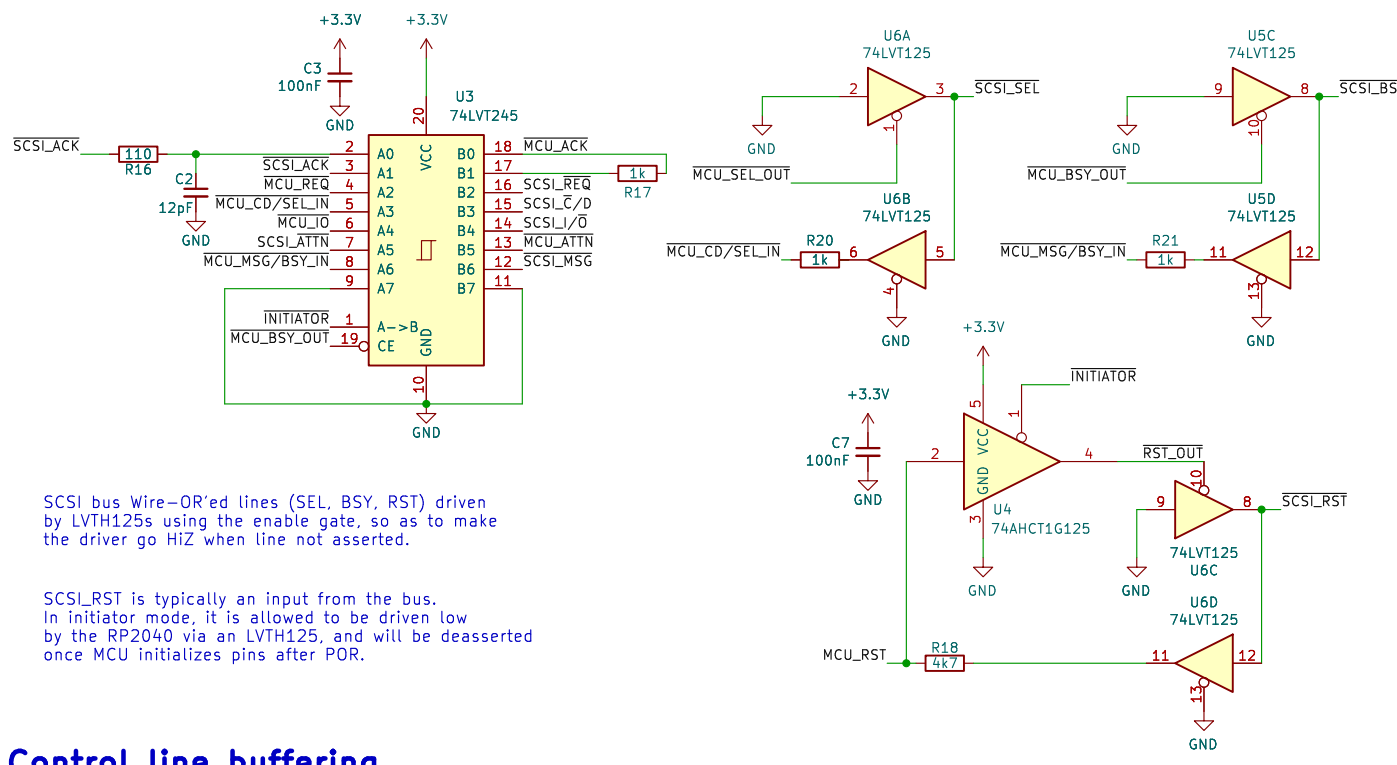
Pull-ups for MCU-side SCSI bus Wire-OR'ed lines and data lines' buffer direction control.



Data line buffering



LVTH125 Power



SCSI bus Wire-OR'ed lines (SEL, BSY, RST) driven by LVTH125s using the enable gate, so as to make the driver go HiZ when line not asserted.

SCSI\_RST is typically an input from the bus. In initiator mode, it is allowed to be driven low by the RP2040 via an LVTH125, and will be deasserted once MCU initializes pins after POR.

Control line buffering

