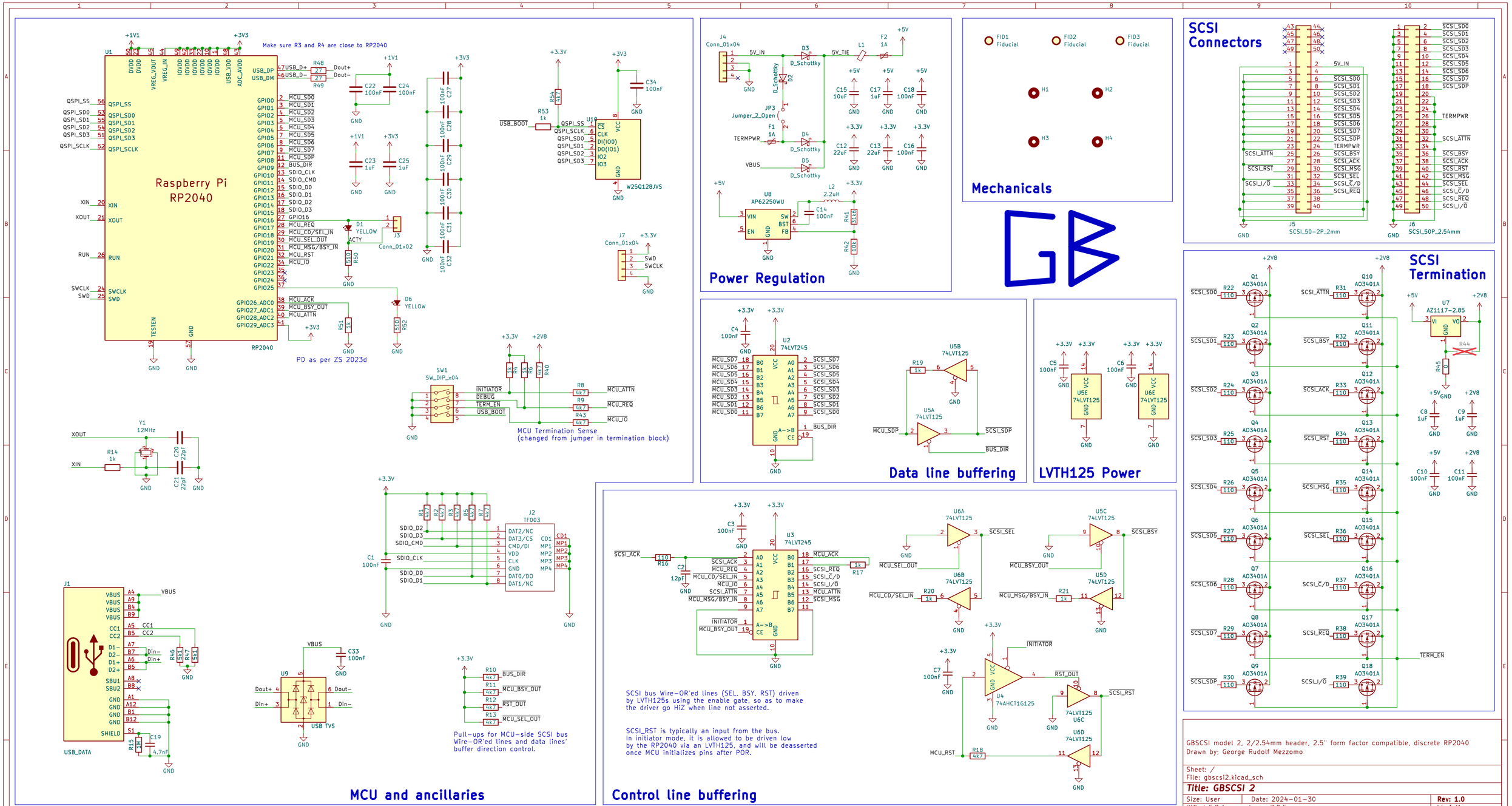
Raspberry Pi RP2040
Make sure R3 and R4 are close to RP2040
PD as per ZS 2023d
MCU Termination Sense (changed from jumper in termination block)
MCU and ancillaries
Power Regulation
Mechanicals
SCSI Connectors
SCSI Termination
Data line buffering
LVTH125 Power
Control line buffering
SCSI bus Wire-OR'ed lines (SEL, BSY, RST) driven by LVTH125s using the enable gate, so as to make the driver go HiZ when line not asserted.
SCSI_RST is typically an input from the bus. In initiator mode, it is allowed to be driven low by the RP2040 via an LVTH125, and will be deasserted once MCU initializes pins after POR.
Pull-ups for MCU-side SCSI bus Wire-OR'ed lines and data lines' buffer direction control.
GBSCSI model 2, 2/2.54mm header, 2.5" form factor compatible, discrete RP2040
Drawn by: George Rudolf Mezzomo
Sheet: /
File: gbscsi2.kicad_sch
Title: GBSCSI 2
Size: User
Date: 2024-01-30
Rev: 1.0
KiCad E.D.A. eeschema 7.0.5
Id: 1/1