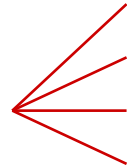


**V1\_DN.V1\_DN, PTEN\_DN.V1\_DN**



ESC\_J1\_UP\_LATE.V1\_DN, ESC\_J1\_UP\_LATE.V1\_DN

ATF2\_UP.V1\_UP, ATF2\_UP.V1\_UP

ESC\_J1\_UP\_EARLY.V1\_DN, ESC\_J1\_UP\_EARLY.V1\_DN

ESC\_V6.5\_UP\_LATE.V1\_DN, ESC\_V6.5\_UP\_LATE.V1\_DN