

**INT\_CXCR5POS\_CD4\_EFF\_TCELL\_D6\_LCMV\_UP, GSE43863\_NAIVE\_VS\_LY6C\_INT\_CXCR5POS\_CD4\_EFF\_TCELL\_D6\_LCMV\_UP**  GSE43863\_NAIVE\_VS\_TH1\_EFF\_CD4\_TCELL\_D6\_LCMV\_UP, GSE43863\_NAIVE\_VS\_TH1\_EFF\_CD4\_TCELL\_D6\_LCMV\_UP, GSE37336\_LY6C\_POS\_VS\_NEG\_NAIVE\_CD4\_TCELL\_UP, GSE37336\_LY6C\_POS\_VS\_NEG\_NAIVE\_CD4\_TCELL\_UP