


**C\_UP.V1\_UP, MYC\_UP.V1\_UP**



ESC\_J1\_UP\_LATE.V1\_UP, ESC\_J1\_UP\_LATE.V1\_UP  
RAPA\_EARLY\_UP.V1\_UP, RAPA\_EARLY\_UP.V1\_UP  
CYCLIN\_D1\_KE\_.V1\_DN, CYCLIN\_D1\_KE\_.V1\_DN  
LEF1\_UP.V1\_DN, LEF1\_UP.V1\_DN  
EIF4E\_UP, EIF4E\_UP  
CSR\_EARLY\_UP.V1\_DN, CSR\_EARLY\_UP.V1\_DN  
MYC\_UP.V1\_DN, MYC\_UP.V1\_DN  
IL2\_UP.V1\_UP, IL2\_UP.V1\_UP  
CSR\_LATE\_UP.V1\_DN, CSR\_LATE\_UP.V1\_DN  
RB\_DN.V1\_DN, RB\_DN.V1\_DN  
ESC\_J1\_UP\_LATE.V1\_DN, ESC\_J1\_UP\_LATE.V1\_DN  
CRX\_NRL\_DN.V1\_UP, CRX\_NRL\_DN.V1\_UP  
CYCLIN\_D1\_UP.V1\_DN, CYCLIN\_D1\_UP.V1\_DN  
CRX\_DN.V1\_UP, CRX\_DN.V1\_UP  
SRC\_UP.V1\_UP, SRC\_UP.V1\_UP  
ESC\_V6.5\_UP\_LATE.V1\_DN, ESC\_V6.5\_UP\_LATE.V1\_DN  
NRL\_DN.V1\_UP, NRL\_DN.V1\_UP  
E2F3\_UP.V1\_UP, E2F3\_UP.V1\_UP  
IL15\_UP.V1\_UP, IL15\_UP.V1\_UP