


**W\_CXCR5NEG\_CD4\_EFF\_TCELL\_D6\_LCMV\_DN, GSE43863\_NAIVE\_VS\_LY6C\_LOW\_CXCR5NEG\_CD4\_EFF\_TCELL\_D6\_LCMV\_DN**  GSE43863\_NAIVE\_VS\_LY6C\_INT\_CXCR5POS\_CD4\_EFF\_TCELL\_D6\_LCMV\_DN, GSE43863\_NAIVE\_VS\_LY6C\_INT\_CXCR5POS\_CD4\_E  
GSE43863\_NAIVE\_VS\_MEMORY\_TH1\_CD4\_TCELL\_D150\_LCMV\_DN, GSE43863\_NAIVE\_VS\_MEMORY\_TH1\_CD4\_TCELL\_D150\_LCM