✓ GSE7852_THYMUS_VS_FAT_TCONV_UP, GSE7852_THYMUS_VS_FAT_TCONV_UP $"RAF6KO_DAY10_EFF_CD8_TCELL_UP, GSE15750_WT_VS_TRAF6KO_DAY10_EFF_CD8_TCELL_UP <math>\buildrel = 0$ GSE15750_WT_VS_TRAF6KO_DAY6_EFF_CD8_TCELL_UP, GSE15750_WT_VS_TRAF6KO_DAY6_EF