Chapter 7, Solution 76.

The schematic is shown below. For the pulse, we use IPWL and enter the corresponding values as attributes as shown. By selecting $\frac{\text{Analysis/Setup/Transient}}{\text{Setup/Transient}}$, we let Print Step = 25 ms and Final Step = 2 s since the width of the input pulse is 1 s. After saving and simulating the circuit, we select $\frac{\text{Trace/Add}}{\text{Add}}$ and display -V(C1:2). The plot of V(t) is shown below.



