Chapter 5, Problem 83

Design a six-bit digital-to-analog converter.
(a) If $|V_o| = 1.1875$ V is desired, what should $[V_1V_2V_3V_4V_5V_6]$ be?
(b) Calculate $|V_o|$ if $[V_1V_2V_3V_4V_5V_6] = [011011]$.
(c) What is the maximum value $|V_o|$ can assume?