## SMX PolyMorph Engine 2.0 Vertex Fetch Tessellator Viewport Transform Attribute Setup **Stream Output Instruction Cache** Warp Scheduler Warp Scheduler Warp Scheduler Warp Scheduler Dispatch Unit Register File (65,536 x 32-bit) 1 + 1 + 1 Ŧ LD/ST SFU Core Core Core LD/ST SFU Core Core Core Core Core Core Core Core LD/ST SFU Core Core Core Core Core Core LD/ST SFU Core Core LD/ST SFU LD/ST SFU Core LD/ST SFU Core Core Core LD/ST SFU Core Core LD/ST SFU LD/ST SFU Core LD/ST SFU LD/ST SFU Core LD/ST SFU Core Core Core Core Core LD/ST SFU Core Core Core Core LD/ST SFU Core Core Core Core Core LD/ST SFU Core Core Core LD/ST SFU Core Core Core Core Core Core LD/ST SFU Core LD/ST SFU Core Core Core Core LD/ST SFU

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