

## **Block Memory Generator v6.3**

DS512 January 18, 2012

**Product Specification** 

An updated version of DS512 is available on the core's documentation page.

© Copyright 2012 Xilinx, Inc. XILINX, the Xilinx logo, ISE, Kintex, Spartan, Virtex, and other designated brands included herein are trademarks of Xilinx in the United States and other countries. All other trademarks are the property of their respective owners.