Special purpose Devices

Zener diode: A special purpose, PN junction semiconductor device, which is designed to operate in reverse breakdown region is said to be Zener diode.

-> Zener diode is heavily doped when compared with PN junction diode.

Symbol: Anode callede.

-> electric current in Zener diode flows from

V-I Characteristics of Zener deode ?

The Lifeward (correct)

Peren diode

Functions like a

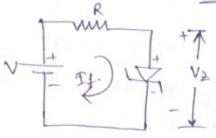
PN junction diode

PN junction diode

Vy (toward the polyment to the polyment to

(Revert # The voltage point at which voltage dry Zener current) across zener diode is const of there is rise breakdown in current is called "Zener voltage V2".

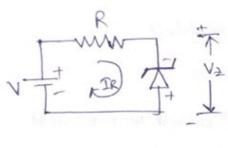
case (1): forward biaring of Zener diode:



V2 by connecting anode of Zenez diode to (tve) terminal of battery and cathode to (ve) terminal.

> Followerd biased zener diode behaves like followerd biased PN junction diode.

case(2): Reverse biased zener diode:



V2 by connecting Anode to Eve)

terminal & cathode to Eve) terminal

g battery.

→ When reverse n voltage is applied to zener diode, only a small amount of current flows through diode, until the zetere voltage (V2).

- -> When revouse bias voltage reaches to zener voltage, then it allows large amount of current to flow through it.
- → At this point, small variation in sieverse bias voltage causes large amor sudden increase in Current
 - A breakdown Known as "Zener breakdown"

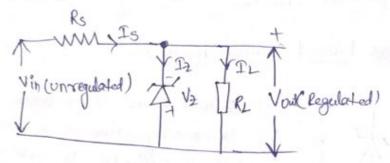
 occurs as a result of this rudden increase in

 Current. The reverse current must be in blw Iz(min) & Iz(mon)
 to operate zener diode in breakdown Region.

Zener diode as a voHage Regulator:

Unitage Regulation: The process of keeping constant
Wo Hage across the load regardless of variation of "IP Wo Hage
(Or) Load current is called " wo Hage regulation!"

-> Zever diode has the ability to maintain constant voltage across load.



- → Zener diode is always revenue biased w.r. + input
- -> The Resistance Rs (Series resistance) current limiting resists) is used to limit reverse current through Zener dio de.
- > Vin & Rs are selected in such a way that diode should oposale in "boundedown region".
- \rightarrow voltage across diede in breakdown region is called as 2 ever voltage V_2 , and V_2 = Voit
- > if Vout < V2 then diade doesn't condect and hence IL = Is. (I2=0)
- \rightarrow if $V_0 \ge V_2$ then diode starts conducting $I_3 = I_2 + I_L$.

> zenez diode as voltage regulator is explained in tollowing two cases. 1) Regulation with varying input vortage Vin (2) Regulation with vacying load resistance RL Case (1): Regulation with varying input voltage Vin. -> As ilp voltage increases. ilp current Is also increases. -> This increases the current through zener diade. Iz without Offeeling IL, There by keeping Vout constant. -> As ilp voltage decreases, ilp wment Is also decreases. -> This decreases the Iz without affecting IL, there by keeping Vout Constant. case(2): Regulation with varying load resistance RL. Is= Iz+IL rough. 5=3+2 rough. 明でいる 5= 4+1 -> if R_ increases then I_ decreases, This increases Iz, as a result the ilp current Is Demains constant, Thereby Keeping Vout constant. If RI decreases then I increases, > This decreases I2, as a result the IP current Is enemains constant, Thereby Keeping Vout Constant.

SCR: Csilicon Controlled Redificer)

-> SCR belongs to Thyristor family.

Definition: A silicon semiconductor device with 3 junctions, which can convert Ac signal into pulsating DC signal, and can controll the amount of signal to be an converted is known as "SCR".

Construction & symbol:

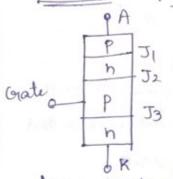


fig: Constructional structure

→ 9t is Constructed ving two P-type & low n-type
Semiconductors, which are arranged as shown in above fig

2222777777777777

As there are 4 layers of semiconductors, 3 junctions named as J1, J2, J3 are formed, and there exist 3 terminals named as Anode, cathode, copte.

Walking principle & V-I characteristics:

-> There are 3 modes of operations for scp

- 1. Reverse blocking mode.
- a. forward blocking made
- 3. forward Conduction mode.

DReverse blocking mode: scr is operated in Reverse blocking by providing negative voltage across Anode & Cathodi Revenue break down rrrrrrrrr J2 Grate J3 -> 9+ is observed that, in this mode It is Reverse based, Jz is forward biased, J3 is Reverse biased. -> As JI & J3 are reverse biased, there exist a small leakage @ Current called " reverse leakage current." If (-Ve) voltage is further increased then I & J3 junction breakdown and hence current increases rapidly. The Eve) voltage at which J. & J3 junctions breakdown and current rises rapidly is known as " Reverse breakdow. voltage (VBR) . -> 9+ is considered as OFF mode of SCR. Follward blocking mode: SCR is operated in follward blocking mode by providing positive voltage across anade of cathode. -> J1, J3 are followered biased and Is is steverse biased. + As JI, Ja are followed to biased there exist a small amount of Current to called "forward leakage Current".

-> This mode is considered as OFF mode of SCR

3 Forward conduction mode: ock can be operated in forward conduction mode by following any of these two ways

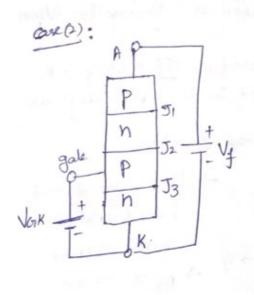
(ase () () By increasing forward with age Vy till forward breakouse woltage (VBO).

enter (2) By proving positive contage blw gate & cathode by entaining Vy.

Case(1):

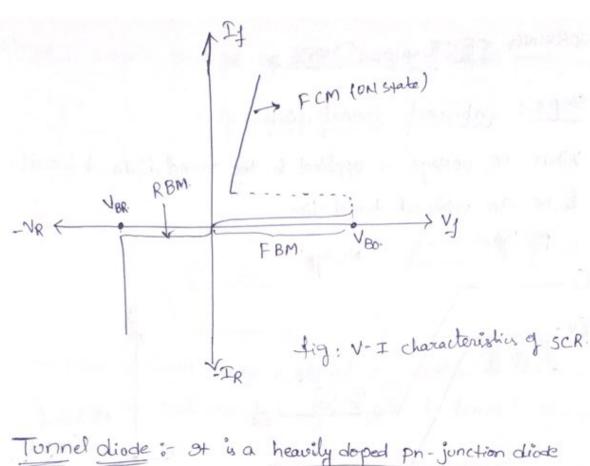
Jaward breakdown over wollage: The forward wollage Vy at which J2 junction bereaksdown and SCR changes its state from off to ON is called "faward breakover (VBO) wollage".

-> At forward breakever wo Hage SCR goes from forward blocking made to forward conduction made.



→ By applying (tve) Voix along with (tve) Vf, all junctions J1, J2, J3 are followed.

-> Hence ack turns on and current flows linearly.



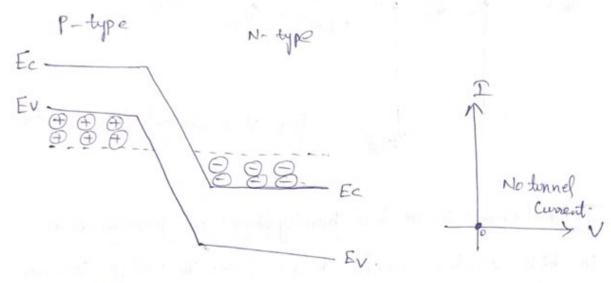
Turnel diode: - 9t is a heavily doped pn-junction diode in which electric current decreases as the voltage increases. i.e it exhibits "negative Resistance" in a certain Region when it is forward biased.

- -> A Lunnel died is also known as "ESAKI DIODE"
- -> Transfel diate have an PN Duration Symbol: A DK.
- -> The heavily doped PN junction is almost 1000 time greater than in wound diade.
 - → Tunnel diose is used as a very fast switching device in tunneling = freet(∞) Tunneling:
 - → As tonnel diode is heavily doped, deplatin layer width is of very somall. Thus conduction band of n-type & valence band of p-type overlaps. As a result for a small applies UD Hage is of n-type conduction band funnel to p-type valence beard. This is called "Tunneling."

WORKING OF TUNNEL DIODES

Step 1: unbiased tunnel diode:

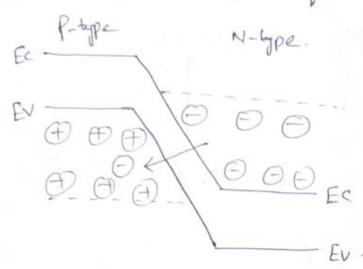
When no voltage is applied to the tunnel diode, it is said to be an unbiased tunnel diode.

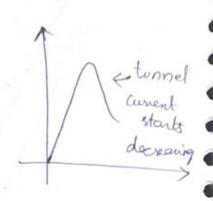


- → In Junnel diode, the conduction band of n-type so overlaps with valence band of p-type so due to heavy doping.
- >> Because of this everlapping the Conduction band electrons at n-ride and valence band holes at p-ride are nearly at the same energy level
- → 50 some es tunnel from Conduction band of n-side to valence band of p-side. Illy holes tunnel from valence band of p-side to conduction band of n-side.
- no: of es & holes flow in opposite direction.

Stepz: small voltage applied to tunnel diode. P-type N-type - A small tunnel -> When a small voltage is applied to tunnel diode, a sma no of es in conduction band of n-type will turned to valence band in P-type -> Hence a small amount of Lunnel current starts flowing Step3: Applied voltage is slightly inveased 000 000 Ec -> Ohen applied wo Hoge is increased, a large no: of free electrons at n-side conduction band tunnel to vale, bond in p-type. As a result maximum tunnel curre, energy well of n-xide conduction band =

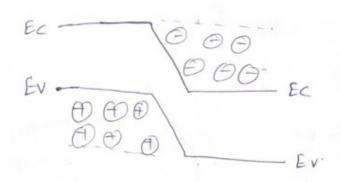
Step4: Applied voltage is further increased.





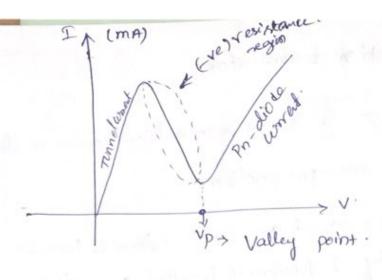
- -> If the applied voltage is further increased, a slight misalignment of conduction band and valence band takes place.
- Volence band. Thus, the turneling awaent starts decreasing.

Steps: Applied voltage is layely increased.





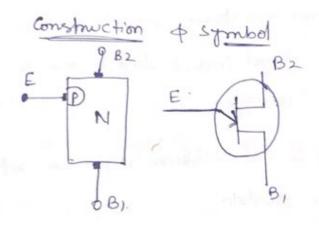
- → It applied vortage is largely increased then turneling current drops to zero.
- At this point, the conduction bound & valence being no longer overlap and & Lunnel diode operates in same as p-n diode.



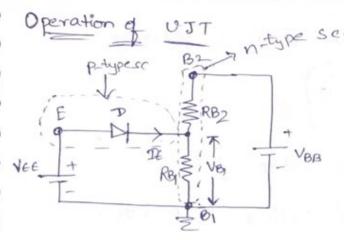
- → The region in which current decreases with invease in VOHage is called " (-ve) resistance region".
- > The voltage at which tunnel current drops to zero is called "valley point" voltage.
- as an amplifier or) an oscillator.

UJT: uni junction transistor

- Despinition: 9t is a 3 terminal semiconductor device, which as only one pn-junction.
 - * 3 terminals are named as Emitter, base 1, base 2.
 - * As it has only I junction, it is called as "unijunction transistor"



- > UJT is constructed with a bare restricte of n-type semiconductor, into which heavily desped P-type SC is diffused closed to base 2.
- Amow mark on Emitter turning indicates that, maximum amount of Current flows from emitter towards based terminal.



terminal open.

VBI SIEVENSE bias the diode then IE=0.

-> But due to minority carriers. Here is a small leaker, Current flowing from B2 to E.

case(1): When a (+ve) Vec is applied to Emitter:

To Vec is applied in such away that, it should

be more than cut-in wortage of diode as well

as wortage across RB, (VBI). Then diode D

is followed biased.

- -> When diede is forward biased, emitter injects holes into n-type sc bar.
- These holes are repelled by B2 (as RB2 is provided a (+ve) woltage) and are attracted by B1. (as RB1 is provided with (-ve) woltage).
- Hence there exist large amount of Current flow from Emitter towards base 1 (Bi). Os a result RBI reduces,
- -> UJT is limited by Ve

* y > intrinsic stand-off ration

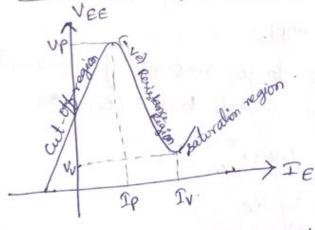
$$\frac{\gamma}{R} = \frac{RB_1}{RB_1 + RB_2} \approx 0.5 \pm 0.75$$

Ves = Vs1

case(3): Inthen (-ve) voltage is applied to Emitter.

Diode is reverse biased and hence UTT is in eff Condition.

Characteristics of UJT:



Vy > valley point.

for VEE < VP. Emitter is revove biased and hence there will be small leakage current -> practically UTT is in OFF state. -> This region is landern as

Cut - of region.

(Loe) Resistance Region of

if Vee > VP, then diode is

forward biased.

It and

decreases RB1 there by

decreasing VEE.

If $\alpha + \beta = \beta$ Negative

resistance.

I them V=IR, if RI, then VI,

Roogh) so Ve Jies with I, in RBJ.

The Region in Which Ie in means

with decrease in Vee is called

-> The region after Valley

point is called saturation.

aturation Region

NP CN VIA.

region.

Varactor diade :

(-ve) Resistance Region

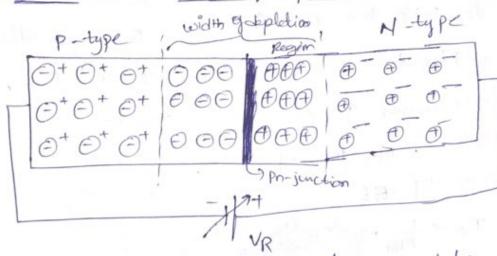
-> Variactor diode is originated from Variable capacitor

- The diode which operates only in stevense bias, and whose capacitance can varied w.r.t reverse bias voltage is called as "Varactor diade".
 - > 9+ is also called as Varicap diode, Tuning diode, Variable executance diode, Variable Capacitance diode.

-> symbol: Anode) 17 cathodo

Junction capacitance $C_j = C_0 \left[1 + \frac{V_R}{V_B}\right]^n = \left[1 + \frac{V_R}{V_B}\right]^n$ $C_0 \rightarrow capacitance$ at zero biaring. $V_R \rightarrow Revene bias voltage$ $C_j \propto V_R$ $V_B \rightarrow barrier voltage$

Wolking principle (00) principle of operation:

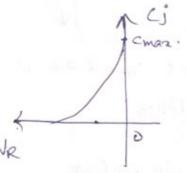


- Then reverse bias is applied to a diede, then holes from p-side & elections from n-side move away from the junction
- -> This process increases the width of the depletion region
- -> Depletion region acts as an insulator or dielectric material;
- -> Width Nepletion region is controlled by reverse bibs voltage
- -> Thus the diode is used as a saviate coHage variable capacitor.
 - -> WKT, junction capacitance

ej d 1 VR.

-> It sieven coltage increases then depletion region width increases which reduces the capacitance

-> Thus severe voltage must be maintained at minimum value to have maximum capacitance.



cmaz. -> Graph depicts that if Hereone woltage increases then junction capacitance decreases.

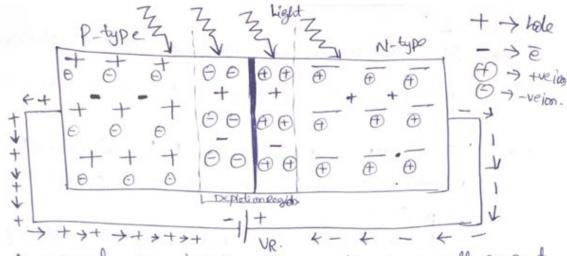
Photo diode . A pri priction semiconductor device that Consumer light energy to generate electric current is termed as " Photo diode".

-> 9+ is also called as photo detector, photo-server, light detector.

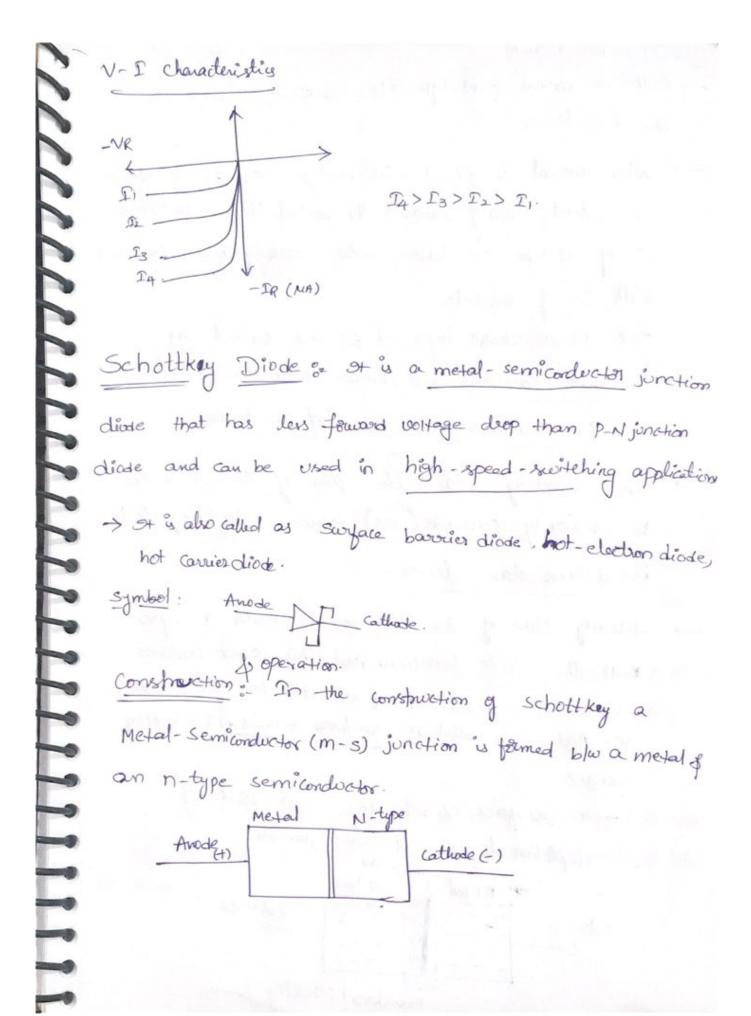
> Symbol: Audo XX Cathodo

> Amow marks represent light (or) photons.

Moking principle (00) principle of operation: -> photodiodes are specially designed to operate in



- A normal pri-junction diode allows small amount of current under reverse bias condition.
 - > To increase current in reverse bias Cordition, we need to generate more minority covorieus.
 - -> Applied reverse contage supply energy to minority change carriers but not increase the no: of minority change carriers.
 - ** To overcome this problem, we need to apply external energy directly to depletion region to generate more charge carriers
- ** Photo diode is designed to generate more not go Charge carriers in depletion region.
- In photo diodas, we use light on photons as external energy to generate charge carriers in deplotion region



Case(1): No biaring.

-> Both in metal & N-type so the majority change carriers are electrons

-> When metal is joined with N-type sc. is of N-type se start moving bounds the metal, this is because es of N-type so have more energy when compared with es of metal.

-> Thus these injected es are called as hot carriers and hence it is also called ey hot carrier diode (or) het es diode

-> In schottky diode the flow of current is due to majority carriers (es) hence schottky diode is a unipolar device.

-> + leavy flow of es into metal creates a region near the M-S junction. And this region contains -ve cons on metal side & more (+ve) cons on N-type sc. And it is called as surface barnies (or) schottky barrier

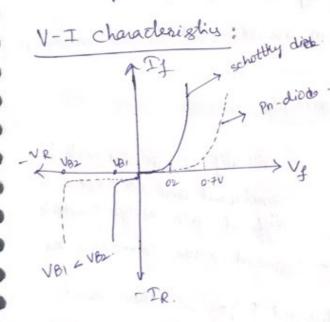
-> surface(or) schottky is a kind of depletion layer at m-s junction. + 7 kde. N-typesc Cathoda (> (ve) 100). is surface | schottky barrier.

→ schotlky/ rurface barries opposes the flow of es from N-type to metal.

-> so we need to provide biaring.

(ase (2); with biasing.

→ When forward biaring is applied by Connecting metal to (+ve) terminal of battery (+ve) terminal of battery then due to force of repulsion es of N-type sc cross the schottky barries and move into metal side. Thereby resulting large amount of coursent flow:



→ V-I characteristics of Schottky diode is almost similar to P-N junction

The forward voltage drop

(cut-in-voltage) of schottky

dio be is of the order of 0.2%

-> In schottky diode, the reverse saturation current occurs at very low voltage as compared to pre juntion diak

LED: Light Emitting Diode 9+ is a PN-junction diode which emits light when an electric Current passes through it in forward direction. -> - Amount of light Emitted of forward current > LED is made of Semiconductor material, and the light is emitted through the junction of the diode. -> semicorductor compound materials can be Grallium Arsenide (GraAS) Gallium phosphide (GraP).etc Symbol & Anode My ochhode silicon & Geremanium are not used in the Construction & LED. Construction & MORKing principle -> Initially 11-type substrate is Anode. Considered and a ptype sc is diffused onto N-type substrate diffused phype Tgold film (Cathode) -> metal anode connections are N-type startrate made at the outside of disbured P-type Sc. There metal

made at the outside of differed p-type SC.

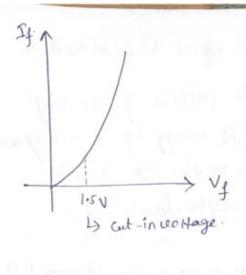
amode connections are made in such away that more central amode connections are made in such away that more central for the light to escape.

Surface is made available for the light to escape.

Surface is made available for the light to escape.

Surface is made available at bottom of substrate to applied at bottom of substrate to applied at lottom of substrate to applied at lottom of substrate to applied at lottom.

WORKINGT: LED must be followed biased to understand the walking principle. > The process of obtaining light energy by providing an electrical voltage is called " electro-luminisence" -> When forward voltage is applied, then forward current is -> This forward current is due to electrons. Produced. -> In LED, electrons of N-type are in higher Conduction. band to holes of p-type are in lower Valence band. ** When current flows through diode, then recombination of electrons & holes takes place i.e., electrons move from higher energy level to lower energy level and (Ex) === high energy leas releases the photons. ** There photons will have a wave length of energy VB ## difference [Ez-E] * The energy difference can be either in the form of heat (or) in the form of light



V-I characteristics

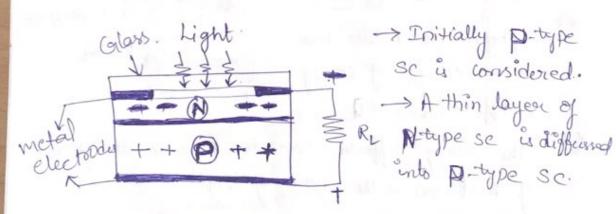
- → cut-in voHage of LED
- → if V4 > 1.5 v then LED orelease energy in the form g light.

Solar cell: et is a p-n junction device which converts solar ovenergy into electrical energy.

Photo-voltaic cell

or > 9+ works on the pormiple

Construction: called "photo-voltaic effect".



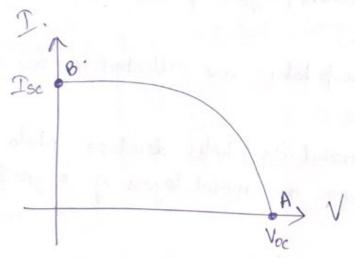
in such a way that; mile central space is made available for light to inject. I creas avoids mechanical shock

-> Another metal layer is developed at bottom of ptype -> metal layor at N-type sc acts as there · p-type " " Wilking: When light is incident on pn-junction then electron-hole pairs are generated. > due to junction field (05) electric field at junction hole more towards P-type & Es move toward N-type sc. -> There electrons & holes are collected at two side of junction. -> There photogenerated es & holes develope photo voltage at both the metal layous of p-type & N-type Sc. -> When external load is connected across metal -0 layous then photo current flows through at load. V-I characteristics -> V-I characteristics lies in 4th quadrant. -0 This is because solar cell doesn't draw current but supplies coment. -> A represents open circuit woltage --> B represents short cht current.

Photo-Woltaic effect: The effect, in which current/ 00 Hage is generated when exposed to light. Through This effect solar colls convert sunlight into electrical energy.

* each solar coll generally 0.5V of wortage.

* Solar pannel is the Combination of many
edar colls.



- → Voc. OoHage when no load is connected Current is minimum)
- -> Isc, Short cht current when load & connected connected connected maximum).

Device	Advantages	Disadvantages	'Applications.
Zener dide	→ High accuracy → Small &ize → low cost	→ lower regulation yodio → can't handle high-power	→ Wolfage Regulator → clipping & clamping chels → switching applications
LED A M	→ long life > Reliability → high intensi brightness.	> Temperature dependence > light qualify > voltage sensitivity	> motorcycles & coop y > home appliance -> traffic light signals.
Photo diede		→ less sonsition > temperature dependence → Active area in somall	-> medical appliation
TEV E	> low cost > < ve) revistance characteristis > low power absorber device	Gorige parties,	→ timing chls riale → relaxation oscillator → contage detector
SCR A DXX	> casy to turn > low cost > can handle la cottages, current	inge > low switching	speed Battery

			3
Device	, Advantages	Disadvantages	Applications
Varactor diode	→ less noise → light weight → Reliability	in forward bias	> Vo Hage controlled oscillators > RF filters > Frequency & Phase medulator
schottky. diode	→ low-tern on wo Hage → low-sunction capacitance → Low- wo Hage drop.	> Reverse leakage current > low reverse Voltage raling	
sdar cell	→ No pollution associated with it → No maintenan cost → long life		> Provides electricity for > waterpumps -> laptop -> completes
Tunnel dide.	→ low cost → Durable → suitable for high-freque speed applie	rcy, > 100- poros	cale > frequency

-

- Draw energy banddiagram of tunnel diode of explain tunneling phenomenon. Draw V-I characteristics. [OR]

 What is tunneling phenomena? Explain principle of with the characteristics on with operation of tunnel diode with its characteristics on with the hap of energy band diagram explain tunnel diode.
- Define Varactor diode ? Explain operation of Varactor diode with its circuit and mention applications
 - (3) How and under what conditions Zener breakdown take place in diode? Draw V-I characteristics of Zener diode and show the breakdown region.
 - @ Explain in detail about poninciple of operation of
 - (5) Eaplain the construction of characteristics of UJT.
 - (6) prove the statement "A zener diode can act as voltage regulator".
 - (F) Draw the symbols of all special purpose devices along with their definitions.

EDG- ASSIGNMENT-5

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