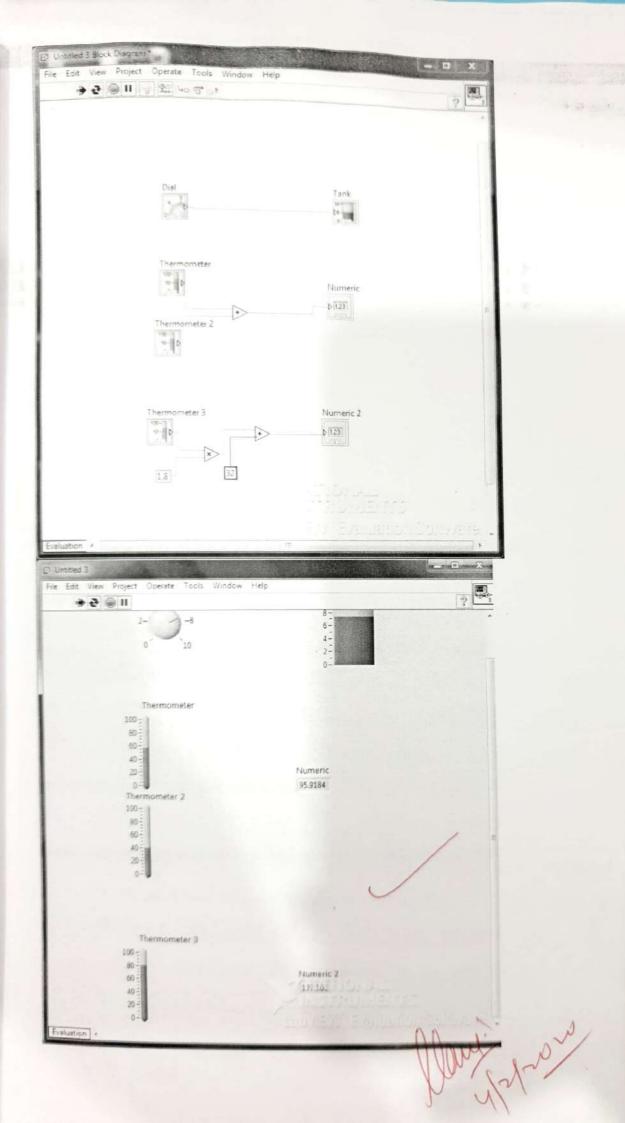
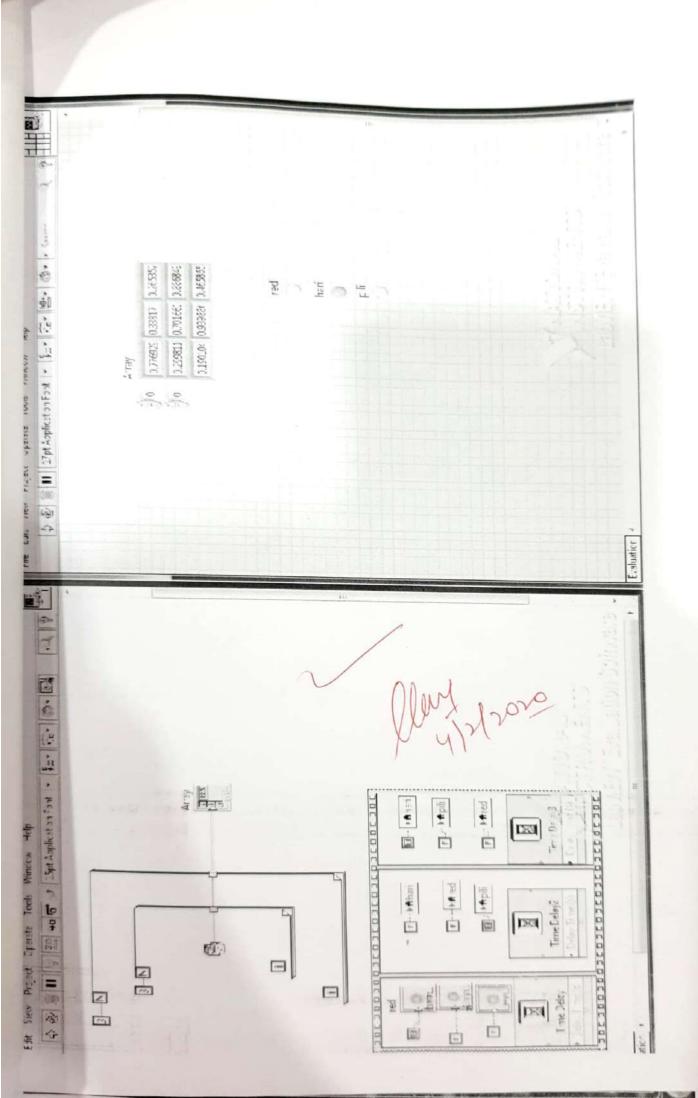
Experiment No-1 Duy: - Debugging a VI, sub VI's Software Used: - Lab View Theory: - Program subsoutines are termed as VIs
lack VI has 3 components:-2) Faout Panel 3) Connector Panel Front fanel: - It is made by using controls and widicators. Glock diagram includes objects such as terminals. Sub VI's functions, constant structures & weres which transfer data. while loop: - It is a control flow statement used to
execute a block diagram of the sub-diagram
code repeatedly until a given boolean condition
is not take DBL numeric constant le used to pass a deuble precision posting gourt numeric value to the block diagram. Conclusion: - Sub VI's were created for temperature blevel measurement & debugged the VI.



Experiment No-2 Sim: - Lab View - Traffic light + pragramming standure, Software used: Labriew Theory: - Traffic light: - It is an optical signaling deviles indicates different signal related to traffic, sail, so naval etc. It aims at teaffic Safety. Programming Stancture: - programs & can hun defending on the Value of Variables. Stancture can be found in Labview structure pallet. You need to draw the cases are for both true & Labre 1/2/1/11 Allays: - These functions are used to create & maniquelate allays. You can perform common deta element from an array, inserting individual of Replacing data elements. Clusters: They group data elements of mixed types eg: - revol cluster in Labbrein. Result: Teaffic lights using stauctures, clustered arrays were uniplemented successfully.

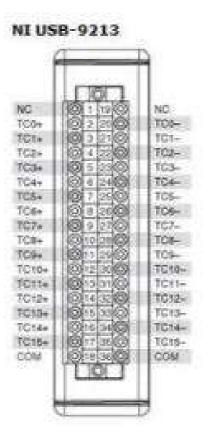


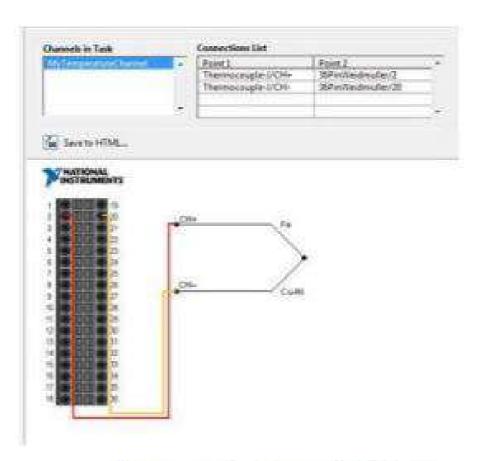
Experiment NO-3 Aus :- Labview - Warefory Measurement Software used: - Lab View 2018 Theory: - Express VIs
The express VI is a VI whose rettling you can configure interactively through (a) dialog box & Expless VIs appear on block diagsam as expandable nodes with none successfully by & blue field. A parameter can be configured and expanded from both locations. Front Panel: it is the user interface of the VI. Eløck Diagram: - It includes terminals purctions, constants, structures and wire which can transfer dota among block diagram objects. plots of evenly spaced measurements. The waveform Kesult: - Différent warreforms were studied and



Experiment NO-4 Sin :- Strain & Temperature measurement with NI DAD cards using Lab View. Software Used :- LabView Theory: - Temperature Measurement It is one of the most common types of physical measurements. Depending upon the desired acturery, large and expense, we have several sensors options for measuring terry. by using NI -USB -9213 Strain Measurement: Strain jange are fundamental sensing devices that function as the building blocks of many other types of transducers, including pressure, load & torque extensively in structural test & mondoring applications. Result: - We have successfully measured strain and temperature I with NI DAR cards weing LabView

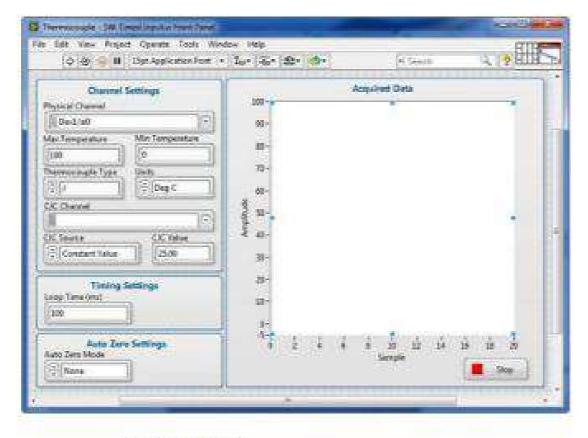
## A) Temperature Measurement using Thermocouple





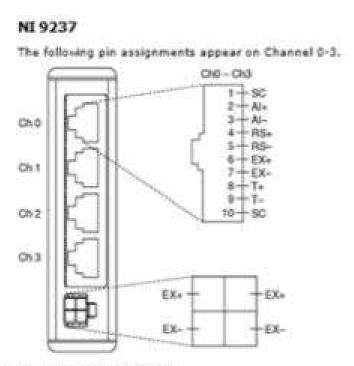
Pinout of NI - 9213

Thermocouple Connection Diagram

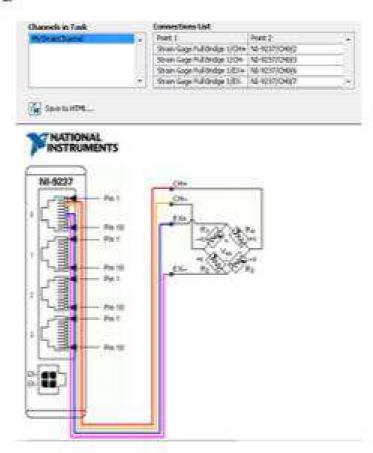


Front Panel

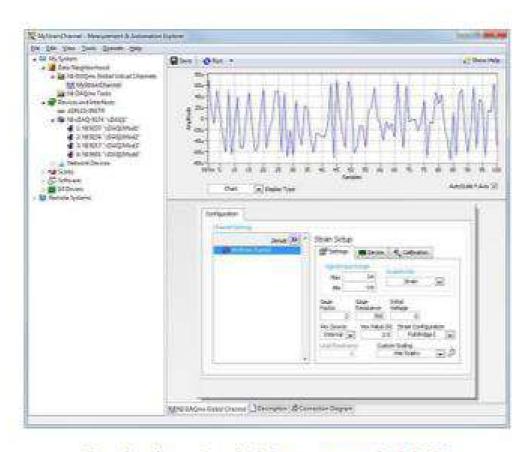
## B) Strain Measurement using Strain Gauge



Pinout of NI - 9237



Strain Connection Diagram

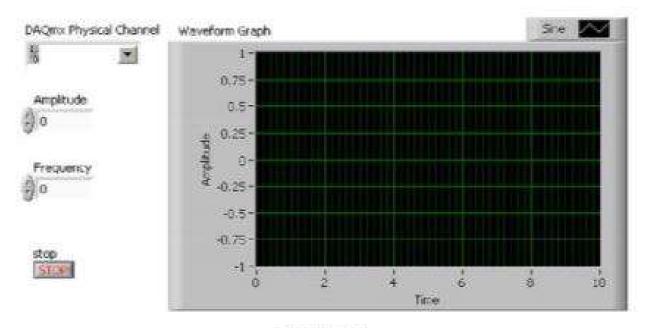


Previewing a Strain Measurement in MAX

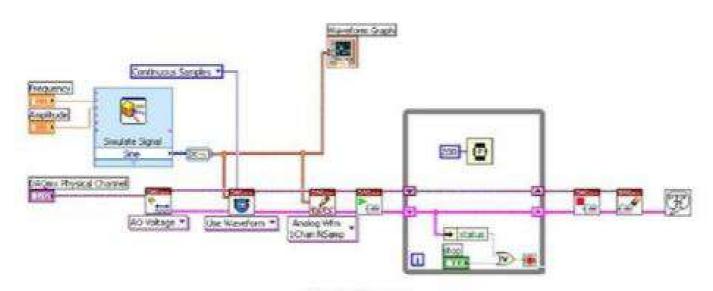
Experiment NO-5 Aun: - Signal Generation & frequency analysis by
NI - DA & cards. Software Used: \_ Lab View Theory: - NI - 9219 is designed for multipurpose
testing with the NI-9219 you can
measure signals from sensors such as
strain gange heristance, temperature
detectors, eté: Data acquisition is the
froces of measuring an electrical or physical fluenomeron as such voltage etc. A s/m consists of sensors, DAG measurements, hardware & a Conjutel with grogrammable software. Lugut: - Parametel

1) Cignals

2) error in Description contains the 1/9 x 0/9 signals describes the error conditions that occur before this node suns output: - Palameter 1) Amplitude 2) Frequency 3) Elson Out Rescription
amplitude of detected signal
frequency of detected signal
contains error information Result: - Signal Generation & Jacq. Analysis were done successfully



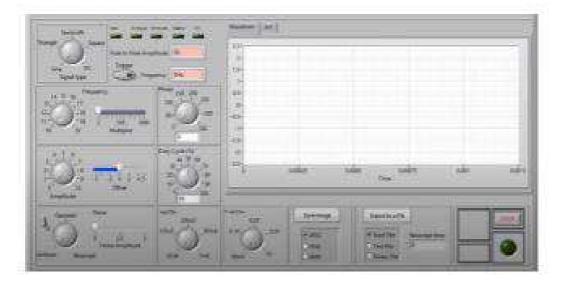
Front Panel



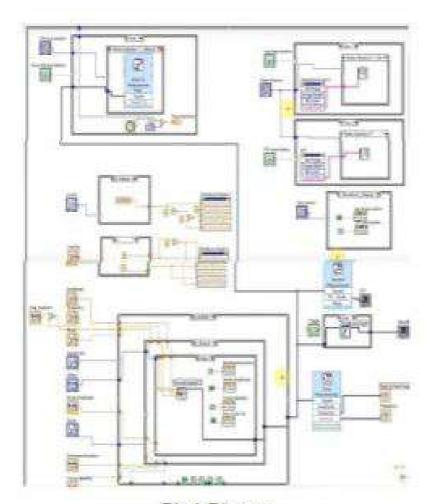
Block Diagram

Experiment NO: - 6 Dery:- Labriew - escelloscope + Attribute Nodes, Menus Software used: - Labriew 2018 Theory: - Oscilloscope also known as CRO or DSO is a type of electronic test equipment that graphically displays varying signal voltages usually as 20 plats of one or more signals as a functions of time. Focus Control: The Control adjust CRT focus to obtain the shortest, most detailed trace. Intersity Control: - It adjusts the brightness. Beau Funder: - modern oscillosroge have direct cougled direction amplifier which means trace can be detected off skeen. Glaticule: - A grid line that saves a reference marks for referrings the displayed trace. Tune base Control: - Selects the hoerzoutal speed of CRT's spet as it creates the trace.

Hold of control: - This sets the time after a trigger during which the surge circuit and be triggered again. Vertical position Control: - mones whole display
up and down. horizontal position Control: - moves whole display left and right. hesult: - Successfully generated function generator with oscilloscope in Labbrew.



Front Panel



Block Diagram

Experiment NO-7 Sur :- RC circuit and measuring time issues Software used :- LabView Theory: - For resistance measurement DAS card can be used for acquisition of resistance in ohm? The das card used is NT \$240. for a cafacitar cafacitance is measured by NT9283 given by

C= RIV where Q=charge f

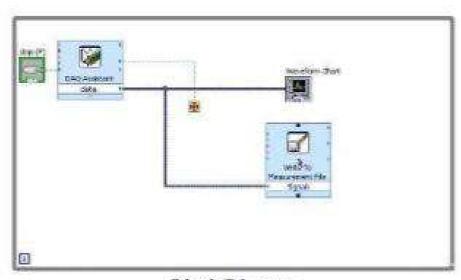
V = voltage Les Revistors & Cafacitors. A first order RC circuit ilusing of Presistol & I cafacitor The RC time Constant, z is equal to pardent of heristance & capacitance of is time required to charge capacitar through resistor from initial voltage to 63.24 of applied DC voltage. Vo = V(I) = Vo(1-e-1/2) Result: - RC went with its ting was sludged.



White Line: Step input with amplitude 5 - White Line

Red Line: RC Response

## Front Panel



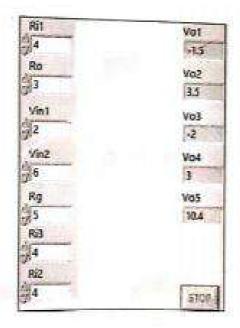
Block Diagram

Experiment NO-8 Ding: - Opany cieuret fits characteristice Software used: - Labriew Theory: - The formula Node is a convinent text based node, one can use to prefate mathematical operations on the block diagram formula nodes are useful for equations that have many Investing amplifiers: It is one of amplifiers in which the output is exactly 180° out of shase with respect to input output is an invested amplified velsion of input; gain of investing analytical is given as: 
Jann: = Av = -RP Non-Investing Amplifier :- It is the one in which output is in phase wat input.

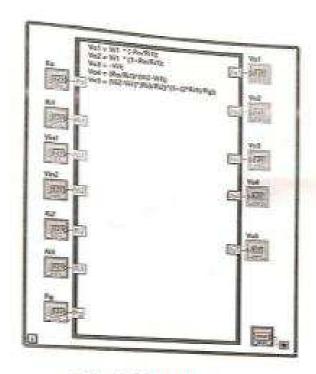
Spain => AV = 1 + Rt Veltage follower: - It is singly a circuit in which off follows input, means output voltage is same as that of right voltage

Defferential Amplifier:
gans = -Ro

RL Result: - Successfully implemented different



Front Panel



Block Diagram

Vol = Voltage output of Inverting Amplifier

Vo2 = Voltage output of Non-Inverting Amplifier

Vo3 = Voltage output of Voltage Follower

Vo4 = Voltage output of Differential Amplifier

Vo5 = Voltage output of Instrumentation Amphifier

Vi1, Vi2 = Input Voltages

Ro = Feedback Resistor

Rg = Gain Resistance