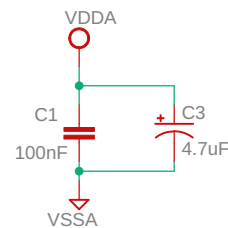
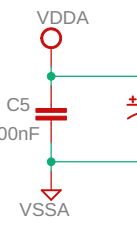


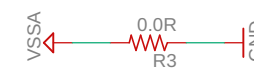
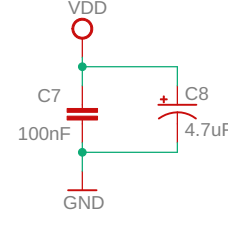
VDD/VDDA Coupling



Vref Coupling



VDDIO2 Coupling



Module H21R20

Title H21R20_Backend

Description

ESP32-C3 Wifi+BLE

Revision 0

Size Letter

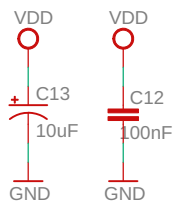
Author Ibrahim Zaeiter

Sheet 1 of 2

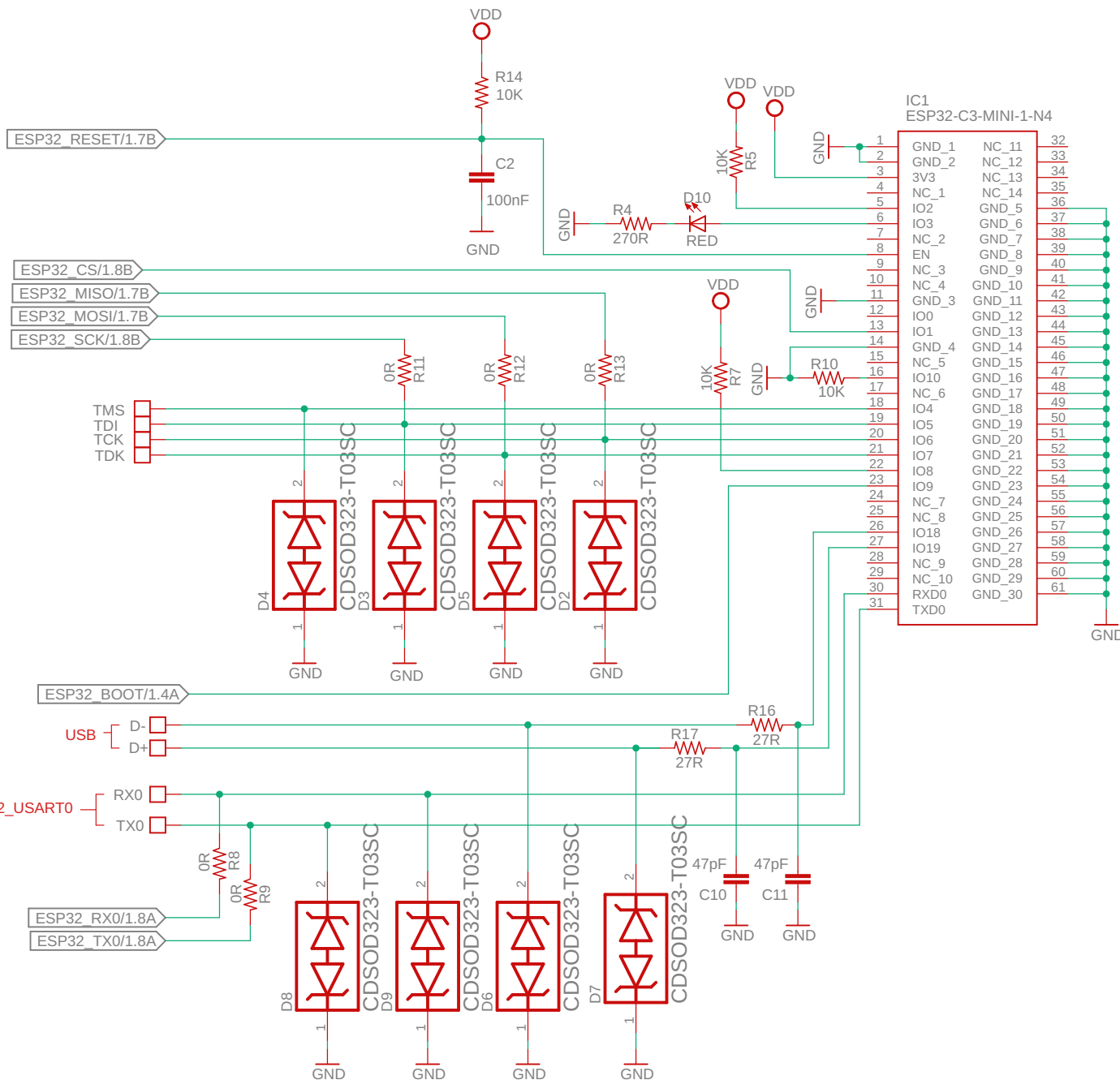


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VDD=3.3 Volts



To use external JTAG debugger
desolder R11 & R12 & R13



To use external FTDI
desolder R8 & R9

Module H21R20	Title H21R20_Frontend
Description ESP32-C3 Wifi+BLE	Revision 0
Author Ibrahim Zaeiter	Size Letter
	Sheet 2 of 2



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