# HD14066B

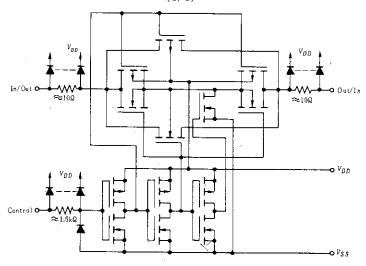
# Quadruple Analog Switch/Quadruple Multiplexer

The HD14066B consists of four independent switches capable of controlling either digital or analog signals. This quad bilateral switch is useful in signal gating, chopper, modulator, demodulator and CMOS logic implementation. The HD14066B is designed to be pin-for-pin compatible with the HD14016B, but has much lower ON resistance. Input voltage swings as large as the full supply voltage can be controlled via each independent control input.

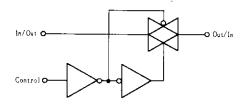
#### **FEATURES**

- High On/Off Output Voltage Ratio = 65dB typ.
- Quiescent Current = 0.5nA/pkg typ. @5V
- Low Crosstalk Between Switches = 50dB typ. @8MHz
- Supply Voltage Range = 3 to 18V
- Linearized Transfer Characteristics,  $\Delta R_{ON} < 60\Omega$  for Vin =  $V_{DD}$  to  $V_{SS}$  (at 15V)
- Pin-for-Pin Replacement for CD4016/66B and MC14016/66B

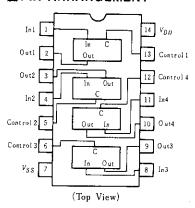
# ■CIRCUIT SCHEMATIC (1/4)



#### **■ LOGIC DIAGRAM** (1/4)



# **PIN ARRANGEMENT**



#### **■TRUTH TABLE**

Control	Switch
0	OFF
1	ON

 $V_{SS} \le V_{in} \le V_{DD}$   $V_{SS} \le V_{out} \le V_{DD}$ 

Vcontrol	Via to Vout Resistance
$V_{ss}$	>10° Ω typ
$V_{DD}$	3×10 <sup>2</sup> Ω typ

# ■ ELECTRICAL CHARACTERISTICS

Characteristic		Symbol		Test Conditions		-40°C		<b>25</b> °C			85°C		17.74
		Symbol	$V_{DD}(V)$			min	max	min	typ	max	min	max	Unit.
		V <sub>IL</sub>	5.0	$R_L=10 \text{ k}\Omega$ SW入力= $V_{DD}$	$V_o = 0.5 \text{ V}$	_	1.5	_	2.25	1.5	_	1.5	v
			10		$V_0 = 1.0 \text{V}$	_	3.0	_	4.50	3.0	_	3.0	
Input Voltage	15		$V_0 = 1.5 \text{V}$		-	3.75		6.75	3.75		3.75		
		5.0	D 101 O	$V_0 = 1.0 \text{V}$	3.5		3.5	2.75	_	3.5	_		
		VIH	10	$R_L = 10 \mathrm{k}\Omega$ SW入力 = $V_{DD}$	$V_o = 1.0 \text{V}$	7.0		7.0	5.50		7.0		v
			15		V <sub>o</sub> -1.5V	11.25		11.25	8.25	-	11.25		
Input Cur	rrent	I.,	15			_	±0.3	_	±0.00001	±0.3		±1.0	μA
Input	Control			1/ 0		_	_	_	5.0		_		1-
Capacitance	Switch Input	Cin		$V_{i,n}=0$	/.,=U		_		8.0		-	_	pF
Output Ca	apacitance	C	10			_	_		8.0	_	_	_	pF
Feedthrou	ugh Capacitance	Cincout	10			-	-	_	0.5		_	-	рF
Quiescent Current		5.0	7 Cianal		i -	1.0	_	0.0005	1.0	_	7.5		
	Current	$I_{Q}$	10	Zero Signal per Package		_	2.0	-	0.0010	2.0		15	μA
			15	рег гаскаде		_	4.0	_	0.0015	4.0	-	30	
		Ron	5.0				880	_	250	1050	_	1200	
ON Resistance	10				_	450	_	120	500	_	520	Ω	
	15				_	250	_	80	280		300		
A ON D		△Ron	5.0			_	-	_	25	_	_	-	
△ON Resistance Between	10				-	-	-	10	_	_	_	Ω	
Any Two Channels			15			_	_	-	5.0		_		`
Input/Outpu	t Leakage Current		15			<u> </u>	±300	_	±0.01	±300		±1000	πA

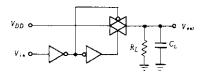
# **E**SWITCHING CHARACTERISTICS ( $C_L = 50 \mathrm{pF}$ , $Ta = 25 ^{\circ}\mathrm{C}$ )

Characteristic	Symbol	$V_{DD}(V)$	Test Conditions	typ	max	Unit
Propagation Delay Time	tрLH,	5.0		20	45	ns
		10	$R_L = 10 \mathrm{k}\Omega,  V_{SS} = 0$	10	30	
	t PH L	15		7.0	20	
	thz	5.0		35	100	ns
		10		30	90	
Output Disable Time		15	$R_L = 1 k\Omega \cdot V_{SS} = 0$	25	75	
	t LZ	5.0	$R_L = 1812, V_{SS} = 0$	30	90	ns
		10		25	75	
		15		20	60	
Output Enable Time	ŧzн	5.0		60	180	ns
		10		20	60	
		15		15	45	
	121	5.0	$R_L = 1 k\Omega, V_{SS} = 0$	60	180	
		10	ļ ;	16	50	
	<u> </u>  -	15		14	40	
Sine Wave(Distortion) $(V_{SS} = -5V)$		5.0	$V_{in} = 1.77 \text{ V}, R_{i} = 10 \text{ k}\Omega, f = 1 \text{ kHz}$	0.1	_	. %
Bandwidth(Switch ON) (Vss=-5V)		5.0	$R_L = 1 \text{k}\Omega$ , $20 \log_{10} \frac{V}{V} = -3 \text{dB}$	65	_	MHz
Feedthrough(Switch OFF) $(V_{ss} = -5V)$		5.0	$R_{\perp} = 1 \mathrm{k}\Omega$ , $20 \mathrm{log_{10}} \frac{V}{V} = -50 \mathrm{dB}$	1.0	-	MHz
Crosstalk(Switch A ON, Switch B OFF)(Vss = -5V		5.0	$R_L = 1 \text{k}\Omega$ , $20 \log_{10} \frac{V_{\text{co}}(B)}{V_{\text{co}}(A)} = -50 \text{dB}$	8.0	_	MHz
Crosstalk (Control Input-Signal Output) $(V_{SS} = -5V)$		5.0		50	_	m V
Maximum Control Freguency	-	5.0		6.0	<u> </u>	MHz
		10	$\begin{cases} V_{SS} = 0, \\ 20 \log_{10} \frac{V_{SM}}{V_{CS}} = -6 dB \end{cases}$	8.0	-	
		15	ZUlogio V. = -bdB	8.5	-	1



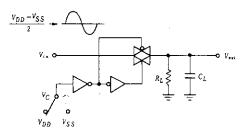
# ■ DC CHARACTERISTIC TEST CIRCUIT

# 1. Input Voltage

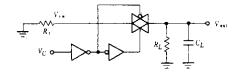


#### 3. Bandwidth, Feedthrough

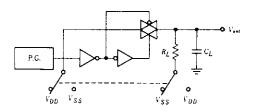
 $V_C = V_{PD}$  for Bandwidth Test  $V_C = V_{SS}$  for Feedthrough Test



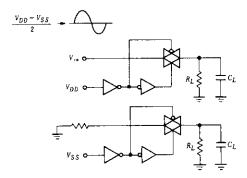
# 5. Crosstalk



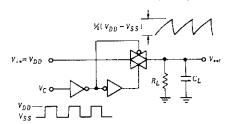
# 2. Propagation Delay Time



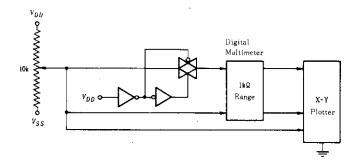
# 4. Crosstalk



# 6. Maximum Control Frequency



# 7. ON Resistance



Unit: mm



Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

Unit: mm

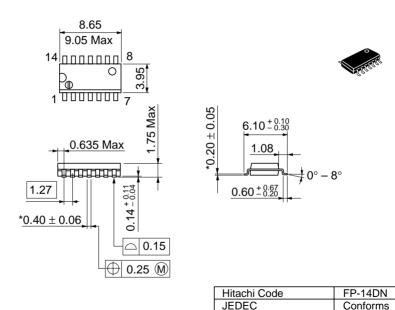


Weight (reference value)

0.23 g

\*Dimension including the plating thickness
Base material dimension

Unit: mm



EIAJ

Weight (reference value)

Conforms

0.13 g

\*Pd plating

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