HyperDbg



ACM Conference on Computer and Communications Security (CCS)
November 2022













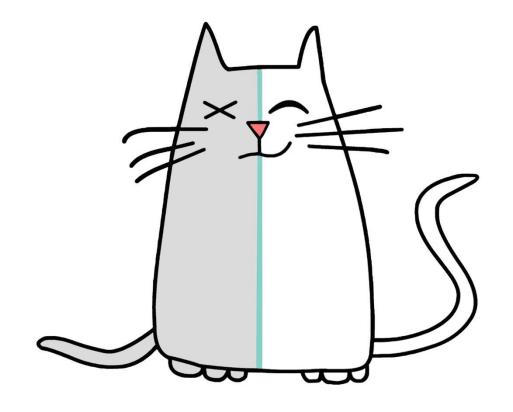
It's a

Transparent

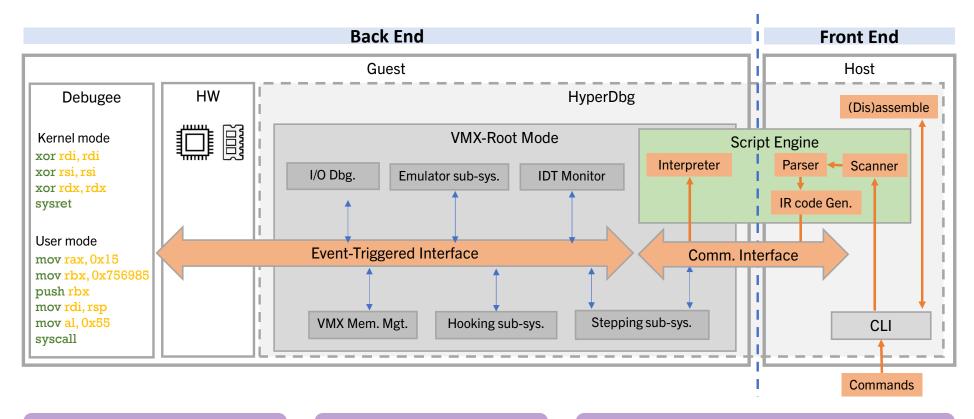
High-Performance

Hypervisor-Assisted

Debugger



	User Mode Debuggers	Kernel Mode Debuggers	Other sub-kernel debugging solutions	HyperDbg
Privilege	Ring > 0	Ring 0	Ring < 0	Ring -1
Transparency	Low	Medium	High	High
Performance	Medium	Medium	Low	High
Practicality	High	High	Low	High
	x64dbg CollyDbg Immunity Debugger	WinDbg GDB	Malt Ether	HyperDbg

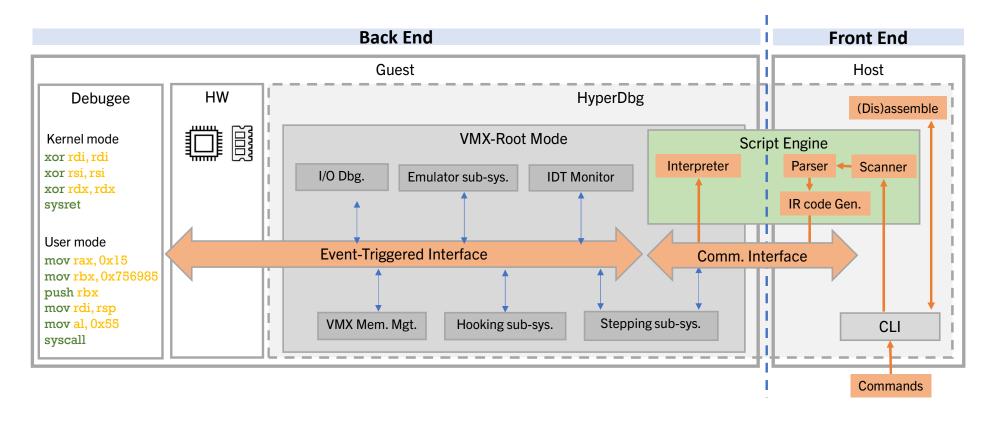


Event: An incident of interest Syscall, specific memory access, ...

Condition: Logical expressions constraining event triggers

Action: A set of functionalities following an event Break, script (view/modify memory, registers), log,...

Script Engine on backend C-like: if, else, for, printf



Performance

CPU Features (e.g., VT-x, EPT)
Script Engine
Efficient low-level implementation

PrivilegeVMX-Root Mode

TransparencyStealth in design

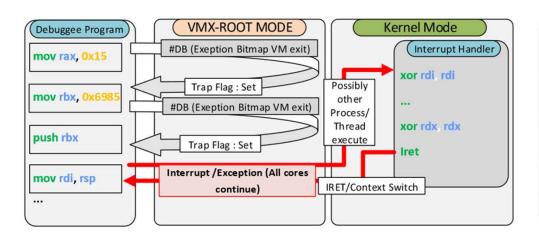
Stepping Subsystem

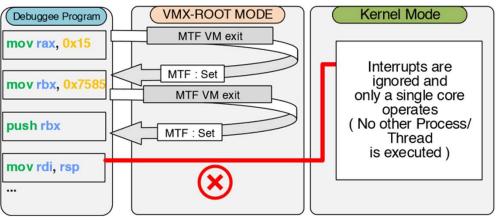


Guaranteed line by line instrumentation

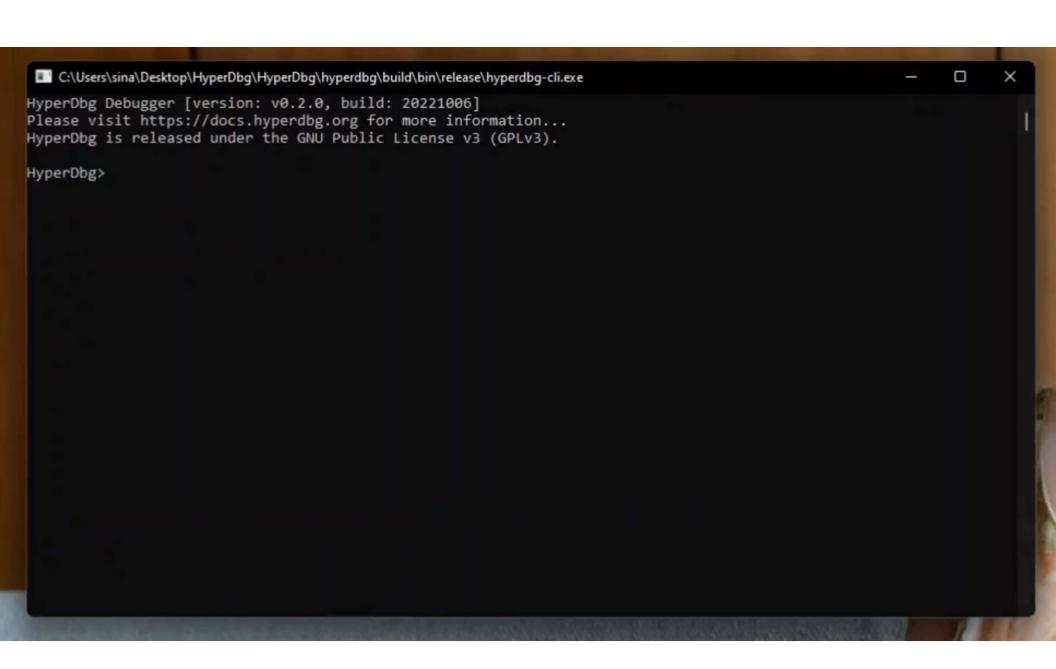


VMX-Enable Single Core execution /Interrupt Masking





MTF+VM exit (and NMI/PIN Based VM EX Control)



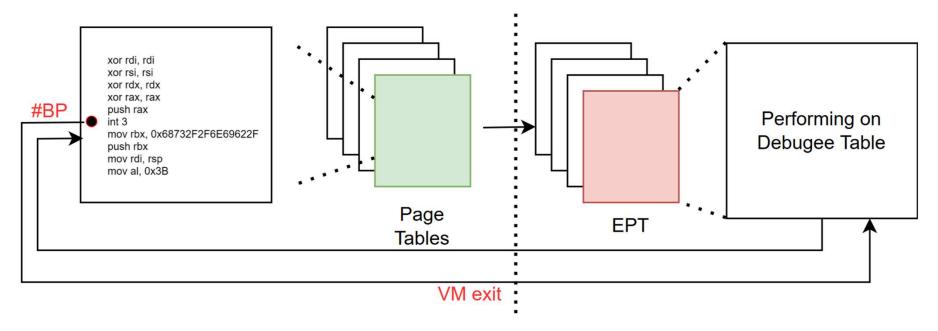
Hooking Subsystem



Transparent Hooking



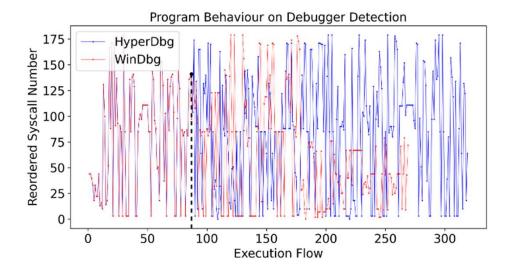
EPT-Hook and Debug Register Emulation



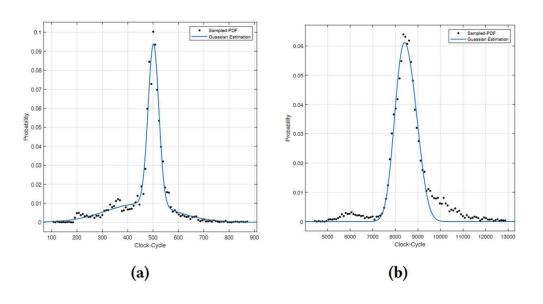
Transparency

Table 1: Anti-Debugging and Anti-VM exercises and mitigation in HyperDbg

Cat.	Methodology Example of the Meth.		Example	Mitigation in HyperDbg
Methods	API-Call (System-Call)	GetCurrentProcessId() CreateToolhelp32Snap() Process32Next() NtQueryInfor.Proc.() FindWindow()	[9, 56]	Modify results via EPT-Hook (hiding process)
inting	PEB Field	IsDebuggerPresent() NtGlobalFlags()	[53]	HyperDbg is not detectable by default
ngerpr	Heap Structure	HEAP.Flags HEAP.ForceFlags	[44]	HyperDbg is not detectable by default
and Fi	#BP Detection	Find BP (0xCC) inst. Read DR (Debug Register)	[57]	dr to modify and disable unwanted BPs
Anti-Debugging and Fingerprinting Methods	Timing Measurement	GetTickCount(), QueryPerf.Counter, GetLocalTime()	[58]	EPT-Hook Modification of results
Anti-De	Trap-Interrupt	Instruction Prefix, INT 3, 0x2D, Interrupt 0x41	[52]	Set Exception bitmap in VMCS
	Control Flow Manupulation	NtSuspendThread(), NtSetInf.Thread(), CreateThread()	[54]	HyperDbg not detectable by default
ition	CPU Instructions	CPUID forces a VM-exit certain info in VM	[61]	VM-exit (CPUID result modification)
Anti-VM/Hypervisor/Emulation	Protection Model Instructions	SIDT, SLDT, SGDT STR, SMSW	[9]	VM-exit (emulation and modification)
rvisor	Architectural Delta-Timing	RDTSC+CPUID+RDTSC RDTSC(P)+RDTSC(P)	[61]	HyperDbg Trans. Mode (!hide command)
(/Нуре	In/Out Instructions	Magic I/O port in VMware	[24]	VM-exit handled (I/O bitmap)
ıti-VM	Invalid MSR Access	Invalid MSR issues General Protection (#GP)	[95]	Emulate !msrread/!msrwrite command
An	Exception Handling	Try-Catch General Protection Excep. (#GP)	[55]	Handled by default Inject routine into user-mode



Timing Transparency



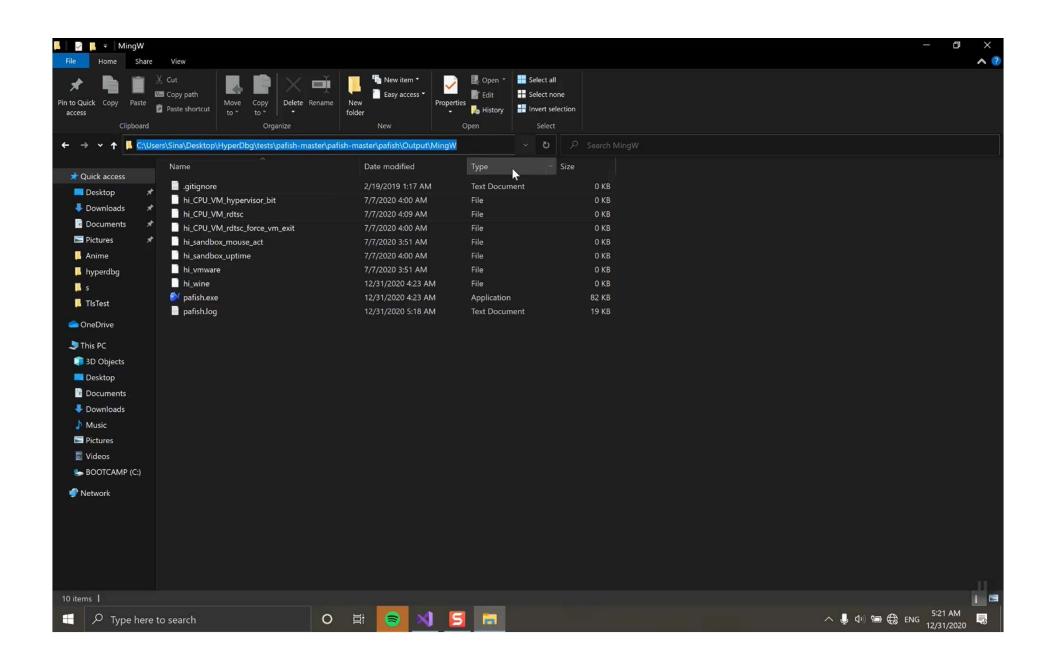
HyperDbg: Off HyperDbg: On Transparency Mode: Emulate **Before Emulation** !hide != cpuid/rdtscp !measure Return !measure default **Get Target** Normal PID/Pname Time (Debugger) Force vm-exit 2nd rdtscp **Store Time** Measuring Do Not (RDTSCP) (rdtscp/vmexit/...) Emulate Return **Get Current** Thread ID Cur_TID != Pre_TID **Get Statistical** Reset **Parameters** Emulate via State (Av,SD) ET (restore Return rax/rdx/rcx)

Figure 5: PDF distribution of timing measurement for deactivated HyperDbG (a), with activated HyperDbG (b)

Figure 6: State Diagram Process of rdtsc/rdtscp emulation by HyperDbg

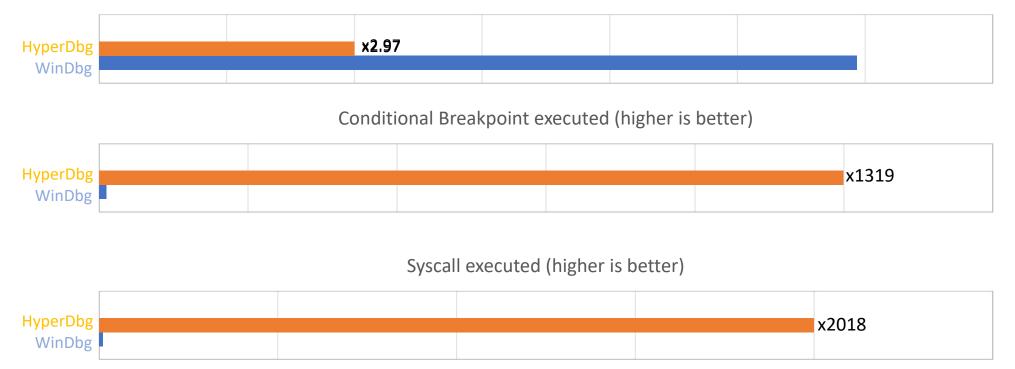
```
rdtscp; get the current time clock
cpuid; Execute a serialization instruction (VM-exit)
rdtscp; Delta Timing
```

Listing 2: The timing measurement code by forcing VM-exit



Performance

Single stepping time (lower is better)



```
File Edit Search View Encoding Language Settings Tools Macro Run Plugins Window ?
/ new 1 ⋈ / new 2 ⋈ / new 3 ⋈ / new 4 ⋈ / new 5 ⋈ / new 6 ⋈
 2 WinDbg :
 4 bp nt!NtOpenFile ".printf /D \"%mu \\n\", poi(poi(@r8+10)+8);g;"
     HyperDbg :
     !epthook nt!NtOpenFile script {
13
         printf("%ws\n", dq(poi(r8 + 10) + 0x8));
 17
                                                                                                                      Ln:18 Col:72 Pos:333
                                                                                                                                             Windows (CR LF) UTF-8
                                                                                                   length: 332 lines: 18
```



Evasive Malware Analysis



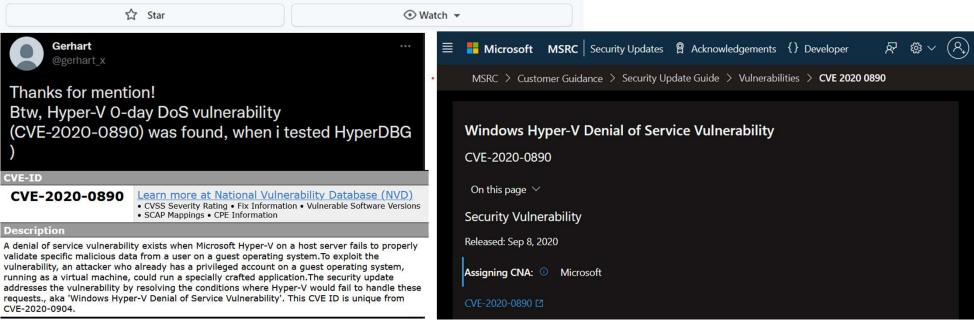
Kernel Fuzzing

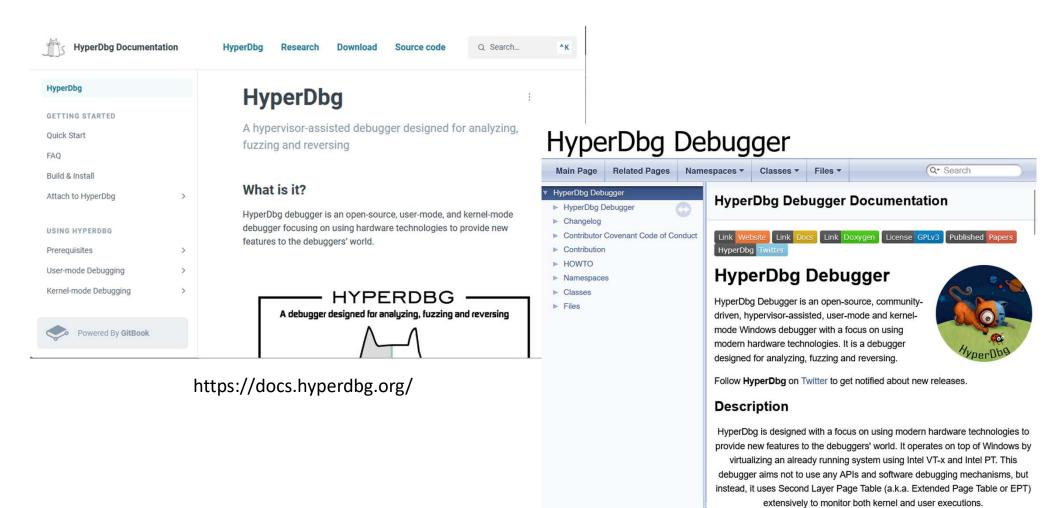


HyperDbg in the Wild

Over 125k+ lines of code
Over 2.5 years of development







https://doxygen.hyperdbg.org/

HYPERDBG DEBUGGER

HYPERDBG DEBUGGER

A DEBUGGER DESIGNED FOR ANALYZING, FUZZING AND REVERSING

1ST EDITION

A DEBUGGER DESIGNED FOR ANALYZING, FUZZING AND REVERSING

VERSION 0.1.0.0

Kernel Debugger Design in HyperDbg

VM-exit Transparency in HyperDbg

Website https://hyperdbg.org

Research
https://research.hyperdbg.org

Website https://hyperdbg.org Research https://research.hyperdbg.org

July 4, 2022

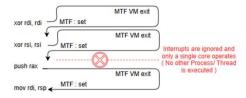
this issue by presenting a guaranteed stepping method. As explained, MTF is a feature that works similar to RFLAGS but is transparent to the guest. The following listing illustrates the set/unset of MTF in an execution sequence.

```
/* Set the monitor trap flag */
void HvSetMonitorTrapFlag(BOOLEAN Set)

{
    unsigned long CpuBasedVmExecControls = 0;
    // Read the previous flag
    ...vmx.vmread(CPU.BASED.VM.EXEC.CONTROL, &CpuBasedVmExecControls);
    if (Set) {
        CpuBasedVmExecControls |= CPU.BASED.MONITOR.TRAP.FLAG;
    }
    }
    else {
        CpuBasedVmExecControls &= "CPU.BASED.MONITOR.TRAP.FLAG;
    }
    // Set the new value
    ...vmx.vmw.rite(CPU.BASED.VM.EXEC.CONTROL, CpuBasedVmExecControls);
```

Listing 1: MTF Set/Unset in an example execution sequence

By executing each instruction, it is ensured that the specific line of code is passed to the CPU. In order to get the execution after executing an instruction, a VM-cxit is triggered by setting an MTF which guarantees that only one succeeding instruction will be executed in the guest. In order to do so, HYPENDEC continues at only one core and disables interrupts in the same core (ignoring external-interrupts by setting external-interrupts extiting bit in VMCS) to offer a fine-grained stepping. Figure 4 depicts the general procedure in



https://research.hyperdbg.org/

Figure 4: The i command Instrumentation Stepping Approach in HyperDbg

3.1 Changing Timestamps

In this approach, by the use of the statistical methodology described in the previous section, an accurate timing profiles are constructed and employed in transparency functions. An overview of the approach is shown in Figure 3.



July 25, 2022

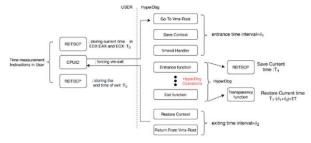


Figure 3: Transparency of HyperDbg by changing IA32, TIME, STAMP_COUNTER

Assuming the target time measuring instruction set in Listing [1], according to the Figure [3], in the presence of HyperDBG, CPUID, will force out a VM-exit. The VM-exit, consist of a entrance and exiting phases as depicted in the figure. entrance and exiting time intervals represented by \(\delta_1\) and \(\delta_2\) respectively, are measured iteratively in order to derive statistical characteristics. These timing are also follow a Gaussian Distribution (or two terms Gaussian Dis). Note that the values of \(\delta_1\) and \(\delta_2\) respectively, are the hiding process itself. To ensure the validity of these measurement for the HYPERDBG, a special function in the user-mode is executed by the debugger, storing the timing stamps (T₀) using Volatile registers (e.g. EDX, EXX). Moreover, additional hash validation is checked for each time measurement.

After the profiling phase, the hiding process is executed by HYPERDIG as shown in the Figure [3]. When an analyzer software is activated to detect any low-level interception, the Entrance Function stee the time by executing RDTSCP (7₁), after VM-cxit entrance. Then, any arbitrary functions in HYPERDIG are executed. At the end of the operations in the hypervisor, a Transparency function is called. Here, the IA32. TIME. STAMP.COUNTER value is replaced with the following value.

$$Time_Stamp = T_1 - (GRG(\delta_1) + GRG(\delta_2)) - GRG(Norm)$$
 (2)

The GRG is the Gaussian Random Generated number by the use of the Marsaglia Polar Method $\boxed{3}$ activated with Standard Deviation (σ) and Mean (μ) values captured in the initial statistical test-cases. GRG(Norm) represents the Gaussian estimated elapsed clock-cycles for the timing instructions when HYPERDBG is not activated in normal condition.

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HyperDbg Documentation

HyperDbg

Research

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Source code

GETTING STARTED

Quick Start

HyperDbg

FAQ

Build & Install

Attach to HyperDbg

USING HYPERDBG

Prerequisites

User-mode Debugging

Kernel-mode Debugging

COMMANDS

Debugging Commands

Meta Commands

>

Extension Commands

Scrinting Language

Powered By GitBook

HyperDbg

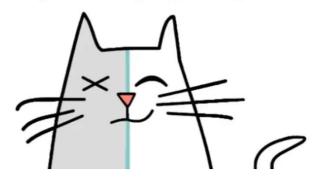
A hypervisor-assisted debugger designed for analyzing, fuzzing and reversing

What is it?

HyperDbg debugger is an open-source, user-mode, and kernel-mode debugger focusing on using hardware technologies to provide new features to the debuggers' world.

HYPERDBG

A debugger designed for analyzing, fuzzing and reversing





Thank you for your attention Questions?



Table 5: A comprehensive comparison of HyperDbg with different debuggers

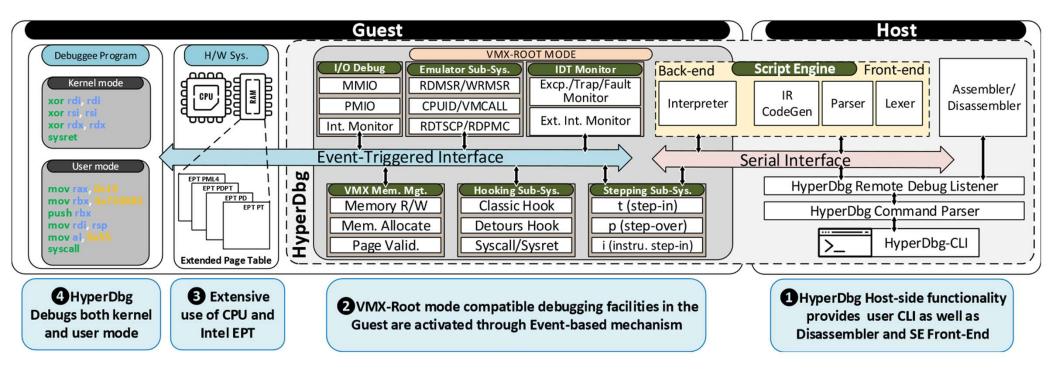
Debugger	Deployment		ging Mode Kernel Mode		Direct Deployment (NO VM/Emulator)		Hooking	Scripting	Custom Assembly Execution	Notable Features (Currently Working Debuggers)
		User Mode	Kernel Mode	Hardware Assisted Methods	,	Code Available		Customized VMX		Different subsystems
HyperDbg	Hypervisor (Ring -1)	/	/	(e.g. TimeStamp Emulation Transparency)	/	/	EPT Hidden Hooking	compatible ScriptEngine	/	Fast script engine I/O debugging
Ghidra[23]	Application (Ring 3)	/	×	×	×	1	×	Python Scripting	×	Advanced Decompiler Multi-platform Multi-architecture support
WinDbg[19]	Operating System	1	1	×	1	×	×	JavaScript WinDbg Script	×	Javascript support Advanced scripting language
SoftIce[20]	Operating System	×	1	×	1	N/A	×	×	×	Local debugging with special GUI
x64dbg[98]	Application (Ring 3)	1	×	Software-Based ScyllaHide	1	/	×	Customized Scripting	×	Flexible and strong GUI Lots of useful features
Ollydbg[76]	Application (Ring 3)	1	×	Software-Based ScyllaHide	✓	×	×	ODBGScript	1	Stability
gdb [22]	Operating System	/	1	×	1	1	×	Bash Scripting Automation	×	Main Linux Debugger Support multiple platforms
Malt[102]	SMM (Ring -2)	N/A	1	SMM Level Transparency	/	N/A	N/A	N/A	N/A	N/A
BareBox[50]	Hypervisor (Ring -1)	N/A	/	Meta OS (Bare Metal) Transparency	/	N/A	N/A	N/A	N/A	N/A
V2E[100]	Hypervisor (Ring -1)	N/A	1	Hypervisor Level Transparency	×	N/A	N/A	N/A	N/A	N/A
Anubis[64]	Hypervisor (Ring -1)	N/A	1	Limited Software-Based Methods	×	N/A	N/A	N/A	N/A	N/A
Virt-ICE[82]	Hypervisor (Ring -1)	N/A	1	Emulating Software-Based Methods	×	N/A	N/A	N/A	N/A	N/A
HyperDbg (old) (deprecated)	Hypervisor (Ring -1)	1	1	N/A	×	1	×	×	×	N/A
Ether[27]	Hypervisor (Ring -1)	/	/	Hypervisor Level Transparency	×	N/A	N/A	N/A	N/A	N/A
VAMPiRE[91]	Hypervisor (Ring -1)	N/A	1	Hypervisor Level Transparency	/	N/A	N/A	N/A	N/A	N/A
SPIDER[26]	Hypervisor (Ring -1)	N/A	/	Hypervisor Level Transparency	×	N/A	N/A	N/A	N/A	N/A
IDAPro[41]	Application (Ring 3)	1	×	×	1	×	×	Built-in Scripting Engine (IDC / Python)	×	Advanced decompiler Multi-architecture Multi-platform support

Table 1: Anti-Debugging and Anti-VM exercises and mitigation in HyperDbG

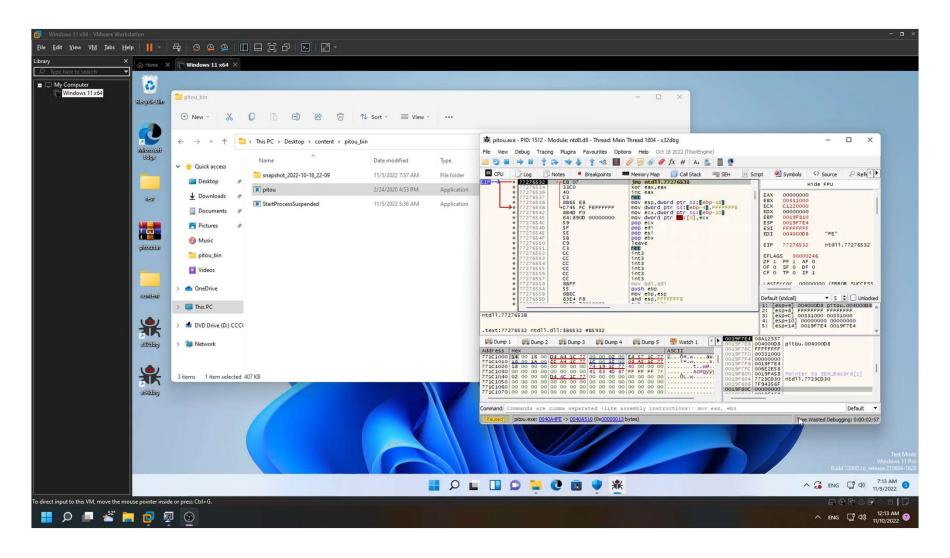
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	In/Out Instructions	Magic I/O port in VMware	[25]	VM-exit handled (I/O bitmap)
	Invalid MSR Access	Invalid MSR issues General Protection (#GP)	[103]	Emulate !msrread/!msrwrite command
	Exception Handling	Try-Catch General Protection Excep. (#GP)	[59]	Handled by default Inject routine into user-mode

Table 2: Evaluation and comparison of HyperDbg for integrated software via packers/protectors

Packer/Protector	File Type	WinDbg	x64dbg	Ollydbg	HyperDbg	HyperDbg's Trans. Mode
ASPack.V2.42	PE32	Error	1	✓	/	/
Enigma.V4.30	PE64	X	X	N/A	X	/
Net_Crypto.V5	PE32	X	X	X	/	1
Obsidium.V1.7	PE64	X	X	N/A	/	/
Themida.V3.0.4	PE64	X	X	N/A	/	/
Upx.V3.96	PE64	1	1	/	/	✓
Vmprotect.V.2.13	PE64	X	X	N/A	X	/
MEW11.V1.2	PE32	✓	✓	/	/	/
Pecompact.V3.11	PE32	/	/	/	/	/
PELock.V2.0	PE32	X	X	X	/	1
Petite.V2.4	PE32	Error	1	/	/	1
TeLock.V0.98	PE32	✓	1	/	/	1
YodaCrypter.V1.02	PE32	X	X	X	✓	/



Pitou



Debugger detected... Trying with HyperDbg

```
Select C:\Users\sina\Desktop\HyperDbq\HyperDbq\hyperdbq\build\bin\release\hyperdbq-cli.exe
 debug : debugs a target machine or makes this machine a debuggee.
                .debug [remote] [serial|namedpipe] [Baudrate (decimal)] [Address (string)]
syntax :
                .debug [prepare] [serial] [Baudrate (decimal)] [Address (string)]
syntax :
syntax :
                .debug [close]
                e.g : .debug remote serial 115200 com3
                e.g : .debug remote namedpipe \\.\pipe\HyperDbgPipe
                e.g : .debug prepare serial 115200 com1
                e.g : .debug prepare serial 115200 com2
                e.g : .debug close
valid baud rates (decimal) : 110, 300, 600, 1200, 2400, 4800, 9600, 14400, 19200, 38400, 56000, 57600, 115200, 128000, 2
valid COM ports : COM1, COM2, COM3, COM4
HyperDbg> .debug remote namedpipe \\.\pipe\HyperDbgPipe
waiting for debuggee to connect...
connected to debuggee Windows 10 Pro - Client 21H2 (OS Build 22000)
getting symbol details...
interpreting symbols and creating symbol maps
please configure the symbol path (use '.help .sympath' for more information)
press CTRL+C to pause the debuggee
debuggee is running...
```



In honor of all the Iranians who are risking their lives in pursuit of liberty and human rights







