Conception Electronique pour le Traitement de l'Information
Julien VILLEMEJANE /
LEnsE / Institut d'Optique Graduate School

5N-027-SCI / CéTI

Bloc 2

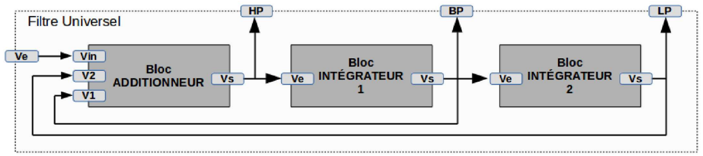
BLOC 2 / FILTRAGE ACTIF

Mission 2.1 - Filtrer des composantes fréquentielles - Ordre 1 Passif

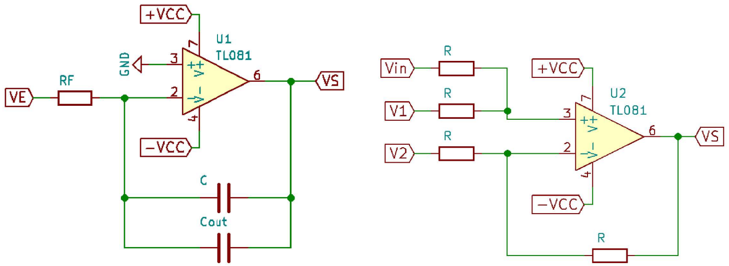
Proposer une structure de filtre du premier ordre qui laisse passer des signaux au dessus d'une fréquence f_c. Donner les principales caractéristiques et limitations d'un tel filtre.

Mission 2.2 - Filtrer des composantes fréquentielles - Ordre 2

On se propose d'étudier la structure suivante :



Pour cela, on se propose d'étudier les deux circuits suivants :



Mission 2.4 - Réaliser un filtre à partir d'un gabarit

On s'intéresse ici aux filtres de **Butterworth** (voir annexe).
On souhaite réaliser un filtre dont le gabarit est le suivant :

— gain supérieur à -1 dB jusqu'à 10 kHz

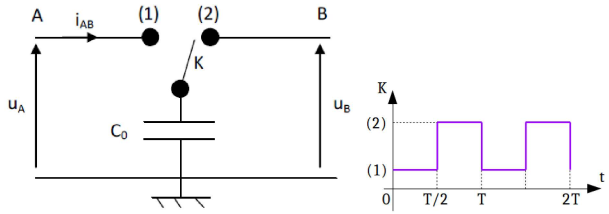
— gain inférieur à -60 dB à partir de 40 kHz

1. Tracer le gabarit du filtre.
2. Déterminer l'ordre du filtre minimal.
3. Déterminer la pulsation de coupure du filtre.
4. Déterminer la fonction de transfert du filtre

Mission 2.3 - Filtrer des composantes fréquentielles autrement

Capacité commutée

On se propose d'étudier la structure suivante, dont l'interrupteur K est piloté par le signal de commande ci-dessous :

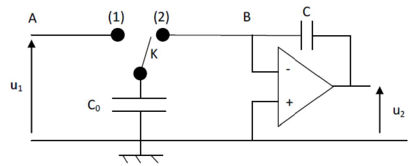


1. Calculer la charge stockée dans C_0 entre les instants 0 et $T/2$, puis entre les instants $T/2$ et T.
2. Quelle quantité de charges passe de A vers B entre les instants 0 et T ?
3. Calculer alors le courant moyen circulant du point A au point B pendant une période T.
4. Donner l'expression de la résistance équivalente R_{AB} vue entre les bornes A et B de cette cellule.

Intégrateur

On réalise un intégrateur à partir du circuit de la figure 2.

1. Donner la fonction de transfert du circuit $T(j\omega) = u_2/u_1$ en fonction de R_{AB} et de C.



2. Que devient alors la fonction de transfert $T(j\omega) = u_2/u_1$ en fonction des éléments du système (C_0 et C) ?
3. Quel est l'intérêt d'un tel circuit ?

Etude du MAX296

On s'intéresse au composant MAX296 dont une partie de la documentation technique est donnée en annexe.

1. Quelles sont les fréquences maximales utilisables sur l'entrée INPUT ? Sur l'entrée CLOCK ? Quelles sont les applications visées ?
2. Quelle fréquence faut-il appliquer sur l'entrée CLOCK pour avoir une fréquence de coupure de 3 kHz ? Que vaut alors l'amplification théorique du signal à : (a) 300 Hz ? (b) 30 kHz ? (c) 5 kHz ?
3. Avec un filtre du second ordre (type Rauch) avec une pulsation de coupure à la même valeur, quelle aurait été l'amplification : (a) à 30 kHz ? (b) à 5 kHz ?

MAX291/MAX292/MAX295/MAX296

8th-Order, Lowpass, Switched-Capacitor Filters

General Description

The MAX291/MAX292/MAX295/MAX296 are easy-to-use, 8th-order, lowpass, switched-capacitor filters that can be set up with corner frequencies from 0.1Hz to 25kHz (MAX291/MAX292) or 0.1Hz to 50kHz (MAX295/MAX296).

The MAX291/MAX295 Butterworth filters provide maximally flat passband response, and the MAX292/MAX296 Bessel filters provide low overshoot and fast settling. All four filters have fixed responses, so the design task is limited to selecting the clock frequency that controls the filter's corner frequency.

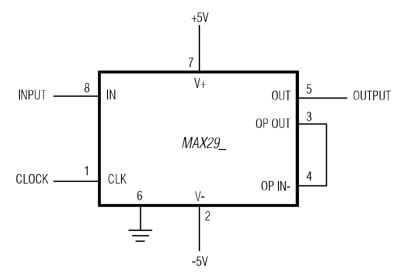
An external capacitor is used to generate a clock using the internal oscillator, or an external clock signal can be used. An uncommitted operational amplifier (noninverting input grounded) is provided for building a continuous-time lowpass filter for post-filtering or anti-aliasing.

Produced in an 8-pin DIP/SO and a 16-pin wide SO package, and requiring a minimum of external components, the MAX291 series delivers very aggressive performance from a tiny area.

Applications

ADC Anti-Aliasing Filter
Noise Analysis
DAC Post-Filtering
50Hz/60Hz Line-Noise Filtering

Typical Operating Circuit



Pin Configuration is 8-pin DIP/SO.

Features

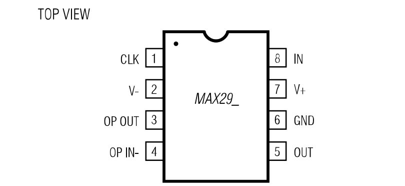
- ♦ **8th-Order Lowpass Filters:**
  Butterworth (MAX291/MAX295)
  Bessel (MAX292/MAX296)
- ♦ **Clock-Tunable Corner-Frequency Range:**
  0.1Hz to 25kHz (MAX291/MAX292)
  0.1Hz to 50kHz (MAX295/MAX296)
- ♦ **No External Resistors or Capacitors Required**
- ♦ **Internal or External Clock**
- ♦ **Clock to Corner Frequency Ratio:**
  100:1 (MAX291/MAX292)
  50:1 (MAX295/MAX296)
- ♦ **Low Noise: -70dB THD + Noise (Typ)**
- ♦ **Operate with a Single +5V Supply or Dual ±5V Supplies**
- ♦ **Uncommitted Op Amp for Anti-Aliasing or Clock-Noise Filtering**
- ♦ **8-Pin DIP and SO Packages**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX291CPA	0°C to +70°C	8 Plastic DIP
MAX291CSA	0°C to +70°C	8 SO
MAX291CWE	0°C to +70°C	16 Wide SO
MAX291C/D	0°C to +70°C	Dice*
MAX291EPA	-40°C to +85°C	8 Plastic DIP
MAX291ESA	-40°C to +85°C	8 SO
MAX291EWE	-40°C to +85°C	16 Wide SO
MAX291MJA	-55°C to +125°C	8 CERDIP**

Ordering Information continued at end of data sheet.
* *Contact factory for dice specifications.*
** *Contact factory for availability and processing to MIL-STD-883.*

Pin Configurations



DIP/SO

16-pin Wide SO at end of data sheet.

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

19-4526; Rev 5; 5/10

ELECTRICAL CHARACTERISTICS (continued)

($V+ = 5V$, $V- = -5V$, filter output measured at OUT pin, 20kΩ load resistor to ground at OUT and OP OUT, f_{CLK} = 100kHz (MAX291/MAX292) or f_{CLK} = 50kHz (MAX295/MAX296), $T_A = T_{MIN}$ to T_{MAX}, unless otherwise noted.)

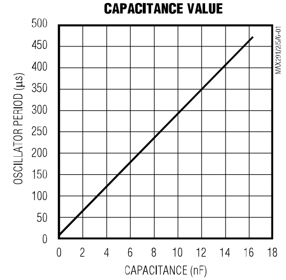
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Output DC Swing		±4			V
Output Offset Voltage	IN = GND		±150	±400	mV
DC Insertion Gain Error with Output Offset Removed		0.15	0	-0.15	dB
Total Harmonic Distortion plus Noise	T_A = +25°C, f_{CLK} = 100kHz		-70		dB
Clock Feedthrough	f_{CLK} = 100kHz		6		mVp-p
CLOCK					
Internal Oscillator Frequency	C_{OSC} = 1000pF	29	35	43	kHz
Internal Oscillator Current Source/Sink	V_{CLK} = 0V or 5V		±70	±120	µA
Clock Input High (Note 1)		4.0			V
Low				1.0	V
UNCOMMITTED OP AMP					
Input Offset Voltage			±10	±50	mV
Output DC Swing		±4			V
Input Bias Current			0.05		µA
POWER REQUIREMENTS					
Supply Voltage Dual Supply		±2.375		±5.500	V
Single Supply	V- = 0V, GND = V±2	4.750		11.000	V
Supply Current	V+ = 5V, V- = -5V, V_{CLK} = 0V to 5V		15	22	mA
	V+ = 2.375V, V- = -2.375V, V_{CLK} = -2V to 2V		7	12	

Note 1. Guaranteed by design.

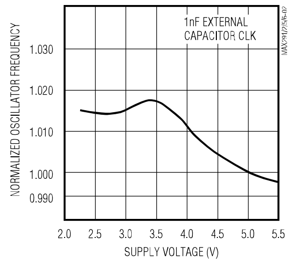
Typical Operating Characteristics

($V+ = 5V$, $V- = -5V$, T_A = +25°C, f_{CLK} = 100kHz (MAX291/MAX292) or f_{CLK} = 50kHz (MAX295/MAX296), unless otherwise noted.)

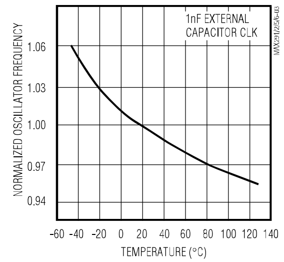
INTERNAL OSCILLATOR PERIOD vs. CAPACITANCE VALUE



NORMALIZED INTERNAL OSCILLATOR FREQUENCY vs. SUPPLY VOLTAGE



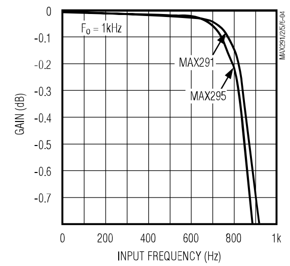
NORMALIZED INTERNAL OSCILLATOR FREQUENCY vs. TEMPERATURE



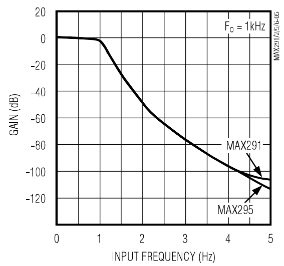
Typical Operating Characteristics (continued)

(V+ = 5V, V- = -5V, T_A = +25°C, f_{CLK} = 100kHz (MAX291/MAX292) or f_{CLK} = 50kHz (MAX295/MAX296), unless otherwise noted.)

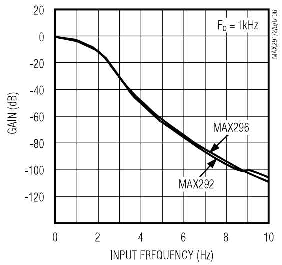
MAX291/MAX295
FREQUENCY RESPONSE



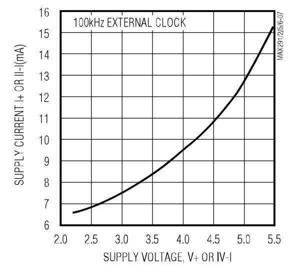
MAX291/MAX295
FREQUENCY RESPONSE



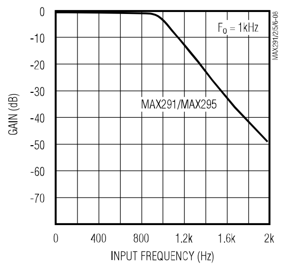
MAX292/MAX296
FREQUENCY RESPONSE



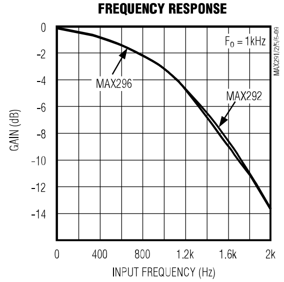
SUPPLY CURRENT
vs. SUPPLY VOLTAGE



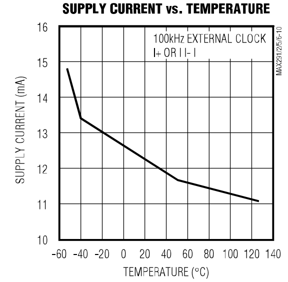
MAX291/MAX295
FREQUENCY RESPONSE



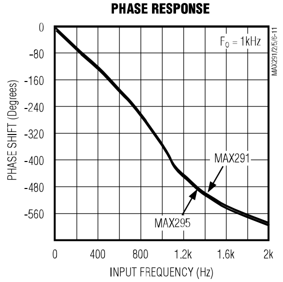
MAX292/MAX296
FREQUENCY RESPONSE



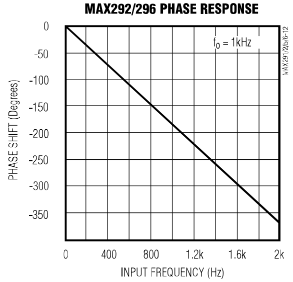
SUPPLY CURRENT vs. TEMPERATURE



MAX291/MAX295
PHASE RESPONSE



MAX292/296 PHASE RESPONSE



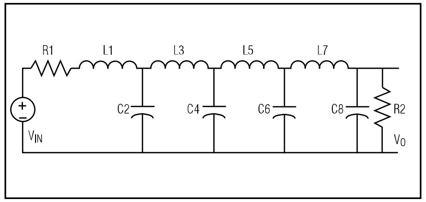


Figure 2. 8th-Order Ladder Filter Network

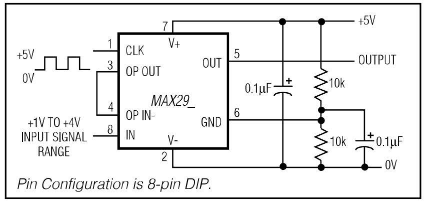


Figure 3. +5V Single-Supply Operation

error on its respective poles, while the same mismatch in a ladder filter design will spread its error over all poles.

The MAX291/MAX292/MAX295/MAX296 input impedance is effectively that of a switched-capacitor resistor (see equation below, and Table 1), and it is inversely proportional to frequency. The input impedance values determined below represent average input impedance, since the input current is not continuous. The input current flows in a series of pulses that charge the input capacitor every time the appropriate switch is closed. A good rule of thumb is that the driver's input source resistance should be less than 10% of the filter's input impedance. The input impedance of the filter can be estimated using the following formula:

$$Z = 1 / (f_{CLK} * C)$$

where: f_{CLK} = Clock Frequency

The input impedance for various clock frequencies is given below:

Table 1. Input Impedance for Various Clock Frequencies

PART	C (pF)	10kHz (MΩ)	100kHz (MΩ)	1000kHz (kΩ)
MAX291	2.24	44.6	4.46	446
MAX292	3.28	30.5	3.05	305
MAX295	4.47	22.4	2.24	224
MAX296	4.22	23.7	2.37	237

Clock-Signal Requirements

The MAX291/MAX292/MAX295/MAX296 maximum recommended clock frequency is 2.5MHz, producing a cutoff frequency of 25kHz for the MAX291/MAX292 and 50kHz for the MAX295/MAX296. The CLK pin can be driven by an external clock or by the internal oscillator with an external capacitor. For external clock applications, the clock circuitry has been designed to interface with +5V CMOS logic. Drive the CLK pin with a CMOS gate powered from 0V and +5V when using either a single +5V supply or dual +5V supplies. The MAX291/MAX292/MAX295/MAX296 supply current increases slightly (<3%) with increasing clock frequency over the clock range 100kHz to 1MHz. Varying the rate of an external clock will dynamically adjust the corner frequency of the filter.

Ideally, the MAX291/MAX292/MAX295/MAX296 should be clocked symmetrically (50% duty cycle). MAX291/MAX292/MAX295/MAX296 can be operated with clock asymmetry of up to 60/40% (or 40/60%) if the clock remains HIGH and LOW for at least 200ns. For example, if the part has a maximum clock rate of 2.5MHz, then the clock should be high for at least 200ns, and low for at least 200ns.

When using the internal oscillator, the capacitance (C_{OSC}) from CLK to ground determines the oscillator frequency:

$$f_{OSC}\ (kHz) \approx \frac{10^5}{3C_{OSC}\ (pF)}$$

The stray capacitance at CLK should be minimized because it will affect the internal oscillator frequency.

Application Information

Power Supplies

The MAX291/MAX292/MAX295/MAX296 operate from either dual or single power supplies. The dual-supply voltage range is +2.375V to +5.500V. The ±2.5V dual supply is equivalent to single-supply operation (Figure 3). Minor performance degradation could occur due to the external resistor divider network, where the GND pin is biased to mid-supply.

Input Signal Range

The ideal input signal range is determined by observing at what voltage level the total harmonic distortion plus noise (THD + Noise) ratio is maximized for a given corner frequency. The *Typical Operating Characteristics* show the MAX291/MAX292/MAX295/MAX296 THD + Noise response as the input signal's peak-to-peak amplitude is varied.

Uncommitted Op Amp

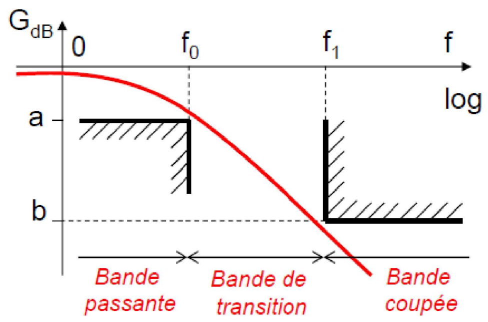
The uncommitted op amp has its noninverting input tied to the GND pin, and can be used to build a 1st- or 2nd-

Annexe : Filtres actif de Butterworth et de Chebychev

Document basé sur le cours de Sylvie Lebrun, Filtrage analogique, 2015.

Gabarit d'un filtre

Le gabarit d'un filtre correspond aux **contraintes fréquentielles et en gain** que doit satisfaire le système à développer.



On souhaite souvent réaliser un système de filtrage qui possède les caractéristiques suivantes :

— transmission de fréquence inférieure à f_0

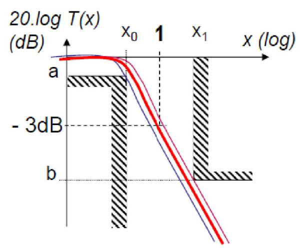
— valeur minimale a de gain dans la bande de fréquence à transmettre

— valeur maximale b de gain dans la bande de fréquence à éliminer (à partir d'une fréquence f_1)

Le gabarit est caractérisé par 2 points (f_0, a) et (f_1, b).

A partir de ce gabarit, plusieurs types de filtres peuvent être utilisés : Butterworth, Chebychev, Bessel, Cauer

Pour la suite, on posera : $X = \frac{\omega}{\omega_c}$ où ω_c est la fréquence de coupure du système, définie à $-3\,$dB par rapport au gain dans la bande passante.



Filtre de Butterworth

Ce type de filtre est utilisé pour sa **réponse extrêmement plate dans la bande-passante**.

La réponse en fréquence d'un tel filtre est tel que son module vaut :

$$T(X) = \frac{1}{\sqrt{1 + X^{2 \cdot n}}}$$

où n est l'ordre du filtre.

Détermination de n

En s'intéressant aux conditions aux limites :

$$\begin{cases} 20 \cdot \log_{10} T(x_0) > a \\ 20 \cdot \log_{10} T(x_1) < b \end{cases} \Leftrightarrow \begin{cases} x_0^{2 \cdot n} < 10^{-a/10} - 1 & (1) \\ x_1^{2 \cdot n} > 10^{-b/10} - 1 & (2) \end{cases}$$

En divisant (2) par (1) on obtient alors la valeur minimale de n. On choisira n la plus petite valeur entière qui satisfasse :

$$n \geq \frac{1}{2} \cdot \frac{\log_{10} \frac{10^{-a/10} - 1}{10^{-b/10} - 1}}{\log_{10} \frac{f_0}{f_1}}$$

Détermination de f_c

On calcule alors avec (1) et (2) les fréquences de coupure limites :

$$f_{c,0} = \frac{f_0}{(10^{-a/10} - 1)^{1/(2 \cdot n)}} \qquad f_{c,1} = \frac{f_1}{(10^{-b/10} - 1)^{1/(2 \cdot n)}}$$

On choisit ensuite la fréquence de coupure comme étant la moyenne géométrique des deux fréquences précédentes :

$$f_c = \sqrt{f_{c,0} \cdot f_{c,1}}$$

Fonction de transfert

Il faut trouver une fraction rationnelle complexe $T(p)$ (avec $p = j \cdot x$) qui admette T(x) comme module. On factorise alors le polynôme : $B_n(x) = 1 + x^{2 \cdot n}$.

On trouve alors que $B_n(p)$ peut s'écrire sous la forme des polynômes obtenus par Butterworth :

n	Polynôme de Butterworth $B_n(p)$ pour $\omega_c = 1$.
1	$(p+1)$
2	$p^2 + 1.4142p + 1$
3	$(p+1)(p^2+p+1)$
4	$(p^2 + 0.7654p + 1)(p^2 + 1.8478p + 1)$
5	$(p+1)(p^2 + 0.6180p + 1)(p^2 + 1.6180p + 1)$
6	$(p^2 + 0.5176p + 1)(p^2 + 1.4142p + 1)(p^2 + 1.9319p + 1)$
7	$(p+1)(p^2 + 0.4450p + 1)(p^2 + 1.2470p + 1)(p^2 + 1.8019p + 1)$
8	$(p^2 + 0.3902p + 1)(p^2 + 1.1111p + 1)(p^2 + 1.6629p + 1)(p^2 + 1.9616p + 1)$

La fonction de transfert normalisée s'écrit alors (n impair et n pair) :

$$T(p) = \frac{1}{(1+p) \cdot (a^2 + b^2 + 2 \cdot bp + p^2) \cdot (\ldots} \qquad T(p) = \frac{1}{(a^2 + b^2 + 2 \cdot bp + p^2) \cdot (\ldots}$$