

Development Board for ARTIK-10

| Revision | Date | Description |
|----------|--------------|---------------|
| 0.5 | 2015.Nov, 02 | First Version |
| | 2015.Nov, 19 | ADD TP etc. |

Project Name : ARTIK Dev. Board10 VER 0.5

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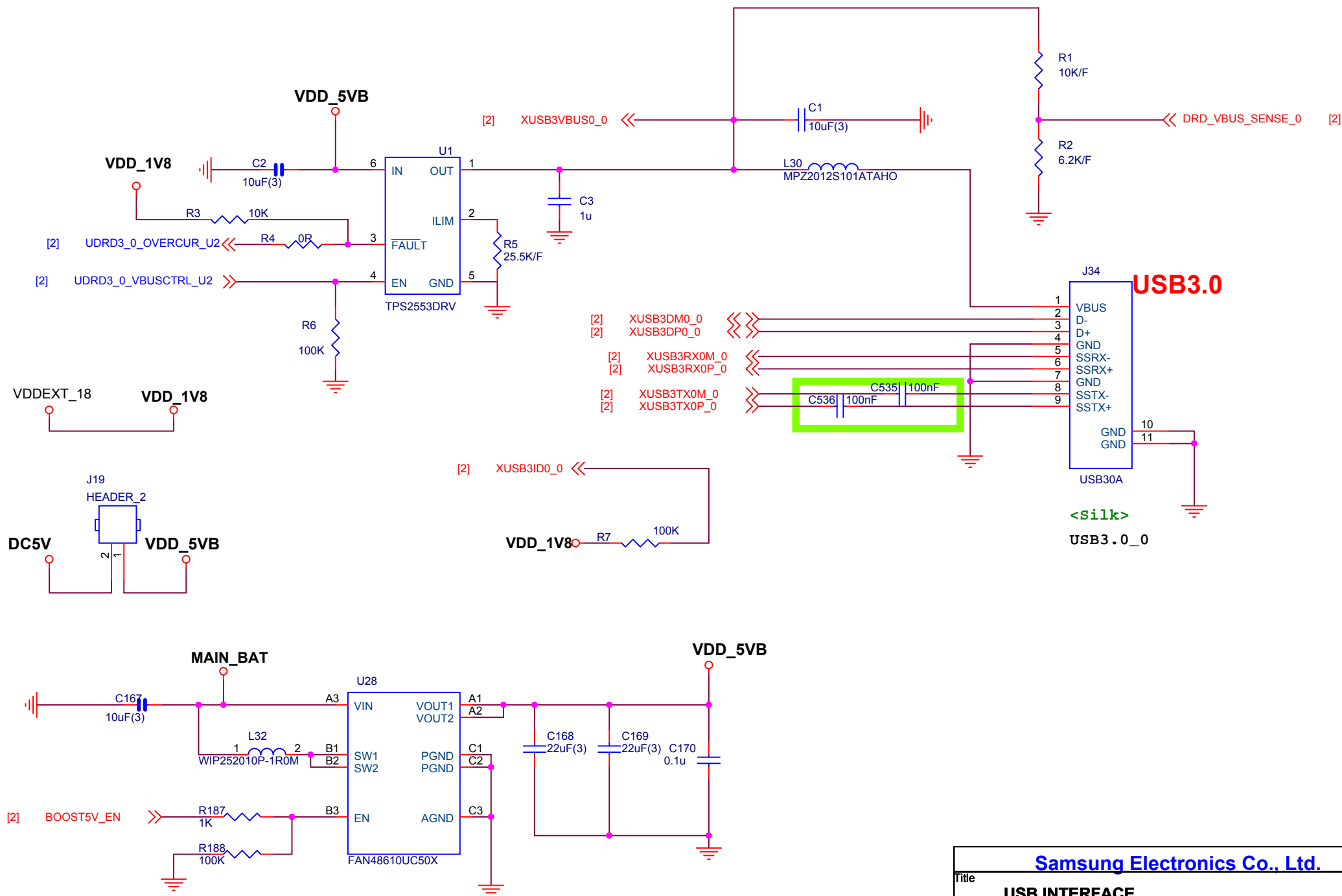
| Table of Contents | Abbreviation List | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|---|-------------------|-------------|----|------------------|----|------|----|---------|----|--------------|----|-------|----|--------|----|---------|----|-------------|----|-----|----|------------|----|--------------|----|-------------|----|------|----|------|----|--------|----|-----------|----|--------|----|-----------------|--|
| <table><tr><th>Page</th><th>Description</th></tr><tr><td>01</td><td>Revision History</td></tr><tr><td>02</td><td>CONN</td></tr><tr><td>03</td><td>USB 3.0</td></tr><tr><td>04</td><td>AUDIO-AK4953</td></tr><tr><td>05</td><td>UARTS</td></tr><tr><td>06</td><td>CAMERA</td></tr><tr><td>07</td><td>T-FLASH</td></tr><tr><td>08</td><td>POWER/RESET</td></tr><tr><td>09</td><td>LCD</td></tr><tr><td>10</td><td>NFC-S3WRN5</td></tr><tr><td>11</td><td>USB/ETHERNET</td></tr><tr><td>12</td><td>ARDUINO I/F</td></tr><tr><td>13</td><td>HDMI</td></tr><tr><td>14</td><td>LPWA</td></tr><tr><td>15</td><td>Z WAVE</td></tr><tr><td>16</td><td>EXT DEBUG</td></tr><tr><td>17</td><td>SENSOR</td></tr><tr><td>18</td><td>WIFI/BT-SWB-B80</td></tr></table> | Page | Description | 01 | Revision History | 02 | CONN | 03 | USB 3.0 | 04 | AUDIO-AK4953 | 05 | UARTS | 06 | CAMERA | 07 | T-FLASH | 08 | POWER/RESET | 09 | LCD | 10 | NFC-S3WRN5 | 11 | USB/ETHERNET | 12 | ARDUINO I/F | 13 | HDMI | 14 | LPWA | 15 | Z WAVE | 16 | EXT DEBUG | 17 | SENSOR | 18 | WIFI/BT-SWB-B80 | |
| Page | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 01 | Revision History | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 02 | CONN | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 03 | USB 3.0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 04 | AUDIO-AK4953 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 05 | UARTS | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 06 | CAMERA | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 07 | T-FLASH | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 08 | POWER/RESET | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 09 | LCD | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 10 | NFC-S3WRN5 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 11 | USB/ETHERNET | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 12 | ARDUINO I/F | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 13 | HDMI | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 14 | LPWA | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 15 | Z WAVE | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 16 | EXT DEBUG | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 17 | SENSOR | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 18 | WIFI/BT-SWB-B80 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

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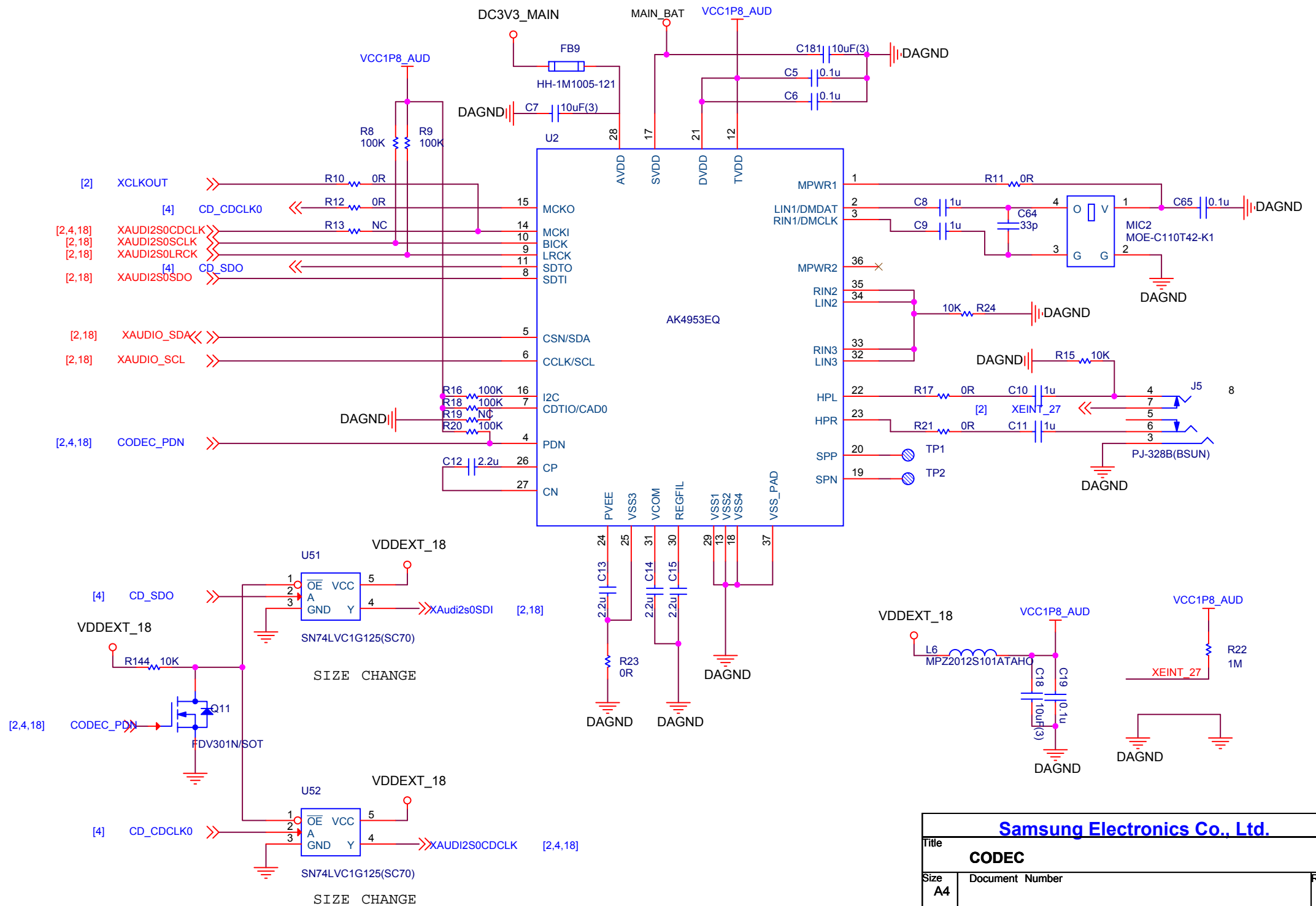
본 문서는 삼성전자가 허가한 열람자만을 위한 것이며, 부정 경쟁 방지 및 영업 비밀 보호에 관한 법률을 포함하여 관계 법령에 따라 보호의 대상이 되는 삼성전자의 영업 비밀, 기밀 정보 등을 포함하고 있는 바, 이 문서에 포함된 정보의 전부 또는 일부를 무단으로 제3자에게 직접 혹은 간접적 방법을 통하여 공개, 배포, 복사하거나 사용케 하는 행위(인터넷을 포함하여 상기의 정보가 불특정 다수에게 공개될 가능성이 있는 어떤 경계에 개하는 행위를 포함)는 엄격히 금지되어 있으며, 이러한 행위가 발생될 경우, 삼성전자는 손해 배상의 청구를 포함한 관계 법령에 따른 법적 조치를 취할 수 있습니다.

| Schematics Design By Name and Name | | |
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| Samsung Electronics Co., Ltd. | | |
| Title Development Board for ARTIK-10 | | |
| Size A3 | Document Number Revision History | Rev 0.5 |
| Date: Wednesday, December 23, 2015 1 of 18 | | |

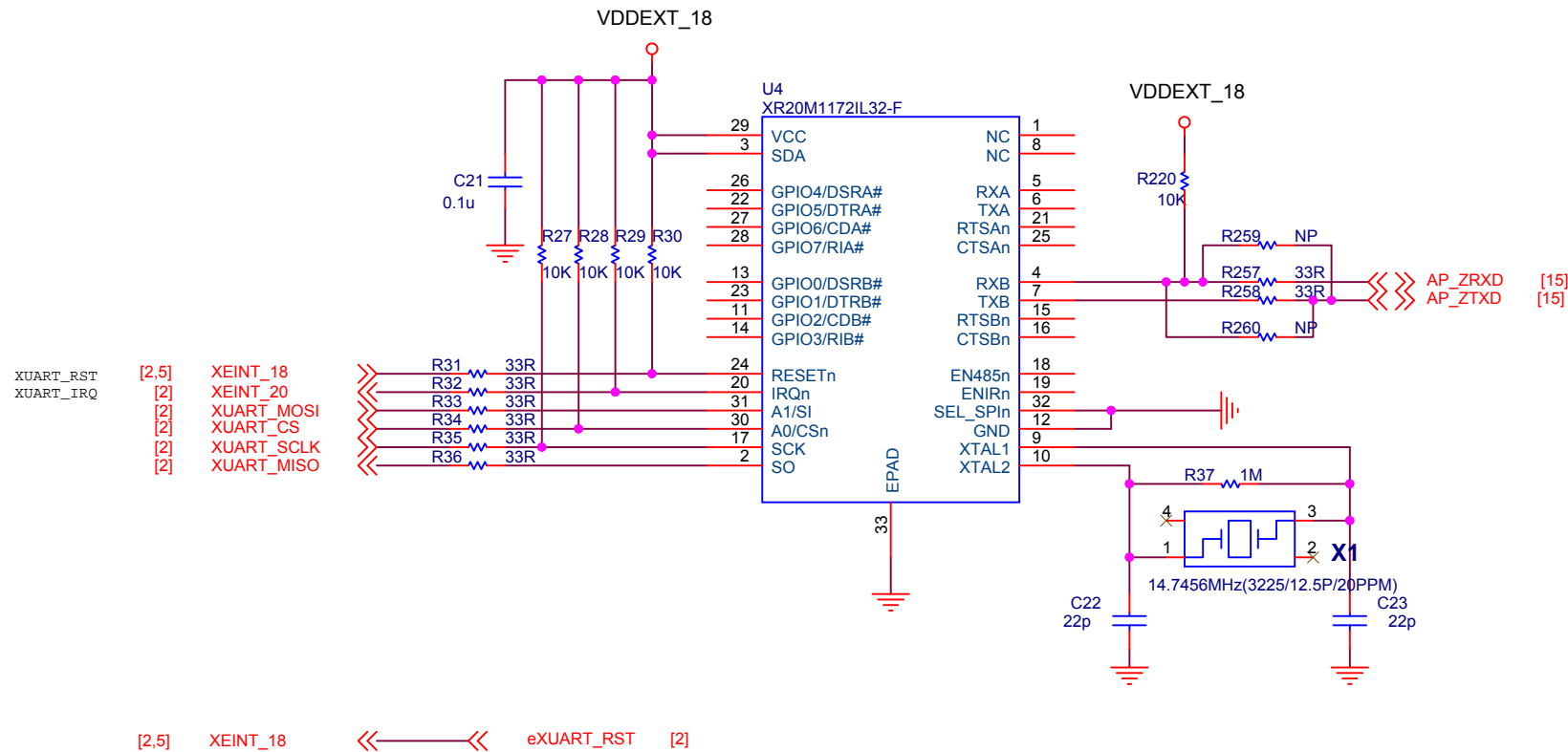
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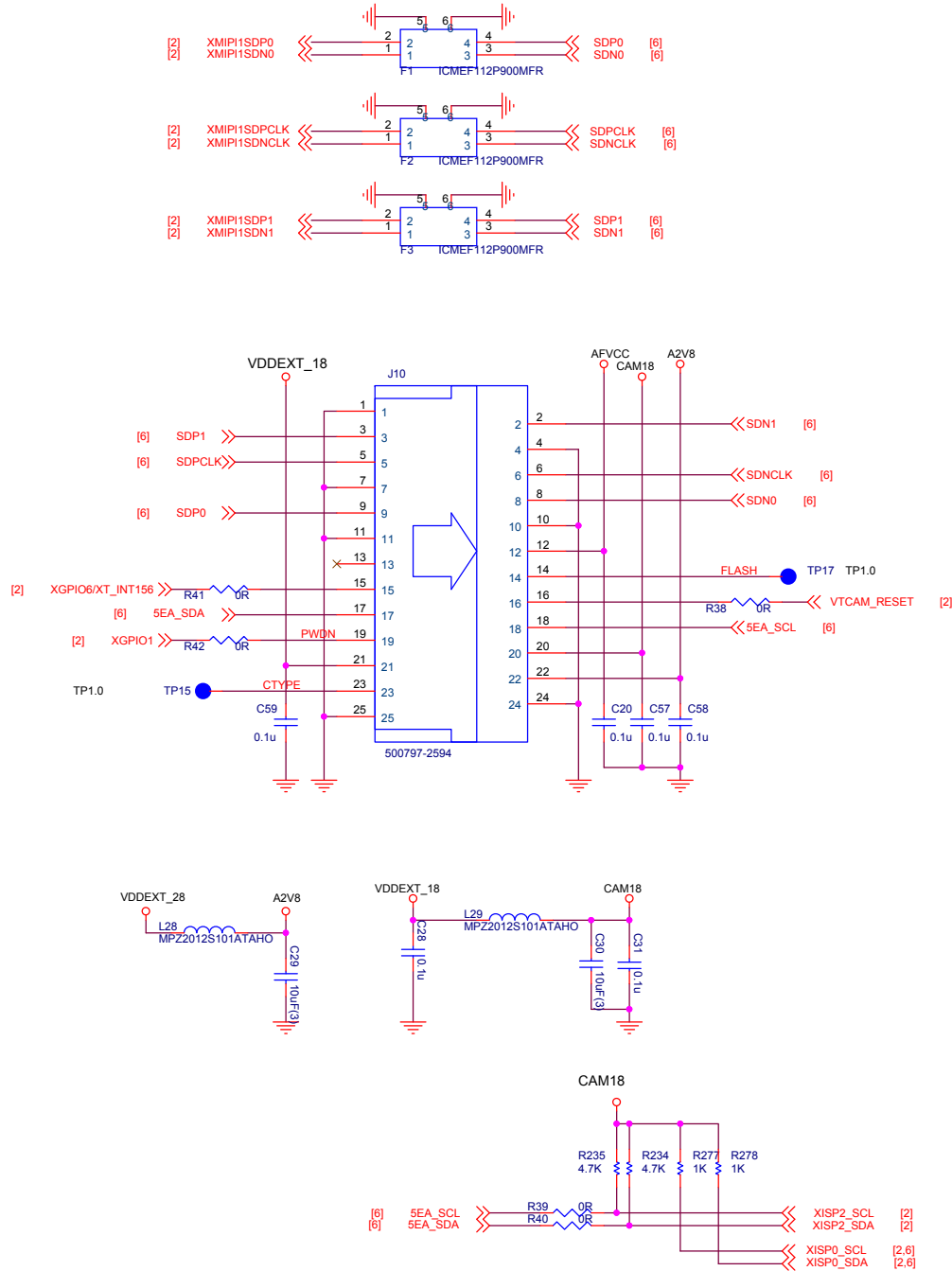
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|-------------------------------|------------------------------|----|-----|
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| USB INTERFACE | | | |
| Size | Document Number | | Rev |
| A4 | | | 0.5 |
| Date: | Wednesday, December 23, 2015 | | |
| Page | 3 | of | 18 |



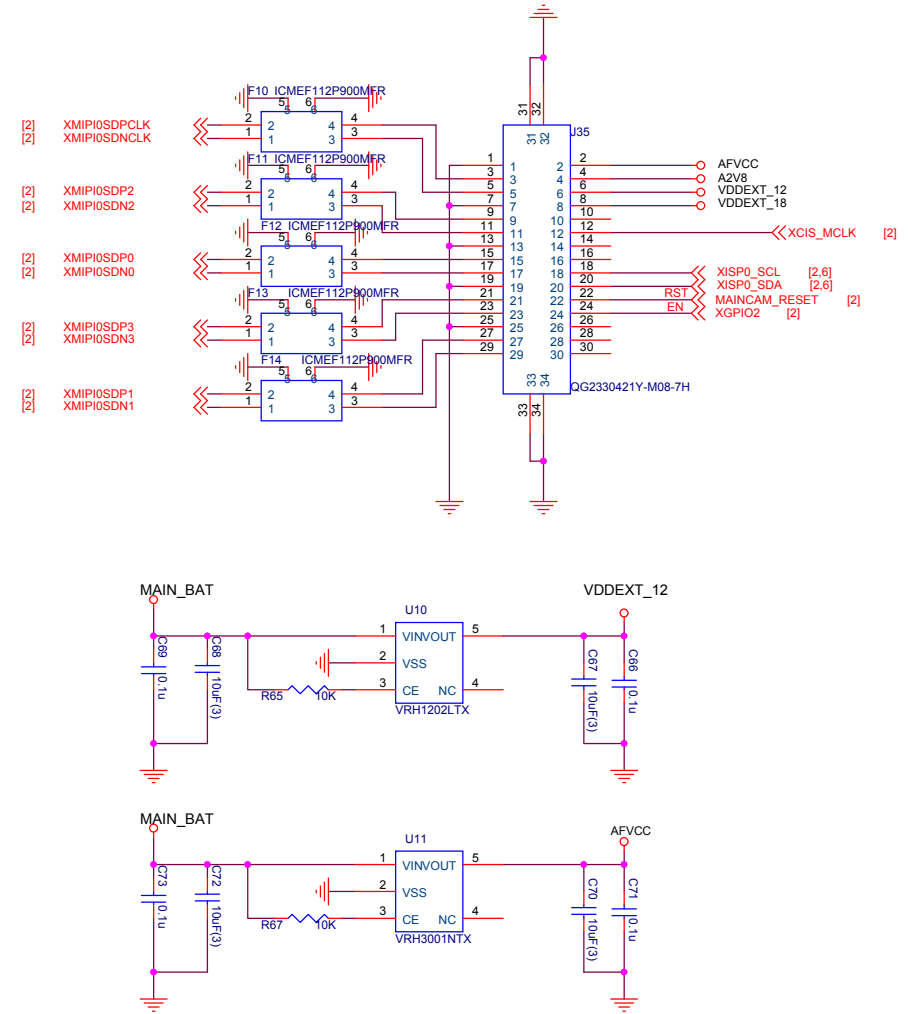
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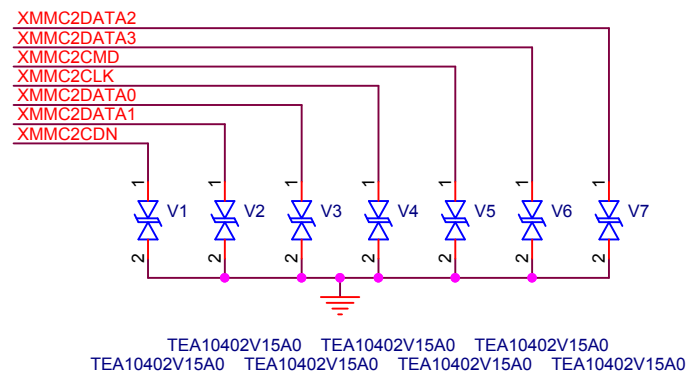
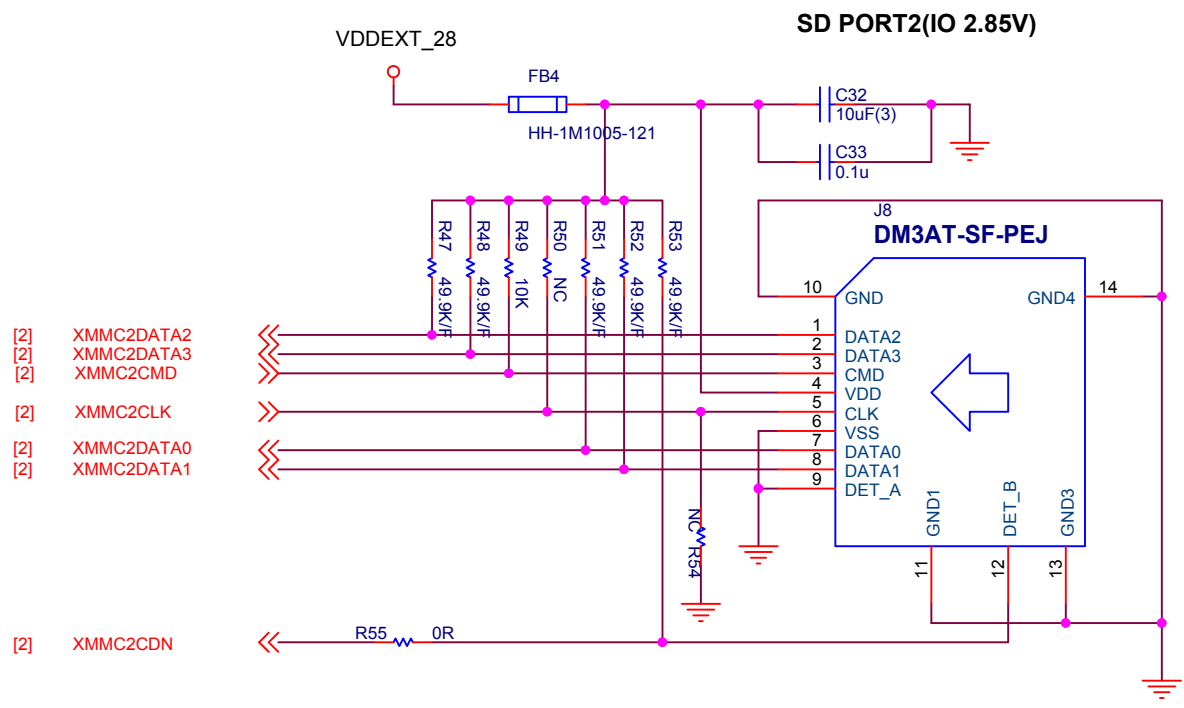


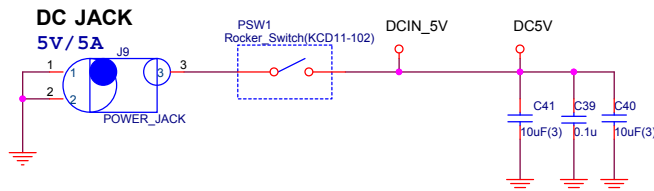
5EA CAM



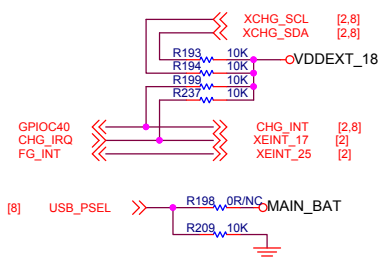
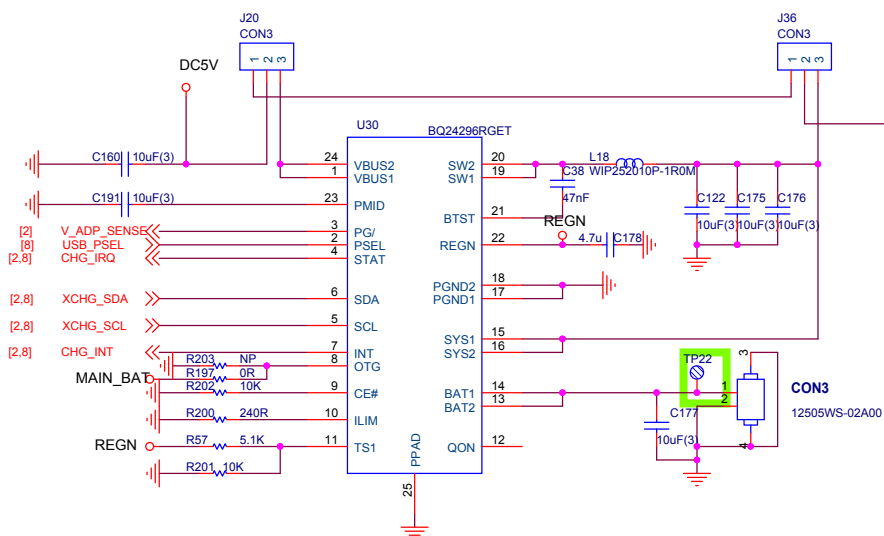
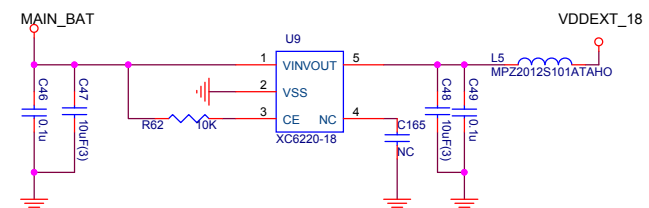
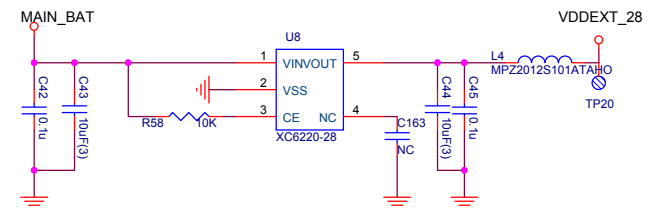
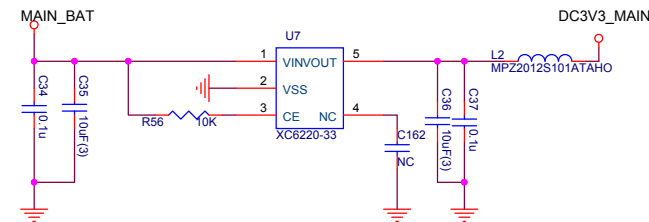
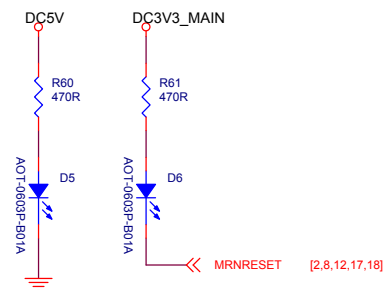
3L2 CAM



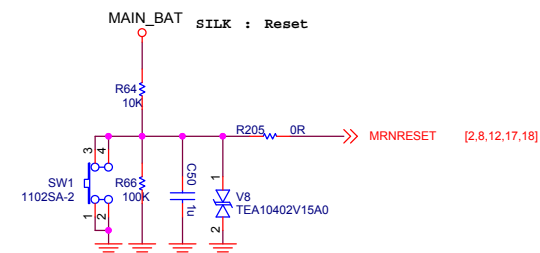




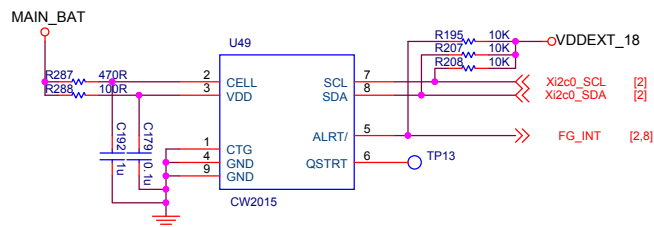
POWER LED RESET LED



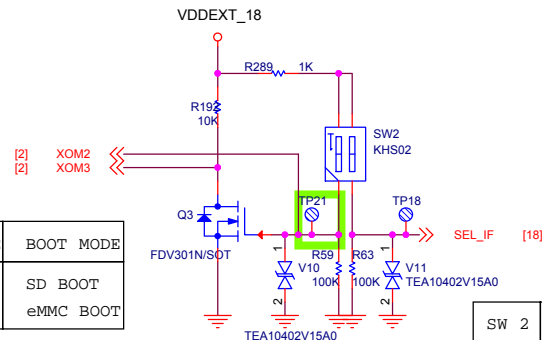
RESET



CHARGER / FUAL GAUGE

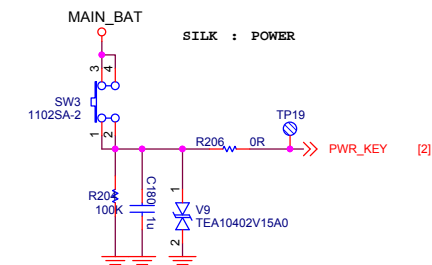


MODE SEL



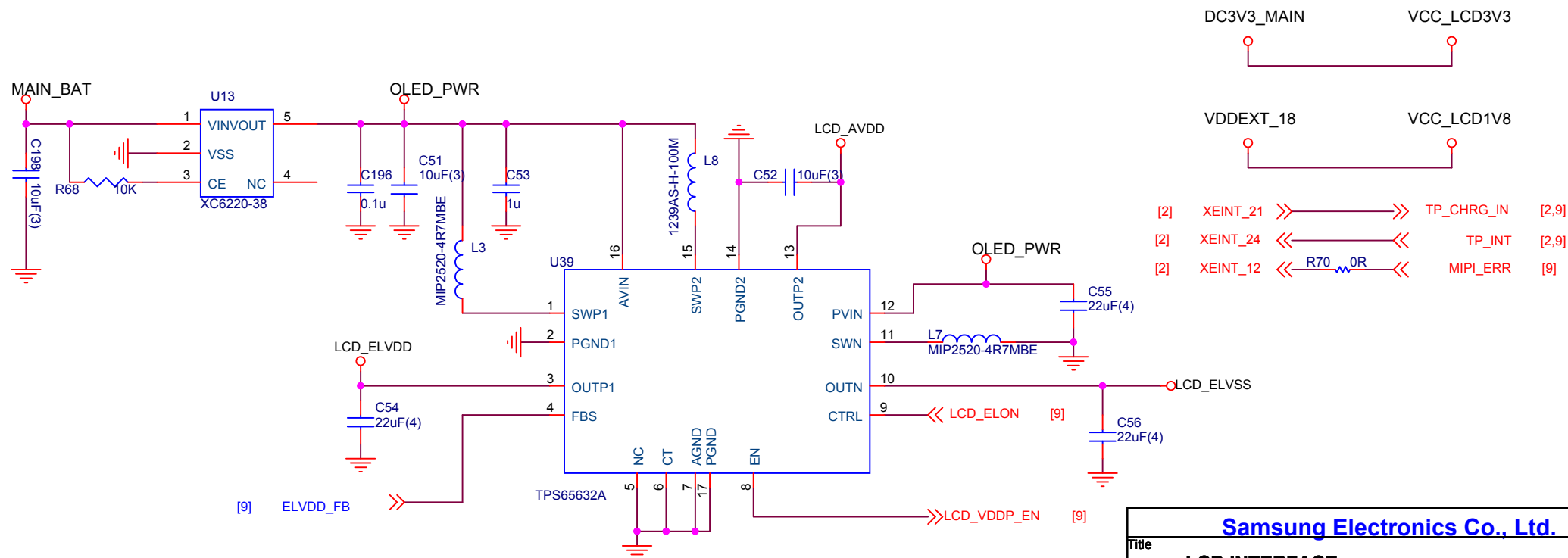
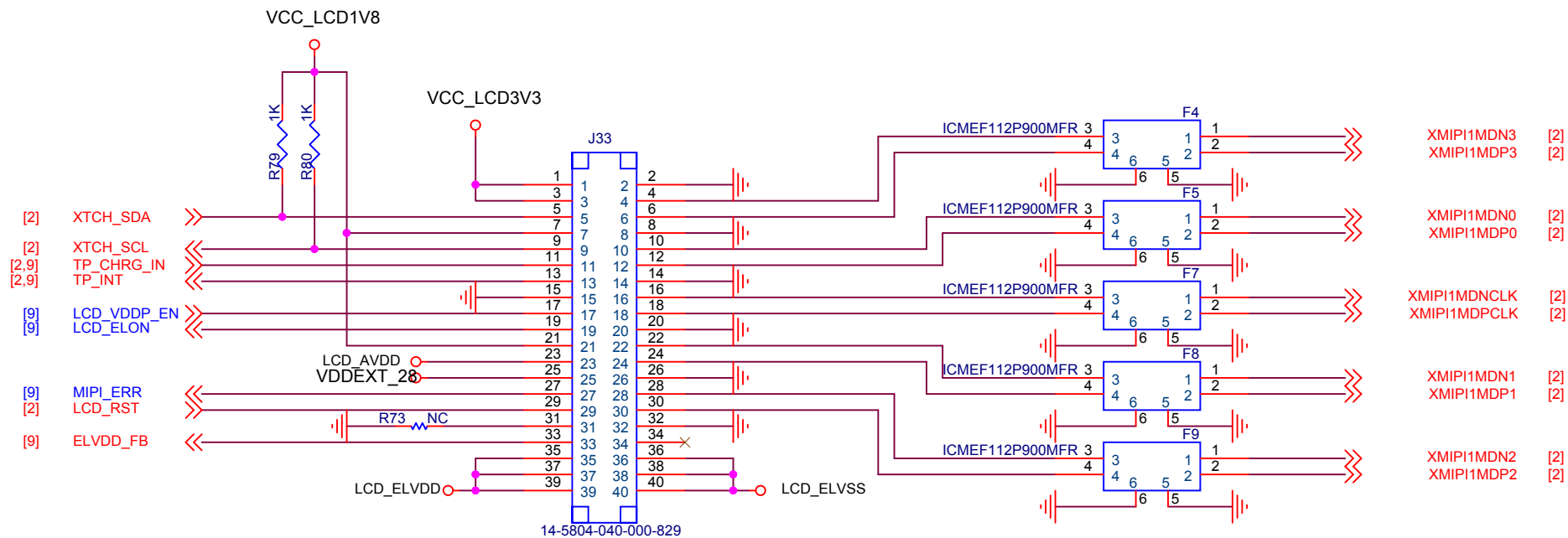
| XOM3 | XOM2 | BOOT MODE |
|------|------|-----------|
| 0 | 1 | SD BOOT |
| 1 | 0 | eMMC BOOT |

POWER



| SW 2 | SEL_IF |
|------|--------|
| 1 | Artik |
| 0 | |

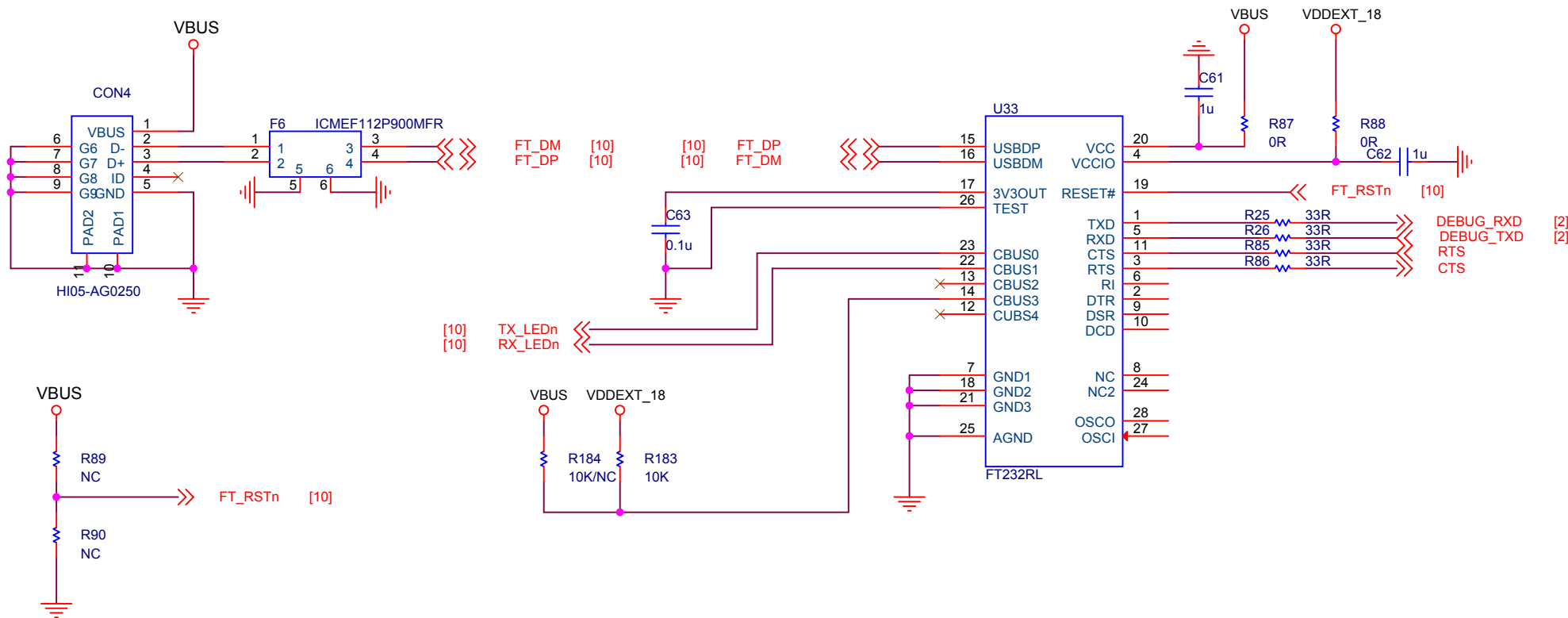
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| POWER | | |
| Title | Document Number | Rev 0.5 |
| Size A3 | | |
| Date: Wednesday, December 23, 2015 | | |
| Page 8 of 18 | | |



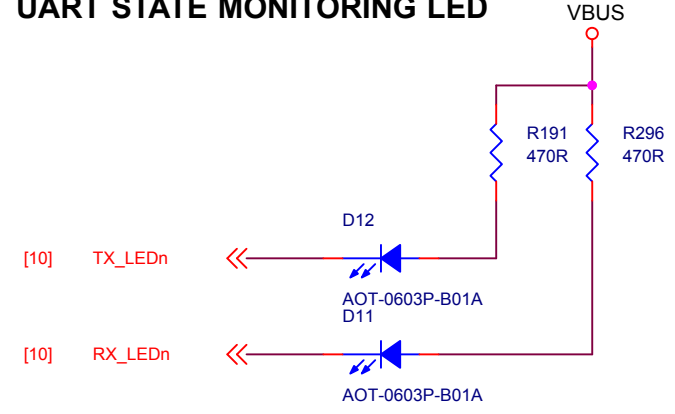
DISP POWER CIRCUIT

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| | | |
|---|-----------------|-------------------|
| Title | | |
| LCD INTERFACE | | |
| Size A4 | Document Number | Rev 0.5 |
| Date: Wednesday, December 23, 2015 | | |
| Page 9 of 18 | | |



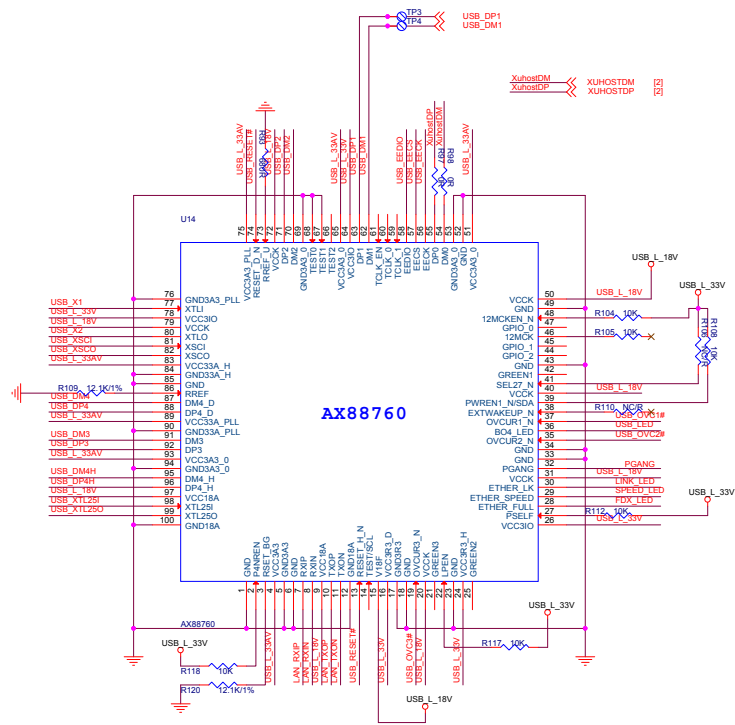
UART STATE MONITORING LED

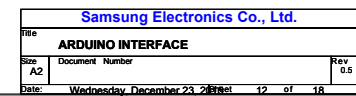


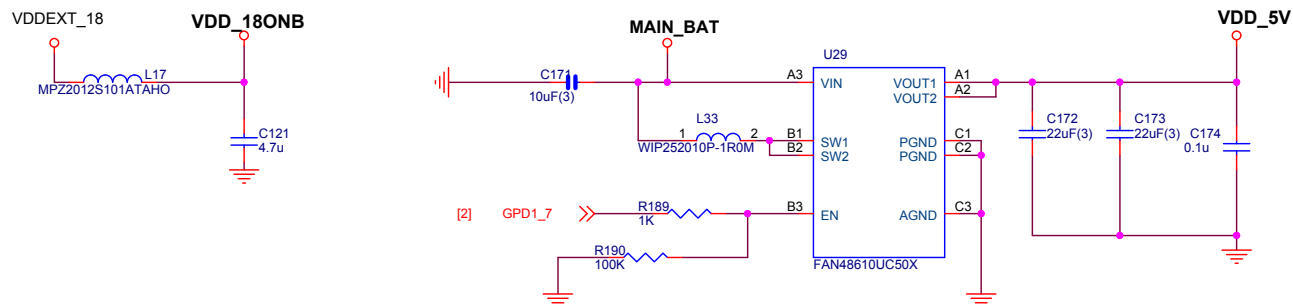
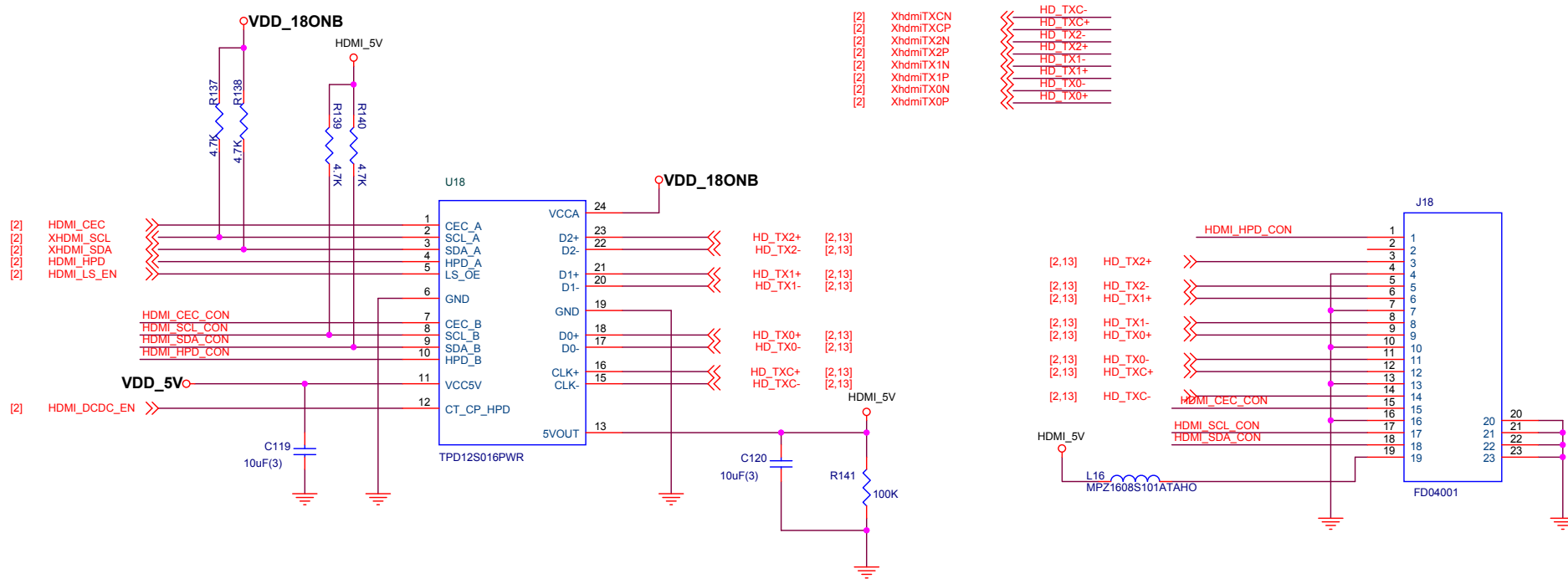
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| Samsung Electronics Co., Ltd. | | | |
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| UART INTERFACE | | | |
| Size | Document Number | | Rev |
| A4 | | | 0.5 |
| Date: | Wednesday, December 23, 2015 | | |
| | 2 | 10 | of 18 |

The image contains three circuit diagrams:

- Top Diagram:** Shows a USB_L_33V supply connected to two LEDs. The first LED is labeled USB_LED and is connected through a 330R resistor (R130) to the D+ pin of an AOT-0603P-B01A LED. The second LED is labeled PDK_LED and is connected through a 330R resistor (R131) to the D- pin of another AOT-0603P-B01A LED.
- Middle Diagram:** Shows a USB_L_33V supply connected to a 10K resistor (R132). The other end of R132 is connected to a signal line labeled XUH0STPWREN. This signal line is also connected to a 4.7K resistor (R134) which is connected to a USB_RESET# pin.
- Bottom Diagram:** Shows a PGANG supply connected to a 1K resistor (R135). The other end of R135 is connected to a signal line labeled D10, which is connected to an AOT-0603P-B01A LED. Below this, there are two pairs of pins labeled USB_DM4 and USB_DP4, each with a 180nF capacitor connected between them.

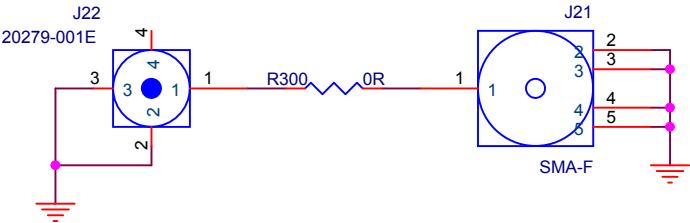
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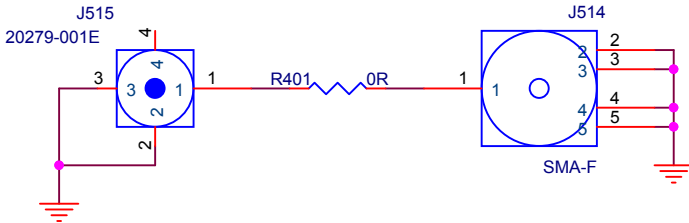




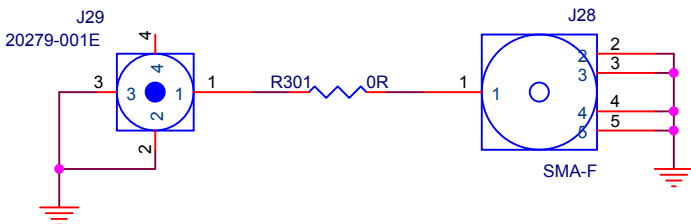
CPU ZIGBEE ANT



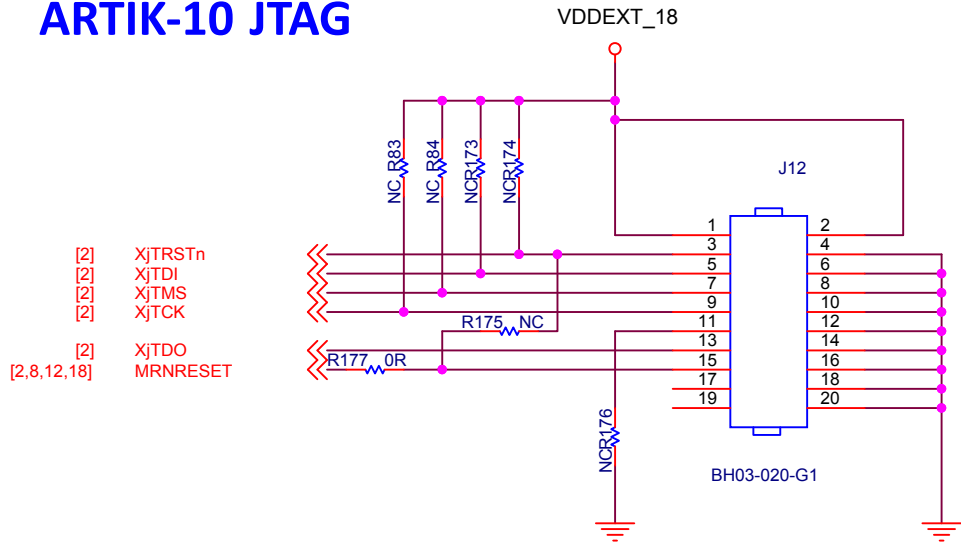
CPU WIFI ANT



CPU WIFI ANT



ARTIK-10 JTAG



ZIGBEE DN TOOL

