

HPS SVT Operations Manual

v2.1

SVT On-Call Cell Phone: 757-310-7198

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0.1 Contact List

System	Experts
General expert	Tim Nelson
Motion and wire scanner	Matt Solt, Tim Nelson
SVT DAQ	Cameron Bravo, Omar Moreno
SVT EPICS controls	Omar Moreno, Matt Solt
MPOD power supply	Omar Moreno
PLC interlocks	Brian Eng
Cooling	Tim Nelson, Matt Solt

Chapter 1

System Description

The SVT, shown in Figure 1.1, uses 7 layers of silicon extending from 5 cm to 90 cm downstream of the target inside of the PS vacuum chamber to measure charged particle trajectories. To accommodate the passage of the beam, the SVT is built in two halves, top

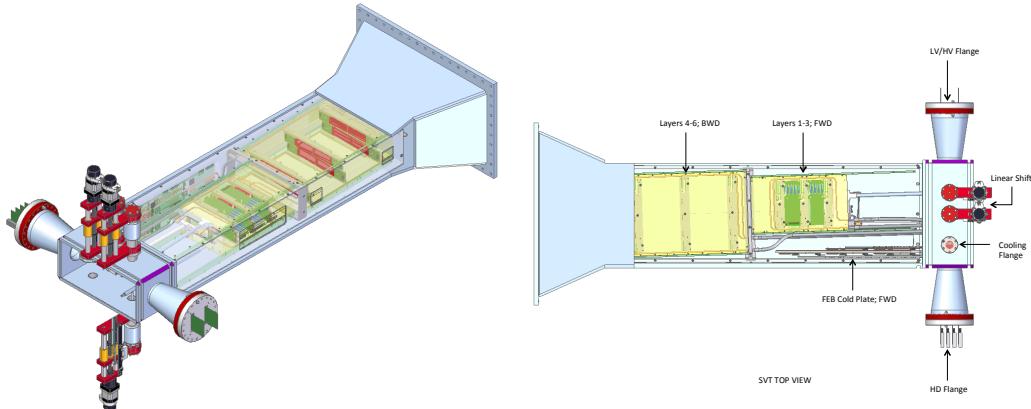
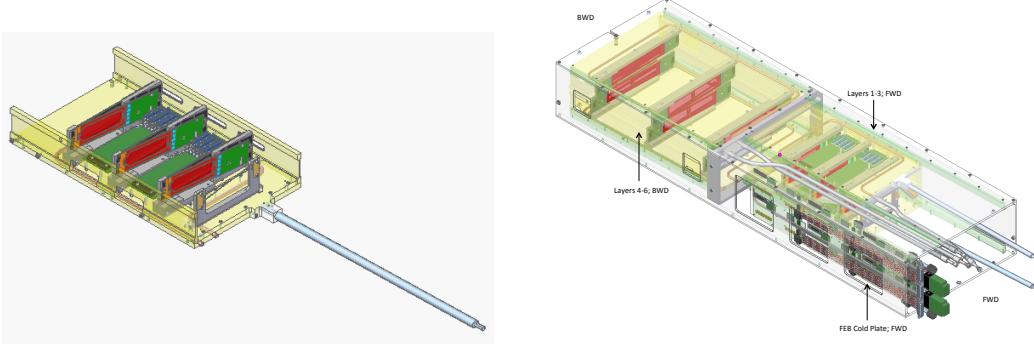


Figure 1.1: The SVT shown inside of the pair spectrometer vacuum chamber. The beam enters through the flange in the front of the vacuum box extension that houses services for the SVT. These images show the detector before the addition of Layer 0 and upgrade of Layer 1.

and bottom, so that each layer consists of a pair of modules, one above and one below the beam plane. Each module uses silicon microstrip sensors placed back-to-back with a small stereo angle between sides to provide 3-d space points for the hits in a module. Modules for Layers 0 and 1 have a single sensor on each side with readout at both ends, where the sense strips are split along the midline of the sensor into pairs of striplets. These new modules use slim-edge silicon which allows them to be placed closer to the beam than during previous runs. Modules for layers 2 and 3 have a single sensor on each side with readout at one end, while those for layers 4-6 are longer, with a pair of sensors on each side and readout at both ends.

Modules are supported in groups of three by a set of four support plates, as shown in Figure 1.2(a). The top and bottom support plates for the back half of the SVT (layers 4-6) are stationary. However, the supports for layers 0-3 can be opened and closed vertically around the beam, rotating around hinges behind layer 3 and moved by levers extending upstream to a pair of linear shifts outside of the magnet. The support plates are kinematically mounted inside a support box that installs into the pair spectrometer (PS) vacuum chamber, shown in Figure 1.2(b).



(a) The lower support plate for Layers 0-3, showing the silicon (red) and readout electronics (green) of the modules, as well as the motion lever for opening and closing the SVT and the SVT beam scan wires. This image shows the detector before the addition of Layer 0 and upgrade of Layer 1.

(b) The SVT support box which contains the upper and lower support plates for Layers 0-3 and Layers 4-6, as well as the cooling plate housing the Front End Boards of the SVT DAQ.

Figure 1.2: Key sub-assemblies of the SVT.

The first stage of readout electronics is located on a hybrid circuit board at the end of each sensor. Multiplexed analog signals from these boards are digitized by a set of 10 Front End Boards (FEBs) mounted to a separate cooling plate inside the SVT support box. Each FEB can control 4 hybrid/sensor units: a single module in layers 4-6 and two modules in layers 0-3. The FEBs also control the hybrids, provide regulated low-voltage power from a single input, and pass externally generated bias voltages (HV) through to the sensors. The FEBs communicate with a set of 4 Signal Flange Boards (SFB), up to three per SFB, which transmit digital signals through the vacuum penetration. The exterior side of each flange board converts digital to optical signals for communication with the RCE DAQ. Power to the FEB are routed through a pair of Power Flange Boards, one for LV and one for HV, supplied by Wiener MPOD power supply modules in a crate on the pie-tower.

Cooling for the SVT is provided by a pair of chillers; one for the hybrids and sensors that operate at -20 °C and one for the FEBs that operates at room temperature. The FEBs have a single cooling loop, while the hybrids and sensors have two loops, one for the top and one for the bottom half of the SVT, where each of these loops runs first through the support structure for layers 0-3 and then through the structure for layers 4-6. There are



Figure 1.3: The Wiener power supply crate for the SVT.

temperature sensors on every hybrid as well as sensors in the FEBs. The linear shifts, the cooling penetrations, and the signal/power penetrations are all located on a set of flanges on an extension vacuum box mounted to the upstream end of the PS vacuum chamber and which connects to the upstream beam line.

Since the SVT sensors are close to the beam, some attention to beam conditions is required prior to turning the SVT on and taking data. A number of systems provide the information used to assess whether beam conditions allow SVT operation. These systems include beam position monitors along the beamline, wire scanners located upstream of the HPS chicane, the wire attached to the SVT itself, a protection collimator located just upstream of the HPS chicane, and beam halo counters located along the beamline downstream of the collimator and signals from the HPS ECal which serve together as beam background monitors. A detailed description of these systems and their operation can be found in the Beamline Operations Manual.

1.1 Voltages

The power and bias needed to operate the SVT is supplied by low voltage and high voltage power supply modules inside a 10-slot Wiener MPOD crate, see Fig. 1.3. The crate is located in a rack in the pie tower with PTFE (teflon) insulated twisted pair copper wires used to bring power to the SVT in the Hall-B alcove.

The low voltage power is supplied by Wiener MPV8008I low voltage power supply modules. Each module has 8 output channels (floating, with sense lines). Each of the five modules supplies power to two FEBs.

ISEG EHS F201p_805F high precision high voltage modules provide the reverse bias voltage to the silicon sensors of the SVT. Each module has 16 output channels (floating). The three modules each supply bias to 12 sensors: SVT L0-3, L4-6 top or L4-6 bottom. The bias supply to Layer 0 and Layer 1 are ganged, so that a single supply provides bias voltage to the sensors on both sides of the modules. All other sensors in the detector have individual bias supply.

The last two slots in the crate are occupied by spare modules (one LV and one HV).

1.1.1 Control and Monitoring

The SVT power supply and bias is fully controlled through GUIs to the EPICS control system. EPICS controls the MPOD crate directly through its SNMP interface which supply the FEBs with power. The FEBs regulate and distribute power down to the hybrid boards of the SVT sensor modules. The control and monitoring of the hybrid power (and temperature) is handled by an EPICS IOC running on the SVT DAQ crate.

Both cooling systems must be running before the MPOD can be turned on; this is enforced by an interlock. Also, the FEBs must be configured before any hybrids can be powered.

1.2 Cooling and Interlocks

The SVT cooling system, summarized in Table 1.1 and Figure 1.4, controls the temperature of the sensors and removes heat dissipated by the electronics. Since the SVT is in vacuum,

Location	Set Point	Resistive Load	Radiative Load
SVT modules	-5 °C	75 W	10 W
Piping to SVT modules	-10 °C	100 W	0
Set point, SVT chiller	-10 °C	-	-
Front End Boards	10 °C	100 W	0
Set point, FEB chiller	4 °C	-	-

Table 1.1: SVT cooling design parameters.

the cooling system is key for the safety and performance of the detector. The main heat sources are the sensor modules and their hybrid circuit boards and the Front End Boards (FEBs) which distribute power and DAQ to reduce the number of wires that must penetrate the vacuum flanges. An additional heat load results from the radiative heat exchange between the SVT U-support and the detector vacuum. The sensor modules need to be controlled to a sub-zero temperature, while the FEBs only need to be thermally managed to remove heat produced by their operation. While the FEBs have typically been operated close to room temperature, the degradation of some components on the FEBs requires them to be cooled to 10-15 °C in order to maintain operability of some parts of the detector. To maintain the ability to set the silicon and FEB temperatures separately, there are two separate chillers

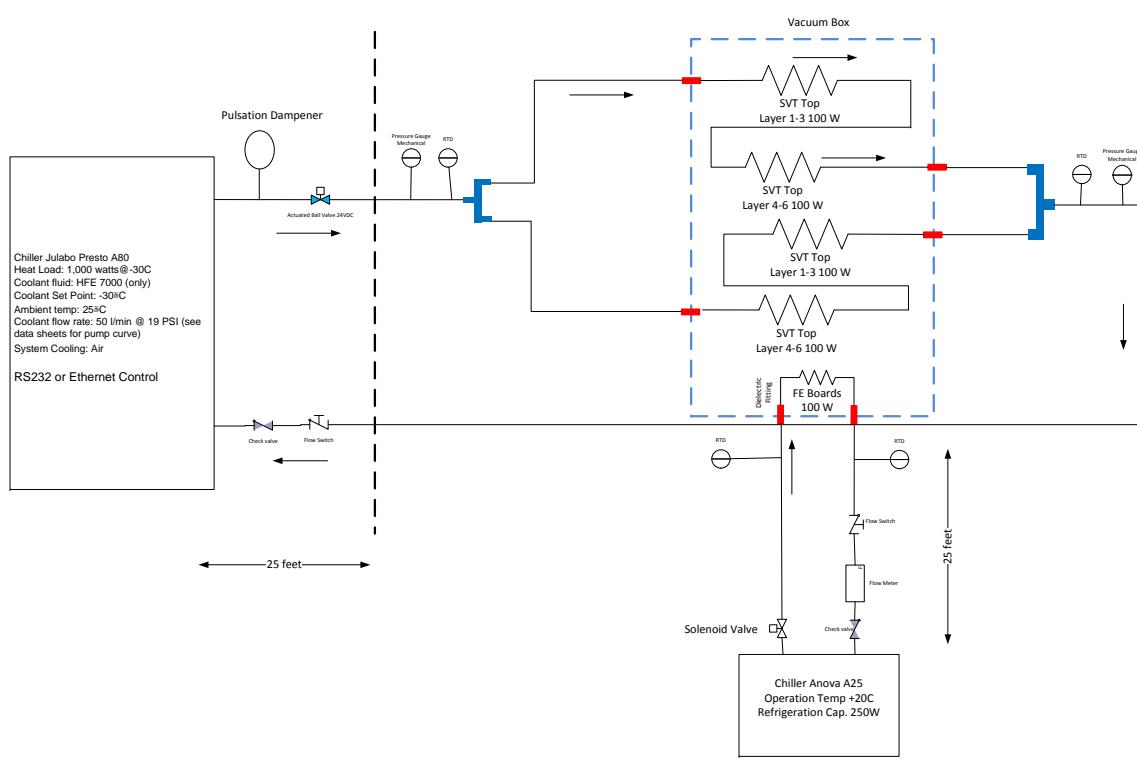


Figure 1.4: The SVT cooling system.

with independent loops and controls. The dew point in Hall B is typically \sim 15 °C. The cold lines from the chillers to the vacuum box is insulated with pipe insulation.

All the instrumentation to control the cooling system will be placed in air, close to the chillers, with an identical layout for the two, as shown in Figures 1.4.

The cooling loop for the sensor modules is connected to a Julabo Chiller, Model Presto A80, which is set at -10 °C, but may be lowered to -20 °C if necessary to reduce bias currents. The heat transfer fluid is HFE-7000, a Fluorinert engineered fluid long used as heat transfer media for cooling applications. However, according to availability, HFE-7100 or HFE-7500 may also be used: all of these are cross-compatible. The inlet and the outlet lines are split to provide cooling in parallel to the top and the bottom parts of the detector. They then pass through flanges on the detector vacuum box, where they provide cooling in series to the L0-3 and L4-6 parts of the detector. The cooling pipes in vacuum are flexible metal hoses from the vacuum feed through to the detector, while the cooling lines built in the detector are 0.25" rigid copper pipe. The flexible lines are brazed on one end to the rigid copper pipes of the detectors and connected with VCR fittings to the vacuum feedthroughs on the other end. The cooling feed through have a ceramic transition spool to insulate electrically the cooling line from the vacuum box. Dielectric fittings to break the electrical continuity of the cooling loop are placed outside the vacuum. The estimated operating mass flow of the system is \sim 10 g/s, providing 5 g/s per cooling channel with a pressure drop \sim 1 bar. The cooling loop inside vacuum is pneumatically tested up to 20 bars without showing any sign of failure.

For the SVT chiller: Two Pt100 temperature sensors are located at the supply and return sides of the SVT manifold (glued to the tee junctions). Two mechanical pressure gauges are located at the supply and return sides of the SVT manifold, just before the tee junctions. A piston flow switch at the return side of the chiller (fixed setpoint of 0.25 GPM) checks that the flow going to the detector is acceptable. A check valve after the flow switch prevents a leak in the cooling loop from draining the chiller. A servo-actuated ball valve at the outlet of the chiller stops the cooling flow when requested by the slow control system (this valve takes \sim 5 seconds to open or close). A pulsation dampener at the outlet of the chiller absorbs vibrations from the pump. The temperature sensors and the flow meter are interfaced with the Slow Control as diagnostic, while the actuated ball valve valve is interlocked. Additional diagnostics are available through the temperature sensors built into the SVT modules and the functional parameters of the chiller like set point temperature and pump pressure, which are controlled through an RS232 software interface.

The FEB cooling plate has a similar cooling loop, which connects to a similar vacuum feedthrough on the front face of the vacuum chamber. The cooling fluid is deionized water. Two Pt100 temperature sensors are glued to the cooling lines next to the feedthrough. A paddlewheel flow switch (adjustable setpoint, normally 6-15 GPH) at the return side of the chiller checks that the flow going to the detector is acceptable. A check valve after the flow switch prevents a leak in the cooling loop from draining the chiller. A solenoid valve at the outlet of the chiller stops the cooling flow when requested by the slow control system. The temperature sensors and the flow meter are interfaced with the Slow Control as diagnostic,

while the solenoid valve is interlocked. Additional diagnostics are available through the temperature sensors built into the FEBs and the functional parameters of the chiller like set point temperature, which are controlled through an RS232 software interface.

1.2.1 Sensors and inputs

FEB cooling loop

- To PLC: Flow switch (adjustable setpoint, set around 10 GPH with reference to flow meter) at chiller return
- To PLC: 2x RTD on supply and return, near vacuum feedthroughs
- Through SVT DAQ: temperature sensors on FEBs
- From FEB chiller (via RS-232): temperature readout, status

SVT cooling loop

- To PLC: Flow switch (fixed setpoint, 0.25 GPM) at chiller return
- To PLC: 2x RTD on supply and return, at manifold (before lines split to top and bottom halves of the SVT)
- Through SVT DAQ: temperature sensors on hybrids
- From SVT chiller (via RS-232): temperature readout, outlet pressure, status
- Local readout only: 2x pressure gauges, on supply and return at manifold

The sensors with PLC or RS-232 readout are monitored through the EPICS slow controls system and displayed in the monitoring GUIs for the chillers and the PLC. The SVT alarm handler has alarms defined for the temperatures: see the SVT monitoring section.

1.2.2 Control outputs

The PLC and chillers can be controlled through EPICS. These control outputs can be used in PLC or EPICS interlocks.

FEB cooling loop

- From PLC: Solenoid valve at chiller outlet
- On FEB chiller (via RS-232): start/stop, temperature setpoint, pump speed

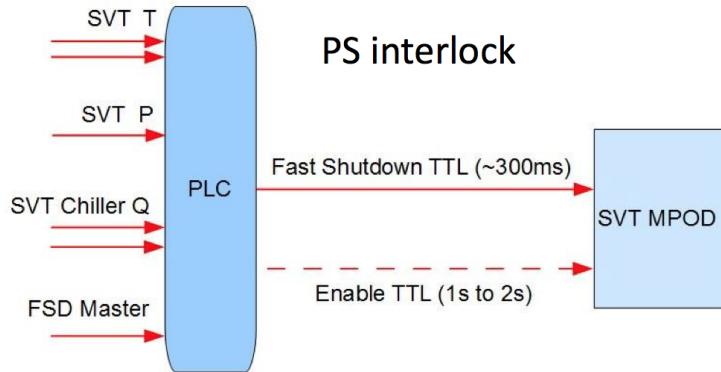


Figure 1.5: Diagram of the SVT interlocks.

SVT cooling loop

- From PLC: Actuated ball valve at chiller outlet
- From PLC: Chiller circuit breaker
- On SVT chiller (via RS-232): start/stop, temperature setpoint, pump power

Voltages

- From PLC: MPOD enable signal (shuts down all LV and HV channels)
- From MPOD IOC: Controls for individual MPOD channels

1.2.3 Interlocks

To safely operate the SVT, the power supplies are included in an interlock system that triggers a fast shutdown of the power supply crate based on a set of conditions. A schematic view of the interlock system that triggers a SVT power supply interlock signal is shown in Fig. 1.5. The interlock system is based on an Allen-Bradley PLC that receives input signals and performs the necessary logic to issue an interlock signal. The interlock signal that triggers a fast shutdown of the SVT power supplies are:

- Hybrid and front end board temperature
- Input and output coolant temperature of the SVT hybrid and front end board cooling loops.
- Coolant fluid pressure in the SVT hybrid and front end board cooling loops.
- Chiller setting and status of the SVT and hybrid and front end board chillers.

Each power control interface (flange board, front-end board, hybrid, bias) shows the status of the interlock signal that allows each system to be powered.

The solenoid valves are interlocked to mitigate any leaks in the cooling system. If the flow meter in either cooling loop detects low flow, the valve at that chiller's outlet closes to prevent coolant from being pumped out of a possible leak in that loop. If the vacuum gauge detects a loss of vacuum, both solenoid valves close in case the vacuum loss is due to a coolant leak inside vacuum.

1.2.4 Potential accidents

Coolant leakage inside vacuum

A small leak inside the vacuum will be detected by the vacuum gauges. A large leak will trip the flow switch.

Coolant leakage outside vacuum

A small leak will drain the chiller until its level switch trips, then the flow switch will trip. A large leak will trip the flow switch.

Low level in SVT cooling system

The HFE 7000 fluid is very volatile and is known to evaporate from the chiller reservoir. Any bad seals in the system would increase the rate of loss. The observed rate of loss in testing is less than 1 L/week, and the reservoir capacity is more than 5 L, but the level should be monitored regularly. A low level warning (reported in RS-232 status) will precede a low level alarm (chiller trip).

Chiller failure or abnormal operation

A pump or power failure in a chiller will trip the flow switch.

A refrigerator or control failure may cause the chiller to run at an unexpected temperature. This would be detected by the RTDs.

Blockage in a cooling line

A blockage in the FEB cooling loop, or the SVT cooling lines before the manifold, would reduce total flow in the system. This would be detected by the flow switch or the RTD on the return side.

A blockage in one half of the SVT cooling manifold would reduce flow to half the SVT but might not be detectable by any of the cooling loop instrumentation. The only likely indication is a rise in the SVT hybrid temperatures. Since we don't expect this to happen suddenly or require rapid intervention, we will set an alarm on the SVT hybrid temperatures but no interlock.

Beam trip

The SVT HV must be shut off following a trip, to ensure that the HV is not accidentally left on while the beam is being brought back.

1.2.5 Interlocks for operation with beam

All interlock actions are triggered by PLC inputs. Actions are divided into PLC (fast and reliable, since the inputs, logic and outputs are all internal to the PLC) and EPICS (slower, less reliable, less critical)

Cooling loop failure (high or low temperature on either RTD, or flow switch trip)

Trip points must be adjustable to allow for operation at different temperatures. There must be some sort of interlock bypass for system startup (i.e. it must be possible to start the chiller when temperatures are out of spec and the flow switch is tripped).

- PLC interlock action: Close the valve at the chiller outlet. Disable the MPOD.
- Software interlock action: Turn off the chiller.

Bad vacuum (vacuum gauge)

- PLC and software interlock actions: Same action as cooling loop failure, for both loops. (so chillers will be valved off and shut off, and the MPOD will be disabled)

Beam off (BPM signal)

If the beam is lost, the SVT should automatically go to a safe configuration until beam is restored.

- Software interlock action: Turn off all HV channels.

1.3 Motion

Figure 1.6 shows the SVT Motion System schematically. The top/bottom of each SVT support plate has three sensor layers (L1, L2, L3) and a wire frame, supported on a bearing at one end and connected to a rod attached to a linear stage at the other end. The linear stage is moved by a stepper motor through the EPICS slow controls system. In the nominal run position, the support plates are parallel to the beam and the Silicon physical edges are at 0.5 (L0), 0.85 (L1), 2.0 (L2), and 3.5 mm (L3) from the beam. The support plates can be retracted by 1.3 degrees. Figure 1.7 shows the beam's eye view of the wires and the L0 sensor edge in the nominal run position and in the fully retracted position.

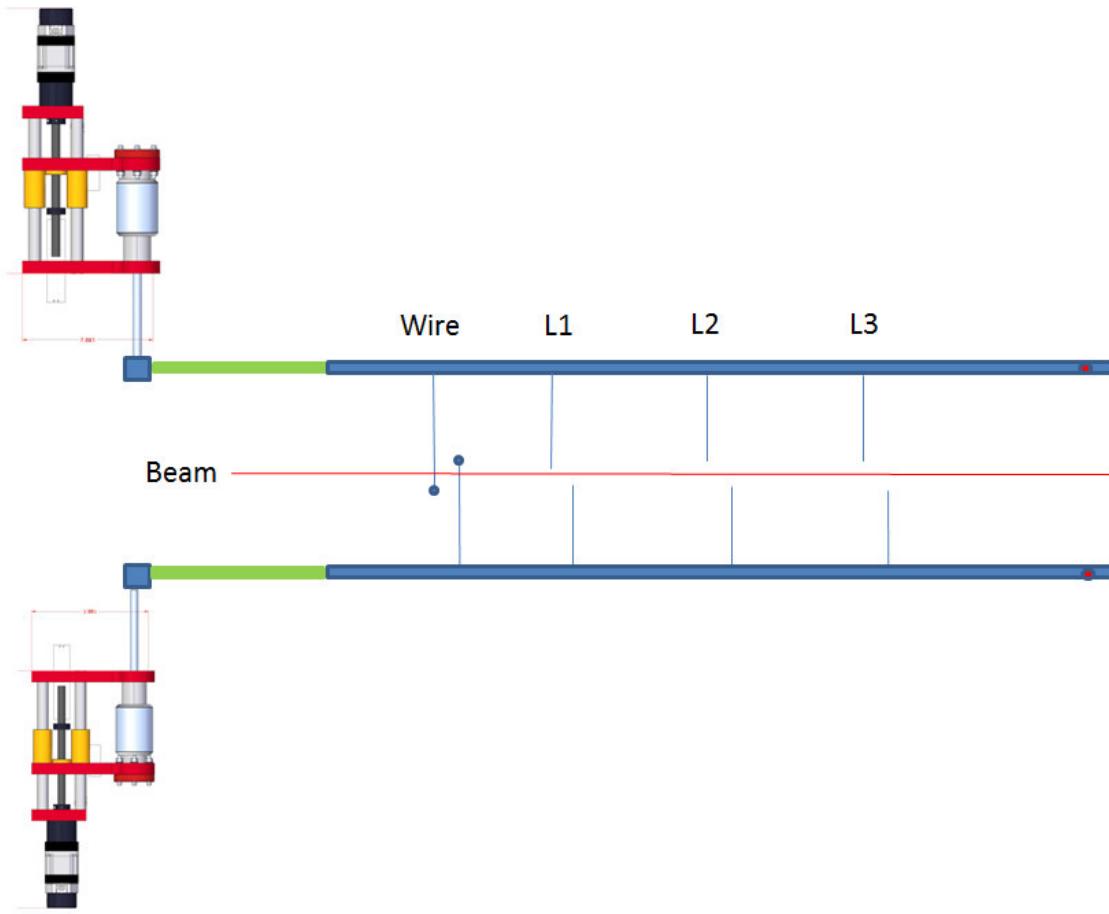


Figure 1.6: SVT Motion System

The stepper motor controller is Newport XPS controller, and is different from the stepper motor controllers found in Hall B. The motor control is based on an optical encoder. Stage movement is precisely calibrated, and re-calibration is not necessary. Since the encoder has no knowledge of the actual stage position when the controller power is turned on, the linear stage must move to a reference point to establish its calibration (called “homing”). A switch is located at the fully retracted position for this purpose.

Three vertical coordinates are used to locate the stage, the horizontal wire and the layer 1 sensor physical edge. The stage coordinate is $y(\text{stage}) = 0 \text{ mm}$ at the fully retracted position and $y(\text{top stage}) = 18.136 \text{ mm}$ and $y(\text{bottom stage}) = 18.212 \text{ mm}$ at the nominal run position with the silicon 0.5 mm from the nominal beam plane. The wire and sensor edge coordinates are measured relative to the nominal beam position (positive y is up), and are given in terms of $y(\text{stage})$ as,

When the beam is first delivered to Hall B, the vertical position of the beam is known to about 1 mm. Since we want to place the SVT layer 0 physical edge at 0.5 mm from the beam, SVT Wire Scanner is used to measure the beam position relative to the sensor edge with

a precision of about $50 \mu\text{m}$. The wire scanner has a $20 \mu\text{m}$ diameter gold-plated tungsten horizontal wire and a $30 \mu\text{m}$ diameter gold-plated tungsten angled wire. The angled wire is at 8.904 degrees to the horizontal wire, and is separated by $y=1.98 \text{ mm}$ at the nominal beam position. The Y position of the beam can be measured directly from the horizontal wire. If ΔY is the apparent Y position difference of the measured beam “gaussian” centers, the X position of the beam can be obtained from $X = (\Delta Y - 1.98)/\tan(8.904)$.

- $y(\text{top-wire}) = -0.4816 \cdot y(\text{stage}) + 1.0516 \text{ mm}$,
- $y(\text{top-si}) = -0.4509 \cdot y(\text{stage}) + 8.6242 \text{ mm}$,
- $y(\text{bottom-wire}) = +0.4648 \cdot y(\text{stage}) - 1.0730 \text{ mm}$,
- $y(\text{bottom-si}) = +0.4227 \cdot y(\text{stage}) - 8.2150 \text{ mm}$.

As the coordinate transformation is done automatically, all you need to specify is the position of the wire or Si sensor edge to operate the SVT Mover and the SVT Wire Scanner.

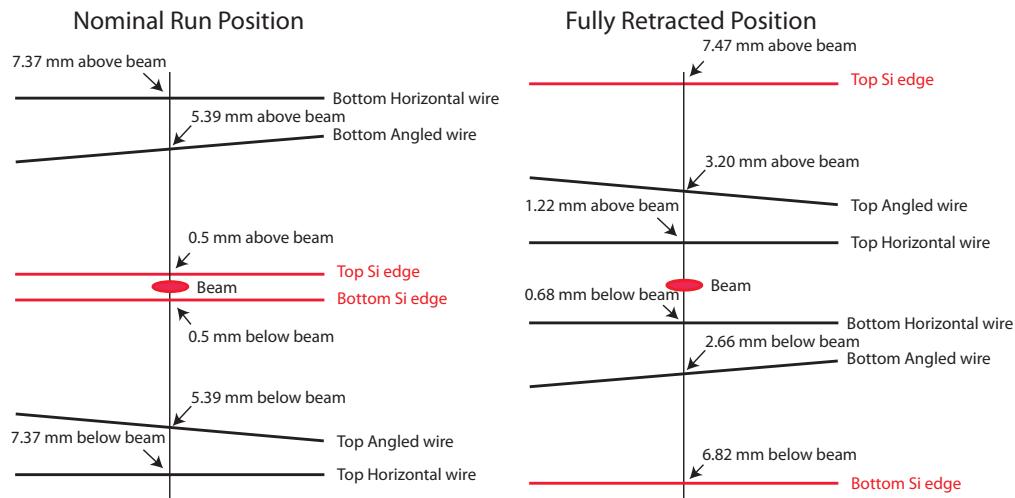


Figure 1.7: Beam's eye view of wires and L0 sensor edge

Chapter 2

Controls and monitoring

2.1 Voltages

The MPOD power supply crate can be accessed directly by pointing a web browser at <http://hpsmpod/>. This shows channel voltages, currents and status directly.

The SVT power and bias as well as alarms are monitored through several GUI's. All GUI's are accessible from the main HPS control screen. To start it:

1. Log into any computer in the counting house.
2. Open a terminal and issue `hps_epics` command to open the main HPS GUI. Figure 2.1 show the SVT part of the GUI open.
3. Each of the individual GUIs can be opened by clicking the appropriate button in the HPS main GUI that opened.

Below is a description of each of the GUI's in the list.

- **DAQ IOC Status** in Fig. 2.2.
 - Monitoring: status and error codes from all SVT DAQ IOCs.
- **DPM Status** in Fig. 2.3.
 - Monitoring: run state and event counts from the data processing nodes in the SVT DAQ.
- **DPM Link Status** in Fig. 2.4.
 - Monitoring: link errors for each of the data processing nodes in the SVT DAQ.
- **Temperature** in Fig. 2.5.
 - Monitoring: FEB (3 temperatures),

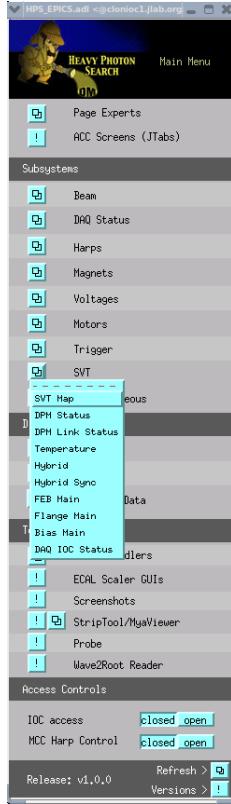


Figure 2.1: HPS EPICS main GUI.

- Monitoring: FEB cooling temperature on the supply and return at the PS magnet for the FEB and SVT chiller.
- **Hybrid GUI** in Fig. 2.6.
 - Monitoring: all measured voltage and currents from hybrids.
 - Monitoring: temperature from hybrids.
 - Control: ON/OFF switch for each of the three voltages for individual hybrids and all hybrids.
- **Hybrid Sync** in Fig. 2.7.
 - Monitoring: sync status for each hybrid.
 - Monitoring: base and peak value of the sync pulse from each hybrid. (EXPERT ONLY).
- **FEB Power GUI** in Fig. 2.8.
 - Monitoring: all currents and voltages of the FEB.

svtDaqIOCstatus.adl <@clonioc1.jlab.org>

SVT DAQ IOC STATUS

	Heartbeat	Code	Connection	XML
Control DPM	●	0		
Data DPM 0	●	-1	socket opened	Seems OK
Data DPM 1	●	-1	socket opened	Seems OK
Data DPM 2	●	-1	socket opened	Seems OK
Data DPM 3	●	-1	socket opened	Seems OK
Data DPM 4	●	-1	socket opened	Seems OK
Data DPM 5	●	-1	socket opened	Seems OK
Data DPM 6	●	-1	socket opened	Seems OK
Data DPM 7	●	-1	socket opened	Seems OK
Data DPM 8	●	-1	socket opened	Seems OK
Data DPM 9	●	-1	socket opened	Seems OK
Data DPM 10	●	-1	socket opened	Seems OK
Data DPM 11	●	-1	socket opened	Seems OK
Data DPM 12	●	-1	socket opened	Seems OK
Data DPM 13	●	-1	socket opened	Seems OK
DTM 0	●	-1	socket opened	Seems OK
DTM 1	●	-1	socket opened	Seems OK

Figure 2.2: DAQ IOC Status GUI.

svtDpmStatus.adl <@clonioc1.jlab.org>

DPM Status

Data DPM	Run State	Trig Count	Event Count
0	Download	0	0
1	Download	0	0
2	Download	0	0
3	Download	0	0
4	Download	0	0
5	Download	0	0
6	Download	0	0
(ctrl1) dpm7	Download		
7	Download	0	0
8	Download	0	0
9	Download	0	0
10	Download	0	0
11	Download	0	0
12	Download	0	0
DTM			
0	Download	0	
1	Download	0	

Figure 2.3: DPM state GUI.

- Control: ON/OFF switch for all FEB boards.
- Control: ON/OFF switch for each FEB board.
- Control: Set point for voltage, trip and ramp values for each flange board (EXPERT ONLY).
- **Flange power GUI** in Fig. 2.9.
 - Monitoring: all currents to the flange boards.
 - Control: ON/OFF switch for all flange boards.
 - Control: ON/OFF switch for each flange board.
 - Control: Set point for voltage, trip and ramp values for each flange board (EXPERT ONLY).
- **High voltage bias GUI** in Fig. 2.10.
 - Monitoring: all voltage set points for the SVT sensors.
 - Control: ON/OFF switch for all sensors.
 - Control: ON/OFF switch for each sensor.
 - Control: Set point for voltage for all modules (EXPERT ONLY).
 - Control: Set point for voltage and ramp values for each module (EXPERT ONLY).
 - Control: **RESET INTERLOCKS** button to restore normal function after an MPOD trip.

2.2 SVT Cooling

All the GUIs shown below are accessed through the **Devices** menu in **hps_epics**. Chiller and interlock settings should only be modified under the supervision of an SVT Expert.

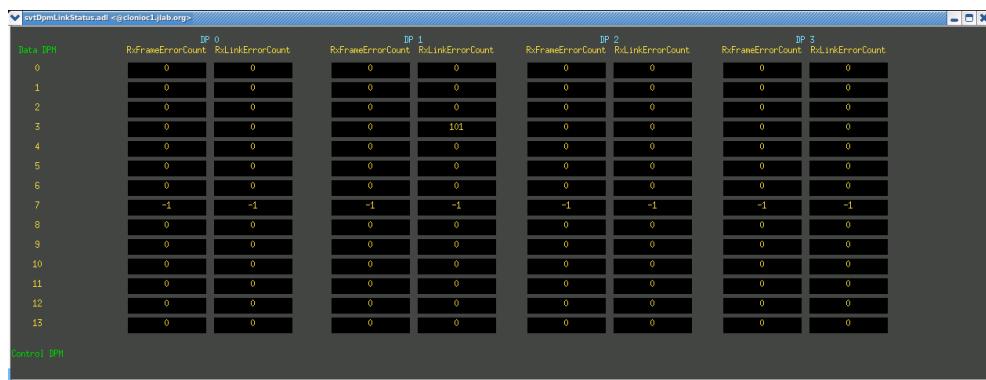


Figure 2.4: DPM link status GUI.



Figure 2.5: Temperature GUI.

Table 2.1 lists the default settings for the SVT and FEB chillers and alarms during running (cold) and between runs when the beam enclosure is open for servicing (warm).

Any major (red) ALH alarm under the “SVT/COOLING” group will send an e-mail alert. Also, there is a webcam (cctv7) pointed at the SVT chiller screen. The webcam can be set to e-mail a snapshot every 24 hours.



Figure 2.6: Hybrid power and temperature GUI.

svtHybSync.adl													
FEB	Physical Layer	Datapath	SyncDetected	APV0		APV1		APV2		APV3		APV4	
				Base	Peak								
0	L2-3b	0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
	L4b	1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
1	L1b	2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
	L6b	3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
2	L5b	0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
	L4t	1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
3	L2-3t	1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
	L6t	2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
4	L5t	3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
	L1t	0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		1	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		2	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		3	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result
		0	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result	no result

Figure 2.7: Hybrid sync GUI.

2.3 Motion

The SVT motor controller can be accessed directly by pointing a web browser at <http://hpsxps/> (SVT expert should know the login).

The SVT movers are controlled using the SVT positioner GUI. The GUI controls the top and bottom halves of the SVT separately. For each half, the GUI shows the position in four ways: stage position, wire-to-beam distance, layer-1-to-beam distance, and angle. Except for the stage position (0 at home, positive as the stage moves in), these are signed values (positive above the beamline, negative under it).

The **Retract Stage** button moves the stage to 0.000 (all the way out). The button is disabled (red border) if this half of the SVT is already all the way out.

The **Move Layer-1** to text box moves the SVT to user-defined positions. Type the desired (signed) value of the layer-1 distance and press **Enter** to move the stage. There is a software limit (a minimum layer-1 distance) to prevent unsafe operation of the SVT. The limits is not displayed on this GUI but are 17.95 mm and 17.54 mm for the top and bottom respectively. These positions correspond to placing the edge of the Layer 1 sensors 0.45 mm from the beam plane.

*Sometimes there is no response to an input in the text box. If this happens, one workaround is to open the motor expert GUI (click the **Expert Screens** button), and enter a value in*

ALL FEB CONTROL		DIGI	ANAP	ANNN	RELOAD ALL FPGAs															
FEB	Phys	Measured Voltage	Measured Sense Voltage	Voltage Setpoint	Voltage Readback	LV On/Off	Channel Status	Measured Current	Max Current	Max Current	Trip Current	Max Sens Voltage	Max Sens Voltage	Max Term Voltage	Max Term Voltage	Ramp Up Rate	Ramp Up Rate	Ramp Down Rate	Ramp Down Rate	
INDIVIDUAL FEB CHANNEL CONTROL																				
100	FEB	ANNN	5,499	6,214	0,000	5,500	OFF ON	0,239	0,000	2,000	0,000	0,500	0,000	8,000	0,000	8,000	0,000	0,000	0,000	0,000
9	L1t	ANAP	5,499	6,150	0,000	5,500	OFF ON	1,181	0,000	5,000	0,000	4,000	0,000	8,000	0,000	8,000	0,000	0,000	0,000	0,000
	DIGI	5,499	6,346	0,000	5,500	OFF ON	1,097	0,000	3,000	0,000	1,500	0,000	8,000	0,000	8,000	0,000	0,000	0,000	0,000	0,000
6	L2-3t	ANNN	5,497	5,188	0,000	5,500	OFF ON	0,228	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
6	L2-3t	ANAP	5,503	6,173	0,000	5,500	OFF ON	1,208	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,503	7,001	0,000	5,500	OFF ON	1,090	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
8	L5t	ANNN	5,502	6,213	0,000	5,500	OFF ON	0,230	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
8	L5t	ANAP	5,502	6,180	0,000	5,500	OFF ON	1,203	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,504	7,035	0,000	5,500	OFF ON	1,082	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
7	L6t	ANNN	5,501	6,228	0,000	5,500	OFF ON	0,229	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
7	L6t	ANAP	5,503	6,193	0,000	5,500	OFF ON	1,204	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,500	7,067	0,000	5,500	OFF ON	1,095	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
5	L4t	ANNN	5,501	6,238	0,000	5,500	OFF ON	0,228	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
5	L4t	ANAP	5,498	6,176	0,000	5,500	OFF ON	1,193	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,500	7,049	0,000	5,500	OFF ON	1,087	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
1	L4b	ANNN	5,500	6,223	0,000	5,500	OFF ON	0,229	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
1	L4b	ANAP	5,500	6,174	0,000	5,500	OFF ON	1,187	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,499	7,073	0,000	5,500	OFF ON	1,104	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
4	L5b	ANNN	5,499	6,298	0,000	5,500	OFF ON	0,229	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
4	L5b	ANAP	5,499	6,180	0,000	5,500	OFF ON	1,191	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,500	7,065	0,000	5,500	OFF ON	1,098	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
3	L6b	ANNN	5,499	6,244	0,000	5,500	OFF ON	0,234	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
3	L6b	ANAP	5,500	6,174	0,000	5,500	OFF ON	1,192	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,501	7,067	0,000	5,500	OFF ON	1,091	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
2	L1b	ANNN	5,499	6,243	0,000	5,500	OFF ON	0,235	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
2	L1b	ANAP	5,499	6,153	0,000	5,500	OFF ON	1,189	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,503	6,989	0,000	5,500	OFF ON	1,099	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000
0	L2-3b	ANNN	5,498	6,208	0,000	5,500	OFF ON	0,233	0,000	2,000	0,000	0,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
0	L2-3b	ANAP	5,499	6,191	0,000	5,500	OFF ON	1,191	0,000	5,000	0,000	4,000	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000
	DIGI	5,500	6,985	0,000	5,500	OFF ON	1,078	0,000	3,000	0,000	1,500	0,000	8,080	0,000	8,080	0,000	0,000	0,000	0,000	0,000

Figure 2.8: FEB power GUI.

Channel Name	Slot Channel	Measured Sense Voltage	Measured Terminal Voltage	Voltage Setpoint	Voltage Readback	LV On/Off	Channel Status	Measured Current	Max Current	Max Current	Trip Current	Max Sens Voltage	Max Sens Voltage	Max Term Voltage	Max Term Voltage	Ramp Up Rate	Ramp Up Rate	Ramp Down Rate	Ramp Down Rate
SVT FLANGE 0		5,500	6,569	0,000	5,500	OFF ON	0,567	0,000	3,000	0,000	1,000	0,000	8,080	0,000	8,080	0,000	100,000	0,000	100,000
SVT FLANGE 1		5,505	6,791	0,000	5,500	OFF ON	0,688	0,000	3,000	0,000	1,000	0,000	8,080	0,000	8,080	0,000	100,000	0,000	100,000
SVT FLANGE 2		5,499	6,575	0,000	5,500	OFF ON	0,562	0,000	3,000	0,000	1,000	0,000	8,080	0,000	8,080	0,000	100,000	0,000	100,000
SVT FLANGE 3		5,502	6,761	0,000	5,500	OFF ON	0,652	0,000	3,000	0,000	1,000	0,000	8,080	0,000	8,080	0,000	100,000	0,000	100,000

Figure 2.9: Flange power GUI.

either **MoveAbs** field. This seems to kick the motor control out of whatever bad state it gets stuck in.

2.4 Online Monitoring

The SVT will be monitored online using the Java based HPS Monitoring Application. The most basic set of plots can be brought up by issuing the following commands from a terminal:

- Log into any PC in the counting house as `hpsrun`
- Issue the command `startSVTHitPlots` to open the monitoring app

Pressing the connect button will bring up plots of SVT occupancies, raw hit counts and raw hit ADC sample amplitudes. Pressing it again will disconnect the monitoring app and reset all plots.

The command `startSVTHitPlots -h` will print a list of additional options.

Channel	Name	FEB	Hubrist	Gate	Measured Voltage	Voltage Setpoint	Voltage Readback	H _i On/Off	Channel Status	Measured Current	Trip On	Trip Off	Purge Up Setpoint	Purge Up Readback	Purge Down Setpoint	Purge Down Readback
T0	L1t	0			180.003	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T1	L1t	1			179.998	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T2	L2-2t	0			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T3	L2-2t	1			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T4	L2-2t	2			179.999	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T5	L2-2t	3			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T6	L5t	2			180.000	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T7	L5t	3			180.000	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T8	L5t	0			179.990	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T9	L5t	1			179.990	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T10	L5t	2			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T11	L5t	3			179.999	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T12	L1b	0			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T13	L1b	1			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T14	L2-3b	0			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T15	L2-3b	1			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T16	L2-3b	2			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
T17	L2-3b	3			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B0	L4t	0			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B1	L4t	1			180.004	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B2	L4t	2			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B3	L4t	3			179.993	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B4	L5t	0			179.999	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B5	L5t	1			179.999	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B6	L4b	0			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B7	L4b	1			180.005	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B8	L4b	2			180.004	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B9	L4b	3			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B10	L5b	0			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B11	L5b	1			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B12	L5b	2			180.000	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B13	L5b	3			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B14	L5b	0			180.002	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B15	L5b	1			179.999	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B16	L5b	2			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000
B17	L5b	3			180.001	0.000	180.000	[OFF] ON	[]	0.000	0.000	0.000	0.000	10.000	0.000	10.000

Figure 2.10: High voltage bias GUI.

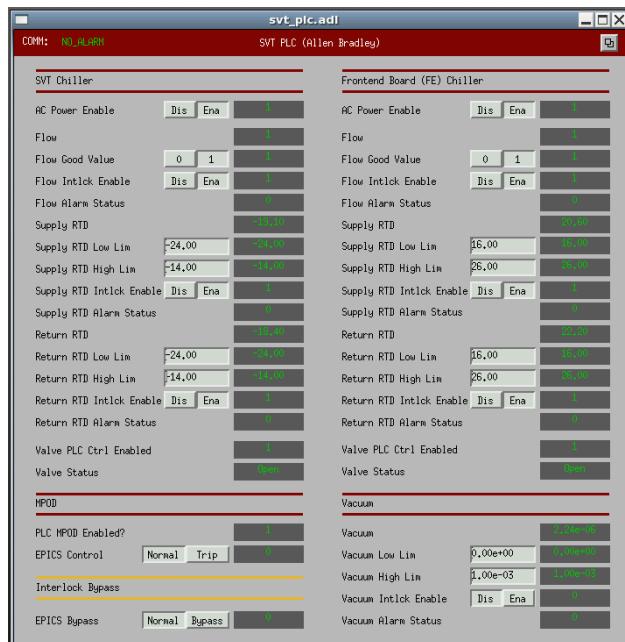


Figure 2.11: SVT PLC GUI, accessed through the **Devices** menu in hps-epics.

2.4.1 Occupancies

Occupancy in each sensor should rise smoothly from 0 to a peak occupancy between 0.0005 to 0.01. If it doesn't:

1. Check that SVT bias is on (measured voltage at 180 V for every sensor). If not, bring



Figure 2.12: SVT software interlocks GUI, accessed through the **Devices** menu in **hps_epics**.

bias up (reset beam interlock if necessary, then push the "180V" button on the bias GUI) and reset the plots.

2. If occupancies still look bad, start a new run. Reset the plots.
3. If occupancies still look bad, post plot to logbook with the run number, and page expert.

2.4.2 Samples

The SVT readout should be in time with the trigger, so that hits from a triggered event show up at a consistent time. The SVT reads out six samples for each trigger; in-time hits should consistently have sample 3 (zero-indexed) be the high sample. This is checked in the max sample plot.

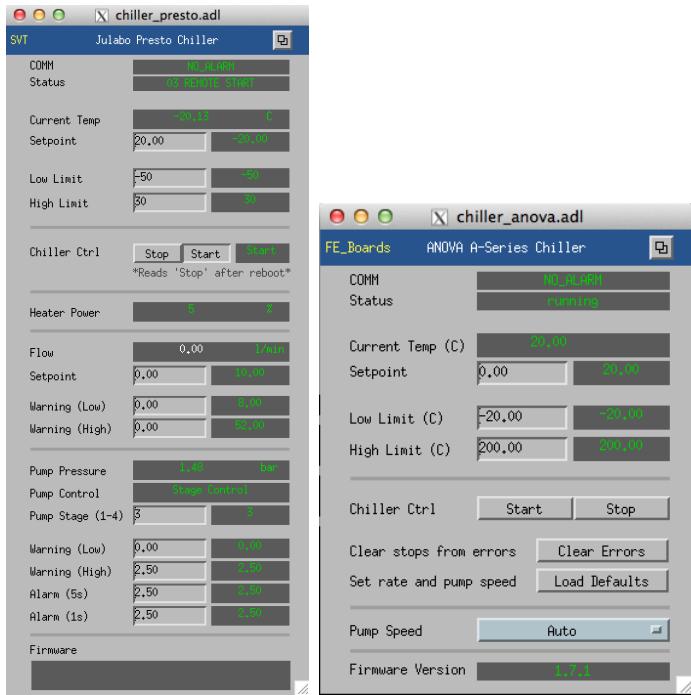


Figure 2.13: SVT (left) and FEB (right) chiller GUIs, accessed through the **Devices** menu in **hps_epics**.

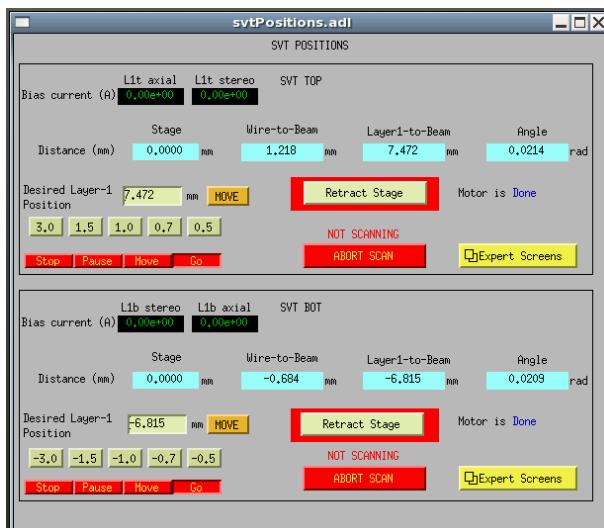


Figure 2.14: SVT positioner GUI, accessed through the **Motors** menu in **hps_epics**.

Parameter	SVT warm	SVT cold
SVT chiller setpoint (°C)	17	-20
SVT RTD alarms (low/high/high/high) (°C)	15/16/18/19	-22/-21/-17/-16
SVT RTD PLC limits (°C)	bypassed	-24/-14
FEB chiller setpoint (°C)	20	20
FEB RTD alarms (low/high/high/high) (°C)	16/17/25/26	16/17/25/26
FEB RTD PLC limits (°C)	bypassed	16/26

Table 2.1: Default SVT cooling settings. Minor alarm limits are in yellow and major alarm limits are in red.

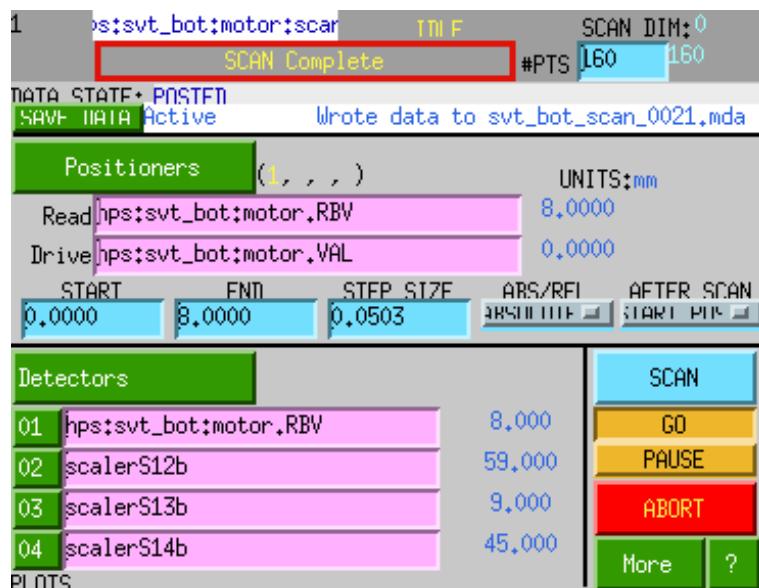


Figure 2.15: SVT wire scan GUI

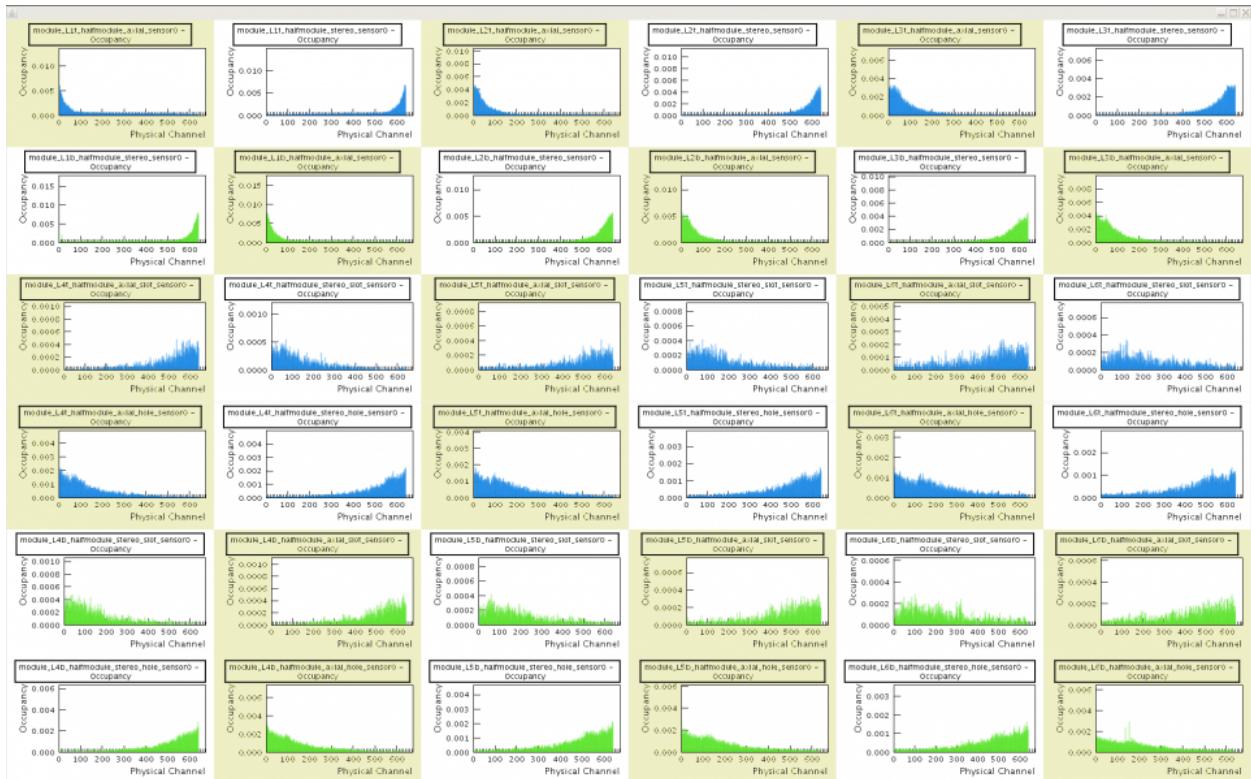


Figure 2.16: SVT occupancies plot.

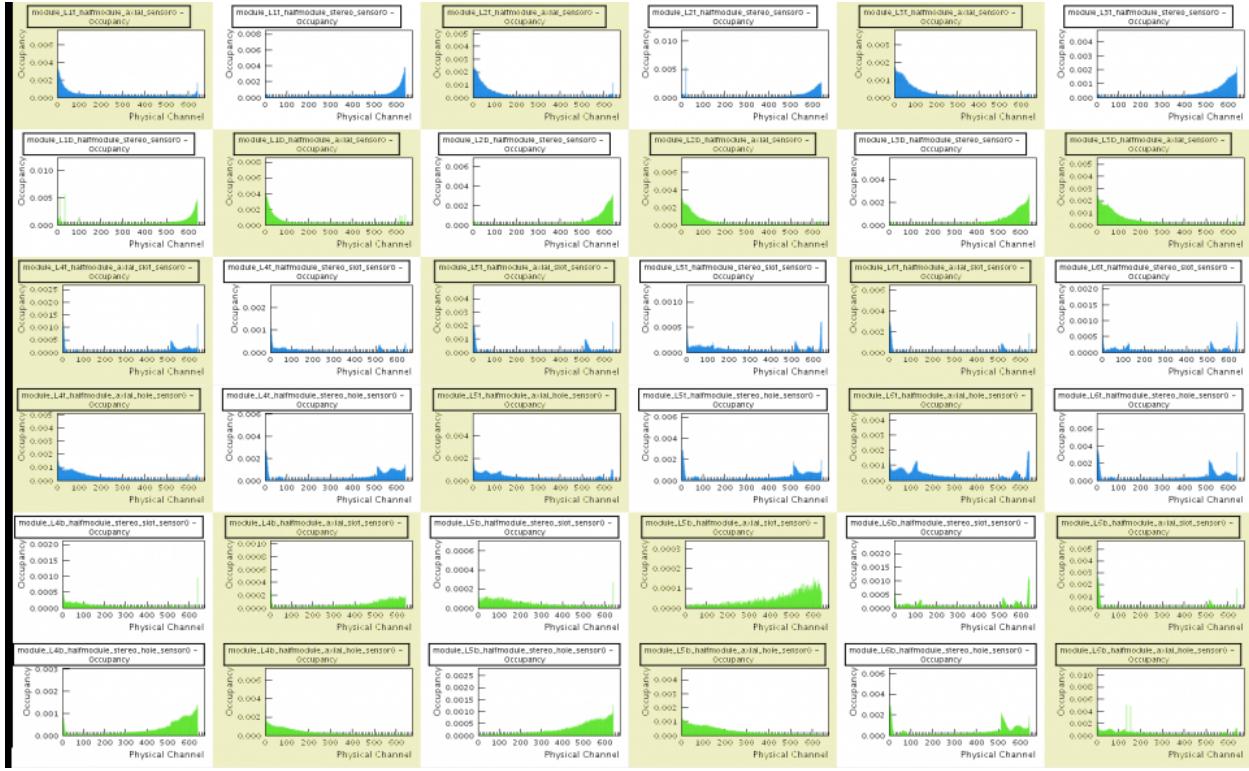


Figure 2.17: SVT occupancies plot. Bumpy patterns on many sensors indicate the bias is off or set to 5 V by the beam interlock. Reset the beam interlock, and reset the monitoring app after bias is restored.

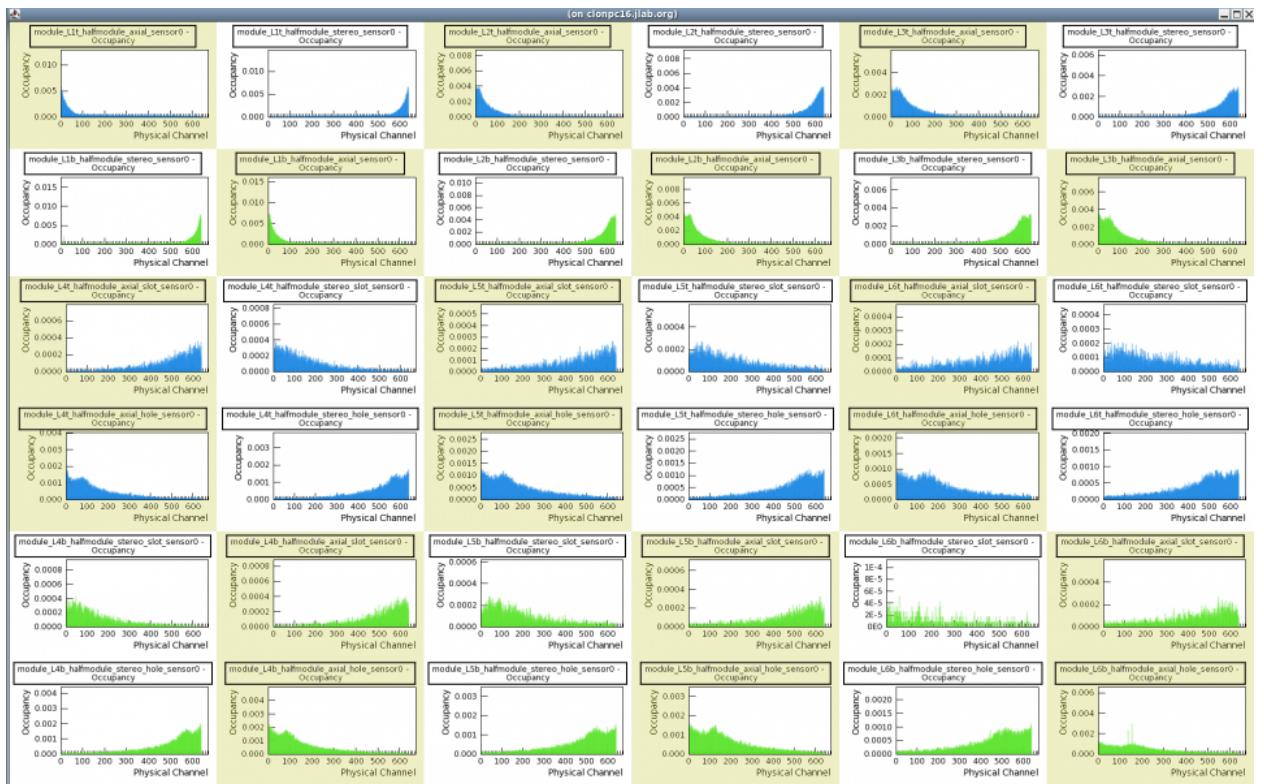


Figure 2.18: SVT occupancies plot. One sensor (second from bottom, second from right) has low occupancy. Log the plot and start a new run.

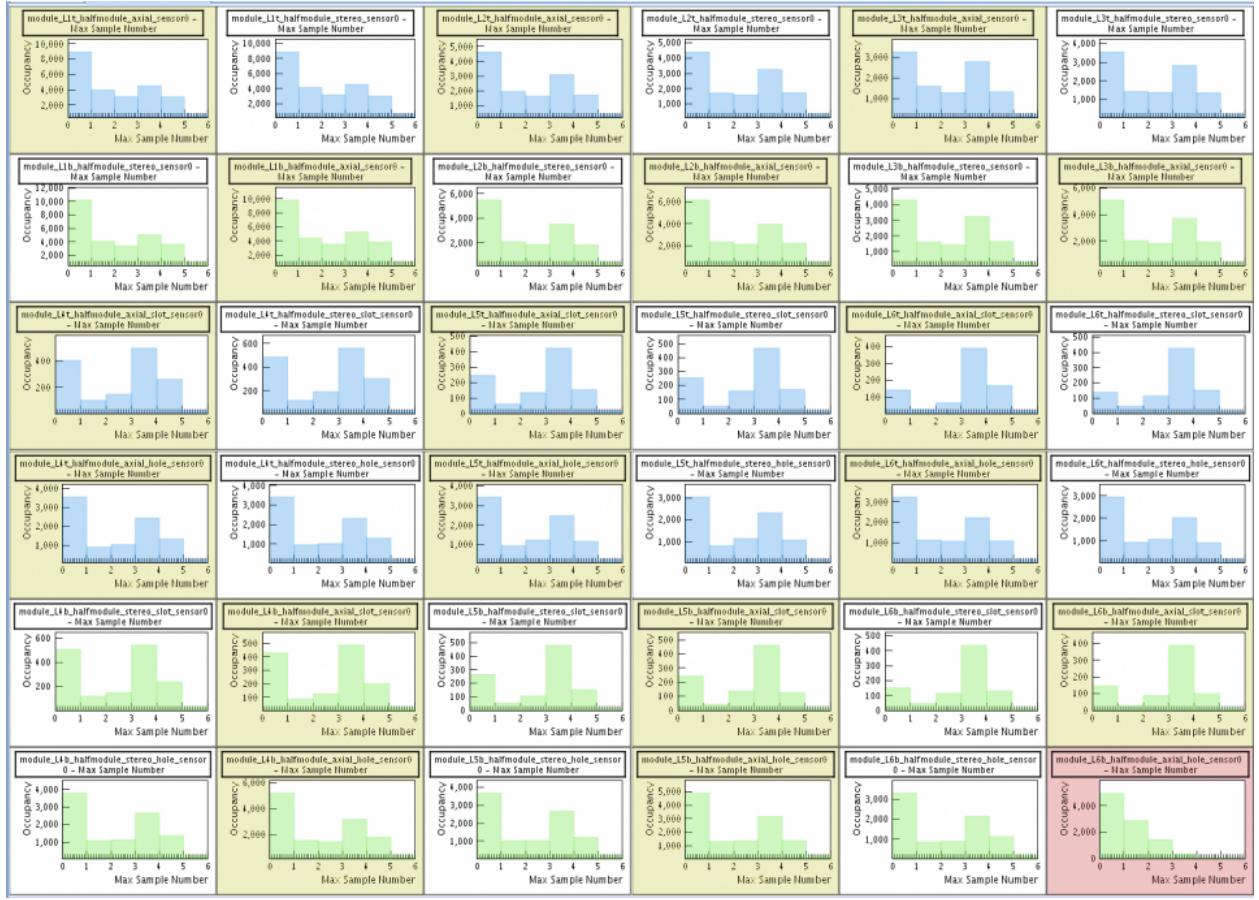


Figure 2.19: SVT occupancies plot. One sensor (bottom right) is out of time. Log the plot and start a new run.

Chapter 3

Procedures

3.1 General Procedures

3.1.1 Powering the SVT

Turning the SVT power and bias ON

The SVT sensor bias (HV) and the low voltage (LV) needed to control and read out the detector are essentially independent. However, the various components powered by the LV supplies; the Flanges, the FEBs and the Hybrids; depend upon one another, so they be powered on in proper order to operate the detector. In particular, the Flanges must be powered to communicate with the FEBs and the FEBs must be powered in order to either power or communicate with the Hybrids. To power ON all of the SVT follow this procedure using the SVT summary GUI, shown in Figure 3.1.

1. Make sure that the SVT and FEB chillers are **ON** and **OK**, and the PLC interlock is **enabled** and **OK**. See Sec. 3.3 for operation of the cooling system. Check that the SVT and FEB supply and return temperatures under **Temperature Status** are as expected.
2. Turn on SVT Flange Power by pressing the **ALL SVT FLANGE ON** button and wait for the status indicator to turn green.
3. Turn on the SVT FEB Power by pressing the **FEB ALL ON** button and wait for the status indicators to turn green and the FEB heartbeat to begin.
4. Turn on the SVT Hybrids Power by pressing the **ALL HYBRIDS ON** button and wait for the status indicators to turn green.
5. Turn on the SVT Sensor HV Power by pressing the **ALL SVT BIAS ON** button.
6. Beam interlock (under **ALL HV CONTROL**): Reset the interlock by pressing the **RESET** button, then the **180V** button. Wait for the status indicator to turn green.

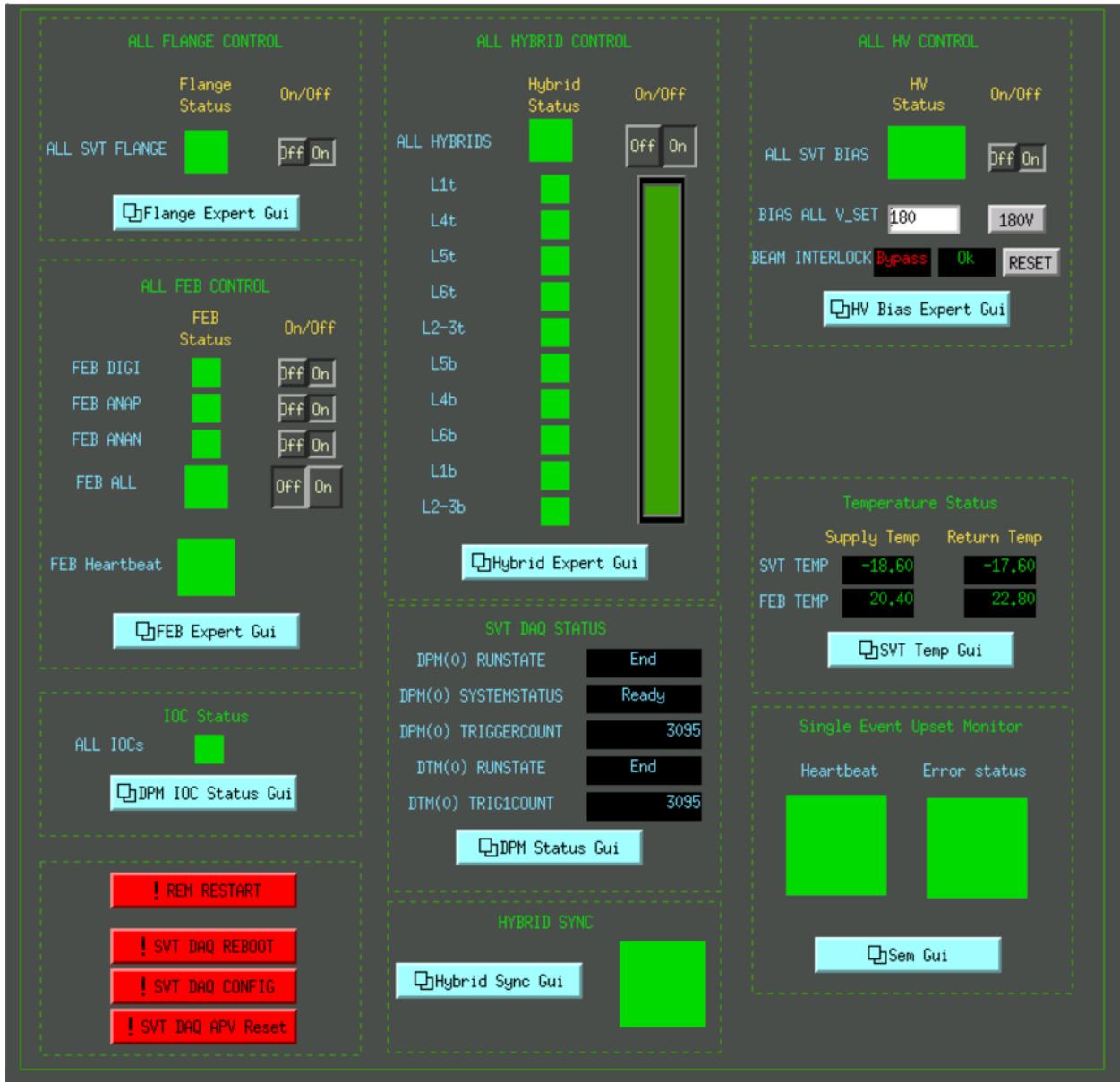


Figure 3.1: SVT summary GUI.

If any status indicator fails to turn green during this procedure or any action results in an alarm, check that the **IOC Status** is green and call the SVT Expert. More detailed information on the status of the system can be found in the various expert GUIs that can be opened from the summary GUI.

Turning the SVT power and bias OFF

To power OFF all of the SVT follow this procedure:

1. Turn off the SVT Sensor HV Power by pressing the **ALL SVT BIAS OFF** button and wait for the status indicator to turn red.
2. Turn off SVT Flange Power by pressing the **ALL SVT FLANGE OFF** button and wait for the status indicator to turn red.
3. Turn off the SVT FEB Power by pressing the **FEB ALL OFF** button and wait for the status indicators for both the FEB and Hybrid power to turn red.

If any status indicator fails to turn green during this procedure or any action results in an alarm, check that the **IOC Status** is green and call the SVT Expert. More detailed information on the status of the system can be found in the various expert GUIs that can be opened from the summary GUI.

3.1.2 Bringing Beam to the Tagger Dump

Very often the SVT will be operating, running tests or calibration, during periods when there is no beam in Hall B. However, because of the potential for damaging sensitive SVT electronics with beam splash while bringing beam to the tagger dump after downtimes, power to the SVT must be turned off before beam is brought into the hall:

- flange power off
- FEB/hybrid power off
- HV off

In addition, the SVT must be fully retracted from the beam and the SVT collimator should be set up as directed in the shift instructions. **Don't assume that just because the SVT expert is present that the SVT is in the correct state. You must ensure that the SVT is in the correct state before bringing beam into the hall.**

3.1.3 Bringing Beam through the Chicane

SVT power should remain off until after beam has been successfully brought through the chicane and wire scans at 2H02 (and with the SVT wires, if so instructed) have been performed that establish that the beam is in the expected position. Before turning on the SVT, the status of the following systems must be checked:

- Check that the SVT is fully retracted from the beam
- Check that the SVT collimator is set up as described in the shift instructions
- Check that all PLC and software interlocks are enabled (in particular, make sure the beam interlock is enabled).
- Check that all temperatures are within limits and that all interlock statuses are clear

Once these conditions have been met, power to the SVT may be restored to prepare for running, following the procedure detailed in the DAQ manual.

3.1.4 Setting up for running with beam

If this is the first run after setting up beam for HPS, the SVT will be open and the SVT Expert will need to be called in order to move the SVT into position. The experts-only procedure for establishing 0.5 mm running with the SVT may be found in Section 3.1.5. To begin a run:

1. Turn on power to the SVT following the **Turning the SVT power and bias ON** procedure, in Section 3.1.1.
2. Start a run with the DAQ (see the "SVT Checklist" in the **Start and stop a run** section of the DAQ Operations Manual).

3.1.5 Moving the SVT to 0.5 mm for Physics Running - EXPERT ONLY

If this is the first run after setting up beam for HPS, follow this procedure for moving the SVT to 0.5 mm.

1. Establish running with SVT at +/-1.5mm, as usual. Verify good data with monitoring plots.
2. Check the following:
 - Acceptable beam at Faraday cup (see run plan for the definition of acceptable beam).
 - Beam is centered with respect to the SVT, either using SVT wire scans (see procedure in Section 3.4.2) or by taking a 2H02 wire scan and comparing with 2H02 wire scan from the last time the beam was centered with respect to the SVT.
 - Halo counter FSD is unmasked, and trip level is set appropriately for the current halo counter rate.

- HPS orbit locks are on, and orbit lock target positions are as specified in the run plan.
 - Beam interlock is enabled (green **Normal**, not red **Bypass**). If not enabled, enable in the SVT software interlock GUI.
 - MCC has been notified to not restore beam following a halo counter FSD trip.
3. Start new run. Move in Bottom SVT 0.1 mm at a time. Watch strip charts and record with each move:
 - FSD counter rate
 - SVT L1b HV currents
 - Peak occupancy in L1b
 4. Stop if FSD trips, peak occupancy exceeds 4%, or any SVT HV currents exceed 1 uA.
 5. If Bottom SVT makes it to 0.5 mm, retract Bottom SVT to 1.5mm and repeat previous steps for Top SVT.
 6. If both top and bottom come all the way to 0.5mm acceptably, then bring bottom back to 0.5mm and begin running with a new run.

If either half of the SVT does not make it to 0.5 mm, consultation will be required in order to decide whether to move the beam slightly and where exactly we should run the SVT. If necessary, running at 0.6 mm or 0.7 mm may be acceptable.

3.1.6 Running the SVT

When running the SVT at 0.5 mm:

- Check the scaler strip charts often
- Check the L1 HV currents often. Call expert if they exceed the levels indicated in the shift instructions.
- Check the occupancy plots at the beginning of each run and at least once per hour. Call the SVT expert if they exceed 4% anywhere in L1.
- For beam trips, follow the special procedure in Section 3.1.7 that applies to running with the SVT at 0.5 mm.

3.1.7 Response to beam trip

If the beam is lost, the SVT beam interlock (if enabled) will lower the HV. In general, for beam trips:

1. Check the SVT shift instructions to see if any special actions are required.
2. **If SVT is at 0.5 mm, check the FSD GUI to see if our halo counters were responsible. If so, follow the FSD trip procedure (below) instead of continuing with this one.**
3. On the SVT Summary GUI, the **ALL SVT BIAS** status indicator should remain green as the bias voltage stays at 180V.
4. When beam is restored, check that the BPM x and y values at 2H02 are as desired and that beam current is stable at our desired value.

Response to beam trip (FSD trip)

When taking physics runs with the SVT in the nominal position, 0.5 mm from the beam plane, the silicon in Layer 1 is not fully protected by the collimator. In this case, the following procedure must be followed for beam trips where the halo counter FSD is responsible:

1. Tell the MCC not to restore the beam. We will inform them when we want it.
2. On the SVT Summary GUI, the **ALL SVT BIAS** status indicator should turn yellow as the bias voltage is ramping down. It will turn red once the bias voltage is at 5V. If this does not happen after about 10 seconds, manually press the **BIAS ALL V_SET 5V** button.
3. Once the status indicator turns red, tell the MCC we are ready for beam.
4. When restored, check that the BPM x and y values at 2H02 are as defined in the orbit locks and that halo counter rates are the same as before the trip.
5. Reset the beam interlock by pressing the **BEAM INTERLOCK RESET** button.
6. Press the **BIAS ALL V_SET 180V** button to turn on the SVT Sensor HV and wait for the **ALL SVT BIAS** status indicator to turn green.
7. Wait until beam current is stable at our desired value.
8. Reset the SVT monitoring plots (disconnect and connect). Check the SVT monitoring plots.

3.2 Response to Alarms

The alarm handling is handled by the ALH extension to EPICS that is widely used at JLab. The SVT alarm handling GUI is shown in Fig. 3.2. It is tree-based with each level showing more detailed info of what alarm has gone off.

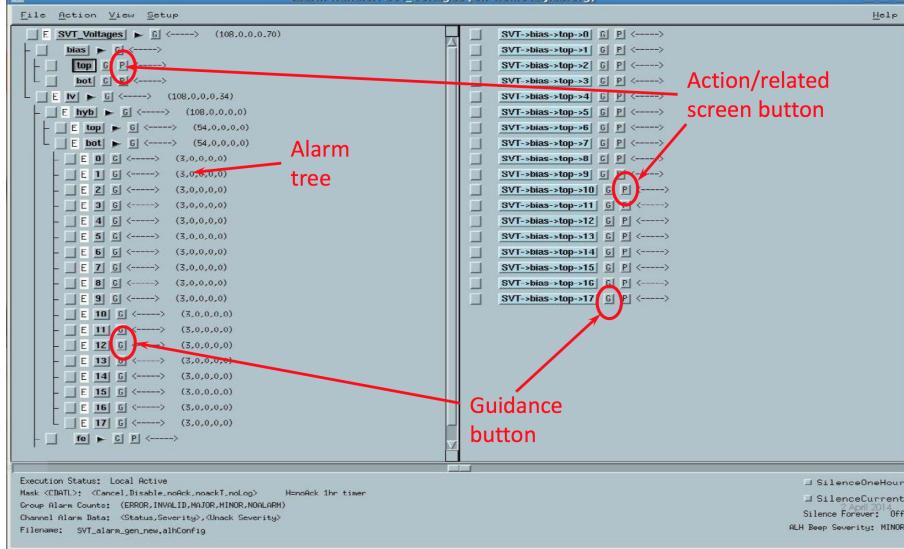


Figure 3.2: SVT alarm handler GUI.

Each level has two buttons: a **guidance** button which opens a pop up display with more information and an **action** button which acknowledges the alarm.

In case any error please contact SVT expert.

Be ready to report what alarm has gone off.

More information about the alarm handler can be found in the slow control manual.

3.3 Cooling and Interlocks

3.3.1 Start FEB chiller

1. Disable the PLC interlock for FEB flow (PLC GUI, upper right).
2. Verify that the chiller temperature setpoint is 20 C. If not, call the SVT expert.
3. Check the RTD temperature limits. The low limits should be set at 16 C and the high limits at 26 C.

4. Verify that all PLC alarms that affect the FEB chiller loop (supply RTD, return RTD, and vacuum) are in “0” state.
5. Verify that the FEB valve is open.
6. Start the chiller.
7. Wait for the flow switch value to change, then enable the FEB flow interlock. If the flow switch does not trigger, call the SVT expert.
8. At this point, the FEBs can be powered.
9. The **current temperature** shown in the FEB chiller GUI should converge to the setpoint in a few minutes.
10. When the temperature has reached the setpoint, lower the high limits in the interlock to their final values.

3.3.2 Start SVT chiller

1. Change the chiller temperature setpoint to 17 C. (This assumes the SVT cooling loop is at room temperature to start. If it is cold, start cold.)
2. Disable the PLC interlocks for SVT flow, supply RTD, and return RTD (left side of PLC GUI). Disable the software interlocks for supply RTD and return RTD (left side of software interlock GUI). Verify all PLC alarms and software interlocks are in “Ok” or “0” state, and the SVT valve is open (PLC GUI).
3. Start the chiller.
4. Wait for the flow switch value to change, then enable the SVT flow interlock.
5. Wait for the chiller temperature to stabilize at the setpoint. Some oscillation is normal in the short term, and the chiller may trip off (most likely if the chiller has been off for a while). If this happens, power cycle the chiller (power switch to the left of the touch screen, or “AC Power Enable” on the PLC GUI), and start over from the beginning of the procedure.
6. Follow the procedure in section 3.3.7 to change the temperature to its desired value.

3.3.3 Stop FEB chiller

1. Verify that hybrid bias, FEB power, and flange board power are off.
2. Press the **Stop** button in the FEB chiller GUI. The interlocks will close the FEB valve and trip the MPOD.

3.3.4 Stop SVT chiller

1. Verify that hybrid bias, FEB power, and flange board power are off.
2. Press the **Stop** button in the SVT chiller GUI. The interlocks will close the SVT valve and trip the MPOD.

3.3.5 Draining SVT chiller (experts only)

1. Chiller should be at 17 C. If not, raise the temperature using the procedure in section 3.3.7.
2. Disable the PLC interlocks for SVT chiller flow, supply RTD, and return RTD (left side of PLC GUI).
3. Put the chiller in manual mode using the touchscreen (Menu→Connect Unit→Remote control).
4. Follow the instructions in the chiller manual to drain the chiller. Use the big plastic jug and the two short pieces of rubber tube.
5. When you disconnect a fitting in the chiller loop, drain both connections into a beaker.
6. Purge both connections using the nitrogen line (use a Swagelok-to-VCR adapter with a used gasket), with the reservoir drain valve (the ball valve with a plastic handle) open. Be sure to cap the connection not being purged using a VCR cap and a used gasket.

3.3.6 Filling SVT chiller from empty (experts only)

1. Disable the PLC interlocks for SVT chiller flow, supply RTD, and return RTD (left side of PLC GUI).
2. Follow the instructions in the chiller manual to fill the chiller. Use the metal funnel.
3. If the final level of the chiller is below 1/4, add a full jug of HFE 7000.

3.3.7 Changing SVT chiller temperature

1. Disable the PLC and software interlocks for SVT chiller supply RTD and return RTD (these are left disabled between run periods).
2. Change the setpoint on the SVT chiller GUI in steps of no more than 10 degrees C. After each step, wait 20 minutes for temperatures to equalize in the system (chiller temperature and RTDs should stabilize in about 10 minutes). Check cooling lines for frost or melting ice.

- When at final temperature, set the alarm and interlock setpoints for the SVT chiller supply RTD and return RTD. Re-enable interlocks if they were bypassed.

3.3.8 Adding fluid to SVT chiller

The HFE-7000 fluid for the SVT cooling loop evaporates steadily and must be replenished approximately once every 3 weeks. The fluid is supplied in 10-pound jugs. It is nontoxic and evaporates rapidly, so don't worry about small spills. The chiller should be refilled when it reaches level 2 (slightly below 1/4); one jug will take it to level 7 (slightly above 3/4). Refilling before the level drops to level 2 will cause a high level warning; waiting until the level drops to level 1 will cause a low level warning.

It is safe to add fluid while the chiller is running. This may cause a temperature spike, so RTD interlocks must be disabled.

- Disable the PLC and software interlocks for SVT chiller supply RTD and return RTD (these are left disabled between run periods).
- Using a funnel, pour a jug of HFE-7000 into the fill port at the top of the chiller (open the metal cover and pull out the white plastic plug). If this causes a high level warning, acknowledge it.
- Wait 5 minutes or until temperatures have stabilized (can use the stripchart on the chiller's touchscreen).
- Check the water level in the FEB chiller (minimum: the cooling coil should be covered, maximum: 1.5 inches below the rim).
- Check that none of the temperatures are alarming (RTD values in the interlock GUIs should be green). Enable all interlocks that were bypassed.
- Make a logbook entry noting the chiller level before and after the fill, and the number of full HFE jugs remaining.

3.3.9 Response to unexpected chiller trip

- Look at the EPICS screens for the PLC. Check the alarm status fields for any PLC alarms. Also look at the software interlock screen and check the interlock status fields for any interlock faults. Call the SVT expert with the list of alarms.
- If the expert tells you to restart the chiller: Disable all PLC alarms that have tripped. Bypass and reset all software interlocks that have tripped. Start the chiller from its EPICS screen. As alarms clear, re-enable all alarms and interlocks that you disabled. If any alarm does not clear, call the SVT expert.



Figure 3.3: Steps of the SVT chiller level indicator (green/yellow bar on right - it may be necessary to push the green “Display” button at the top to get to this view). Level 1 (first image) is the lowest level at which the chiller will continue to run, with a low level warning alarm. Levels 2–7 are levels for normal operation. Level 8 (not shown) will cause a high level warning alarm, but the chiller will continue to run.

3.4 Motion

3.4.1 SVT Mover Operations

Figure 2.14 shows the SVT Mover GUI. The web interface to the motor controller (<http://hpsxps/>) can be used to initialize and home the stages, or reboot the controller.

- Type in the destination of the layer 1 edge in mm in **Desired Layer-1 Position**, and hit Enter. Alternatively, press one of the buttons below (**3.0, 1.5, 1.0, 0.7, 0.5**).
- Press the **MOVE** button to command the move.
- Press **Retract Stage** at any time to fully retract the SVT (this will also abort a move in progress).

The positions of the stage, wire and Layer 1 sensor edge, and the angle away from horizontal, are shown in their respective coordinates.

3.4.2 SVT Wire Scanner Operations

To center the beam, perform SVT wire scans (if the top and bottom are known to be consistent, only use the same set of wires that was used in the last good scan in the logbook; otherwise use both top and bottom, and take the mean) and correct if you see $|y_{beam}| > 50 \mu\text{m}$ or $|x_{beam}| > 300 \mu\text{m}$.

Once the beam offset values ($\Delta X, \Delta Y$) from the nominal beam position are measured, request MCC to move the beam using corrector magnets (MBC2H04V/H and MBC2H08V/H). Wire scan should be repeated to confirm the beam position.

A wire scan can be performed from the SVT wire scan GUI (Figure 2.15). To open the GUI, first open the SVT positioners GUI (Figure 2.14). Click the **Expert Screens** button for the top or bottom half of the SVT, then select the scan GUI from the menu.

- Check:
 - MCC is not moving the beam or changing beam conditions.
 - Ask MCC to mask BOM and Downstream Halo Counter in FSD.
 - Downstream halo counters are operational.
- Click **SCAN** to start a scan using the default settings.
- Watch the SVT positioners GUI to see the stage position move. When it reaches the end of the scan and starts moving back to 0, you can click the **Analyze the data** button to analyze the data. This will pop up some plots and automatically submit a logentry.

The wire scan analyzer output is shown in Figure 3.4. The analyzer looks at two halo counters (HPS_T and HPS_SC), finds the peaks from the two wires, and does an initial fit to constant background plus Gaussian (solid fit curves), and a second fit to the center of the peak (dotted fit curves). Normally all four sets of fit results (numbers on the right) should be similar. The beam X and Y positions are given in the SVT coordinate system, where (0,0) is the nominal beam position.

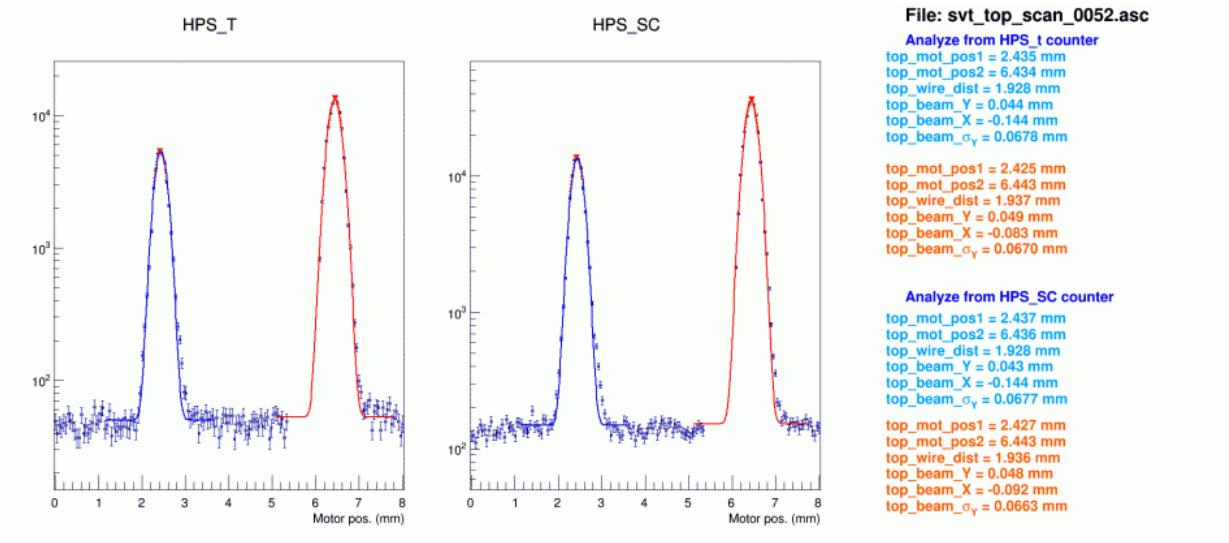


Figure 3.4: SVT wire scan output