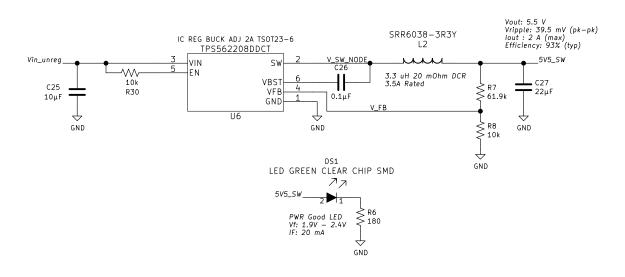
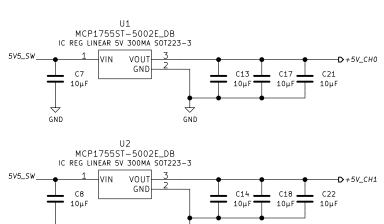
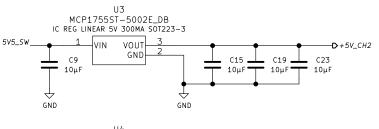


9V-12V Input -> 5.5V SMPS Regulated

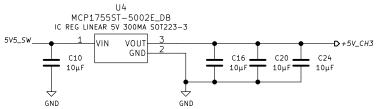




5.5V -> 5V, Per Channel LDO



GND



Per LDO:

| Dout_max = 250 mA (per RF Channel) | P_LDO_max => (Vin - Vout)*lout_max = 125 mW | R_JA = 62.0 degC/W (typ. 507-223-3) | T_rise_max => P_LDO_max*R_JA = 7.75 degC

PSRR = 80 dB Line Regulation = +/- 0.01% (typ)

Designer: John Gentile

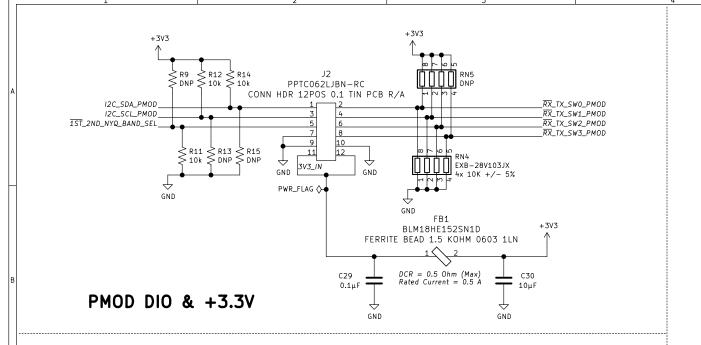
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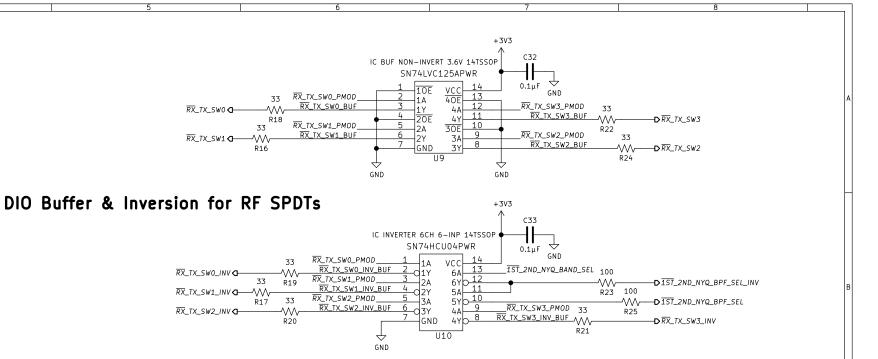
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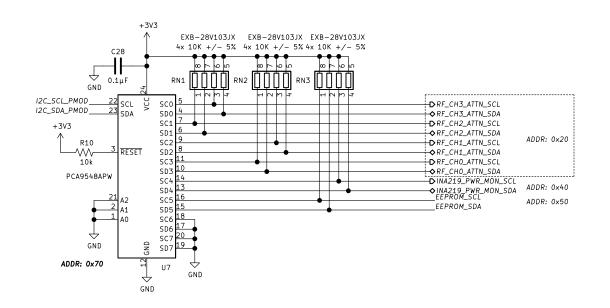
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 Size: B
 Date: 2020-03-08
 Rev: A

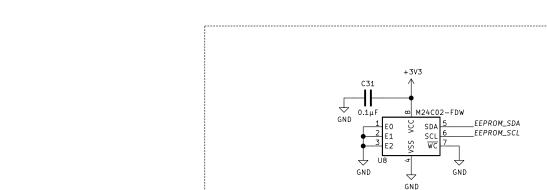
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 Id: 2/7







12C Bus Switch



DS2 LED GREEN CLEAR CHIP SMD フォ

PWR Good LED

2 kb EEPROM

ADDR: 0x50

Designer: John Gentile

JHU Embedded Systems Lab

Sheet: /Digitial I/O via PMOD/ File: PMOD_DIO.sch

Title: 4—Channel RF Front End

 Size: B
 Date: 2020-03-08
 Rev: A

 KiCad E.D.A. kicad 5.1.5-1.fc31
 Id: 3/7

