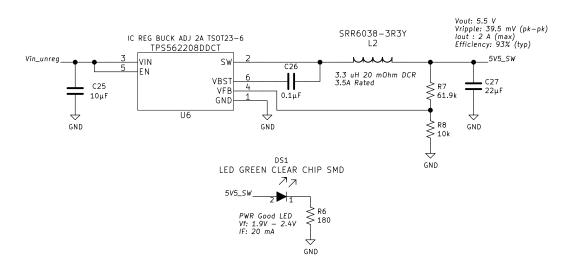
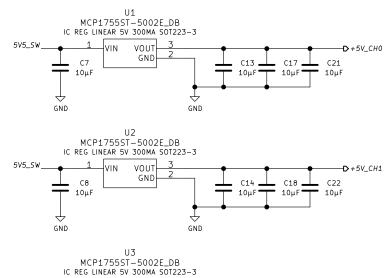


5.5V -> 5V, Per Channel LDO

## 9V-12V Input -> 5.5V SMPS Regulated





Per LDO:

V4

MCP1755ST-5002E\_DB
IC REG LINEAR 5V 300MA SOT223-3

5V5\_SW 1 VIN VOUT 3

C10
10μF C10
10μF 10μF 10μF

— C9 10μF

Designer: John Gentile

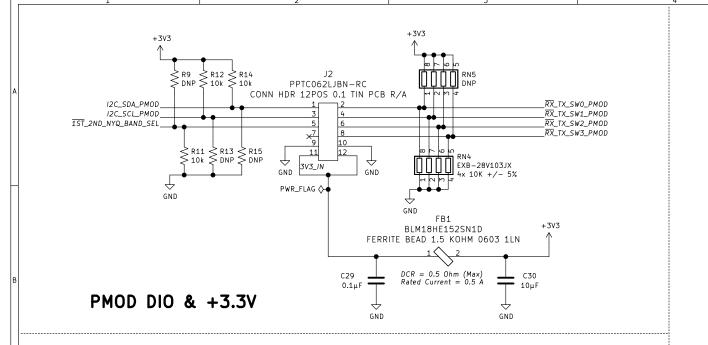
JHU Embedded Systems Lab

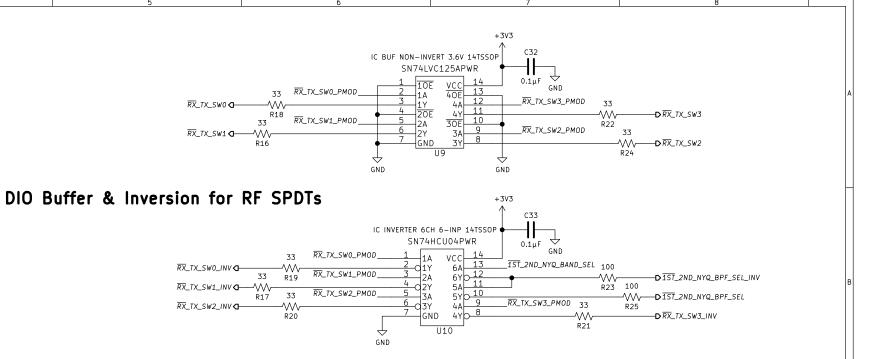
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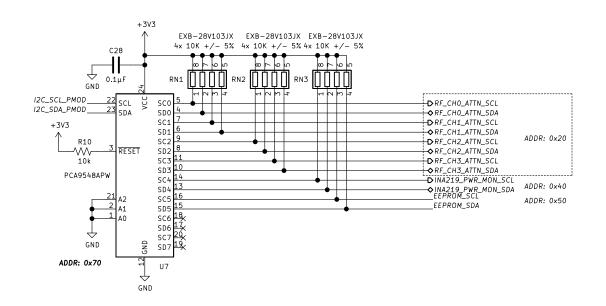
Title: 4-Channel RF Front End

 Size: B
 Date: 2020-03-08
 Rev: A

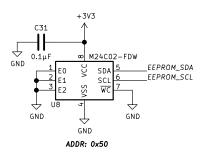
 KiCad E.D.A. kicad (5.1.5-0-10\_14)
 Id: 2/7







12C Bus Switch



## 2 kb EEPROM

	E
Designer: John Gentile	
JHU Embedded Systems Lab	
Sheet: /Digitial I/O via PMOD/	
File: PMOD_DIO.sch	
Title: 4—Channel RF Front End	
Size: B Date: 2020-03-08	Rev: A
KiCad E.D.A. kicad (5.1.5-0-10 14)	ld: 3/7

