



This document is the property of Motorola, Inc., Connected Home Solutions. This document may only be distributed to: (i) a Motorola employee ("Motorola") having a legitimate business need for the information contained herein, or (ii) a non-Motorola having a legitimate business need for the information contained herein. No license, expressed or implied, under any patent, copyright or trade secret right is granted or implied by the conveyance of this document. No part of this document may be reproduced, transmitted, transcribed, stored in a retrieval system, translated into any language or computer language, in any form or by any means, electronic, mechanical, magnetic, optical, chemical, manual, or otherwise without the prior written permission of Motorola, Inc. Connected Home Solutions. (See Document Security Standard, 320190-000 for details.)

MOTOROLA, the Stylized M Logo and all other trademarks indicated as such herein are trademarks of Motorola, Inc. ® Reg. U.S. Pat. & Tm. Off. All other product or service names are the property of their respective owners.

Copyright © 2000-2016 Motorola, Inc. All rights reserved.

<b>Document Title</b>	<b>DCX BLTC Software Low Level Design (LLD)</b>
<b>Number</b>	
<b>Revision</b>	<b>Z1</b>
<b>Revision Date</b>	<b>11/06/2008</b>
<b>Author(s)</b>	<b>Gamma Chen</b>

# Internal Level 2

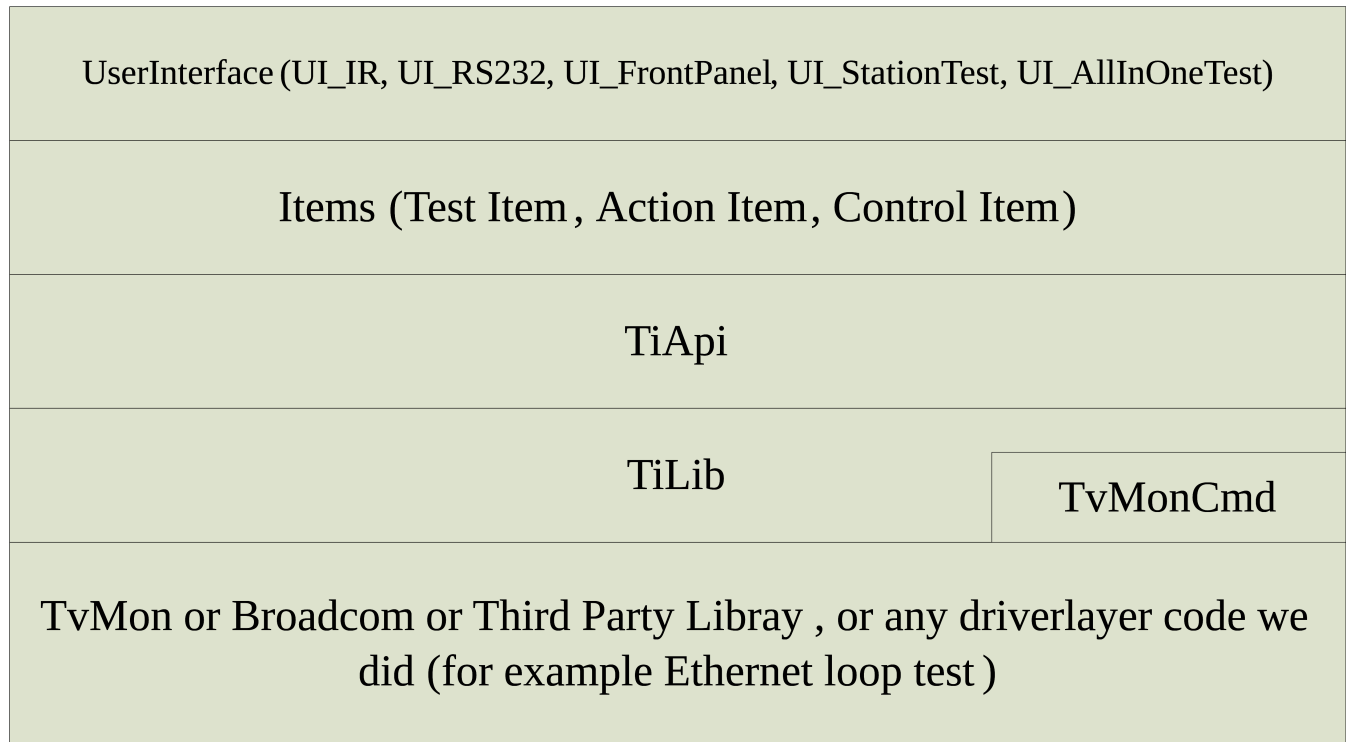
Draft

[illegible]

## TABLE OF CONTENTS

1.	Software Architecture .....	9
5.1.	BLTC Basic Type .....	59
5.2.	Class Diagram (API quick view).....	9
6.	Sequence Diagram.....	10
6.1.	Class Diagram (API quick view) .....	9
5.2.	Class Diagram (API quick view).....	9

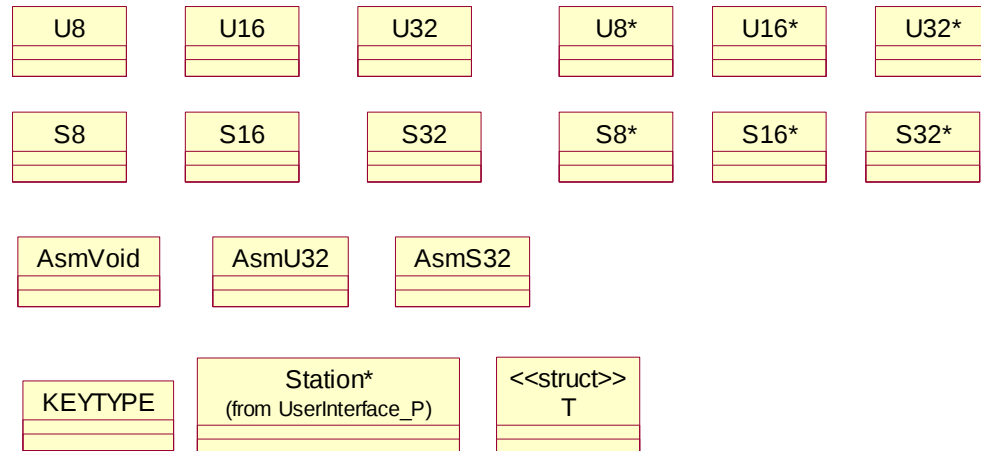
Follow are the Software Structure we mentioned in HLD. In LLD, we will explain class diagram and Sequence Diagram according the Software Layer we use in BLTC design.



# 1. CLASS DIAGRAM

## 1.1. BLTC Basic Type

Broadcom used to using their basic type (U8, U16, ...). BLTC should avoid to define the same type name.



BltcBasicType.h contents as follow,

```
#include "gitypes.h"      // "gitypes.h" include above type and const.
```

```
// Follows for Code Generation Function of QIP7xxx BLTC can work in Rational Ross (UML tool).
```

```
#define AsmVoid          asm void
```

```
#define AsmU32           asm U32
```

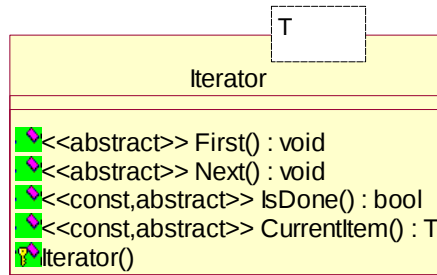
```
#define AsmS32           asm S32
```

Rational Ross be chosen as OOA (Object Oriented Analysis) tool. I made AsmVoid define since “asm void” will make the Rational Ross dead in Code Generation Function or Reverse Engineer Function.

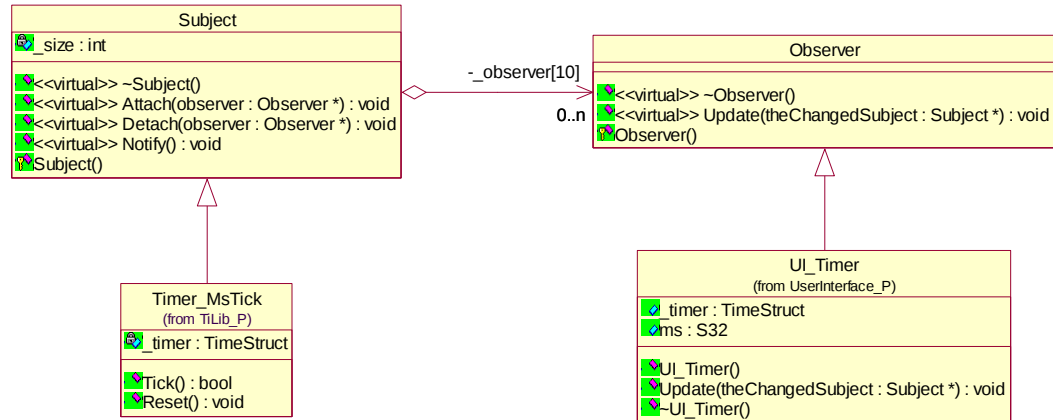
## 1.2. Pattern

A couple of pattern used in QIP7xxx BLTC design (Refer book, Design Patterns, published by Addison Wesley in 1995). They are:

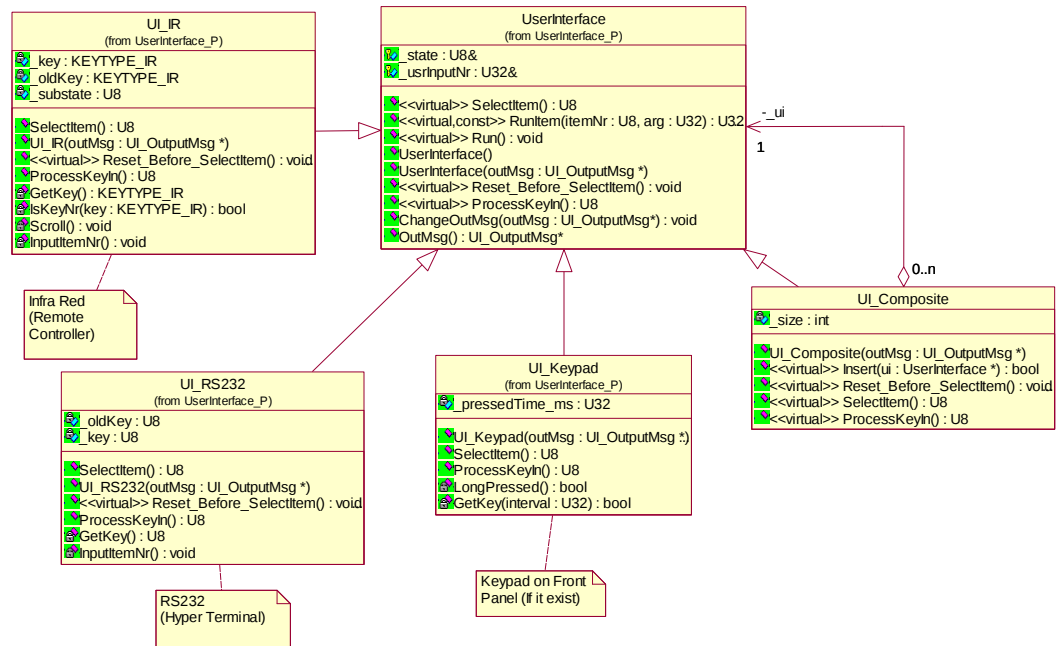
Iterator pattern

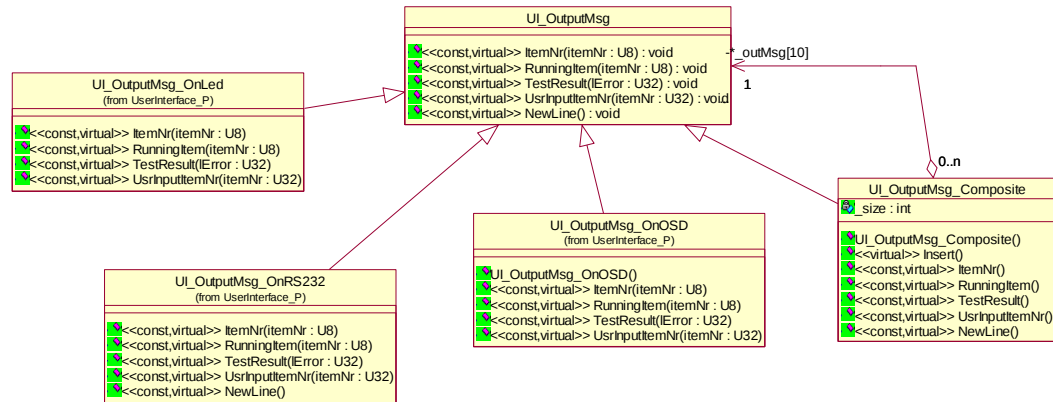


## Observer pattern

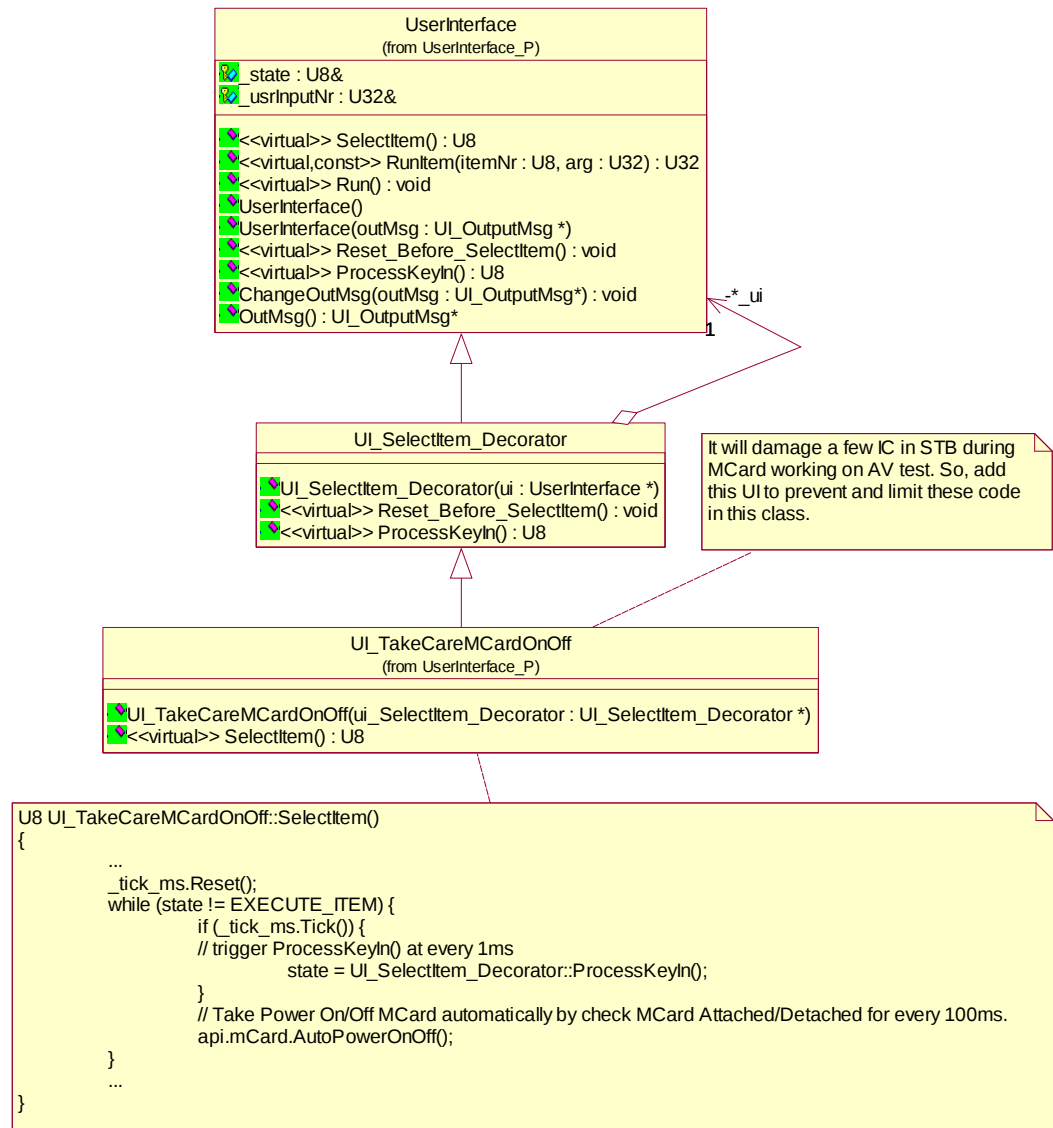


## Composite pattern





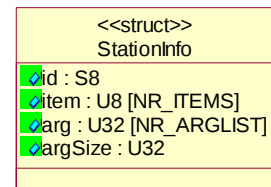
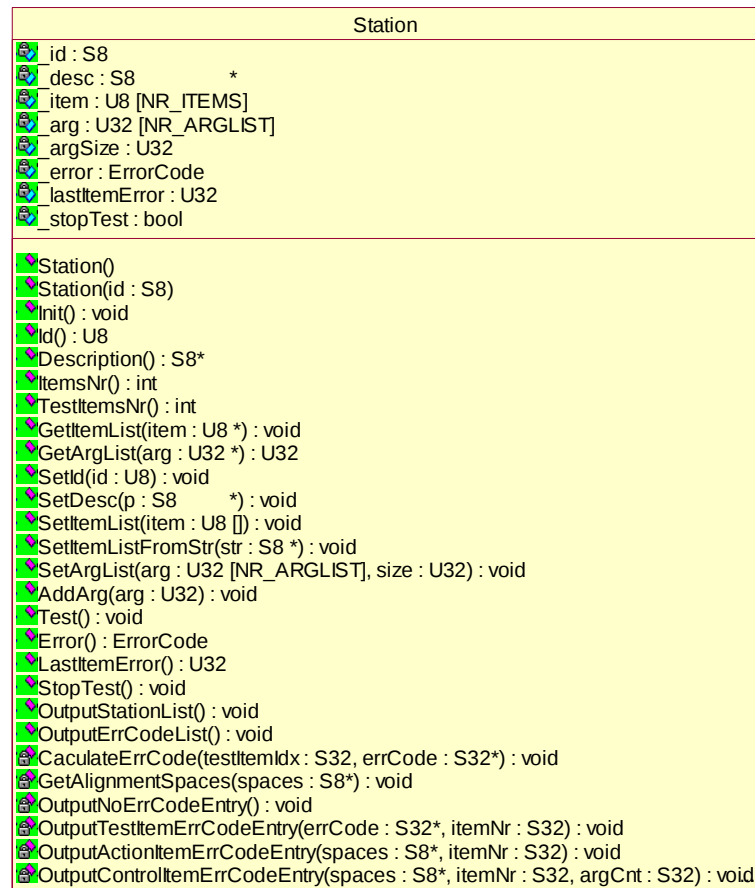
## Decorator pattern



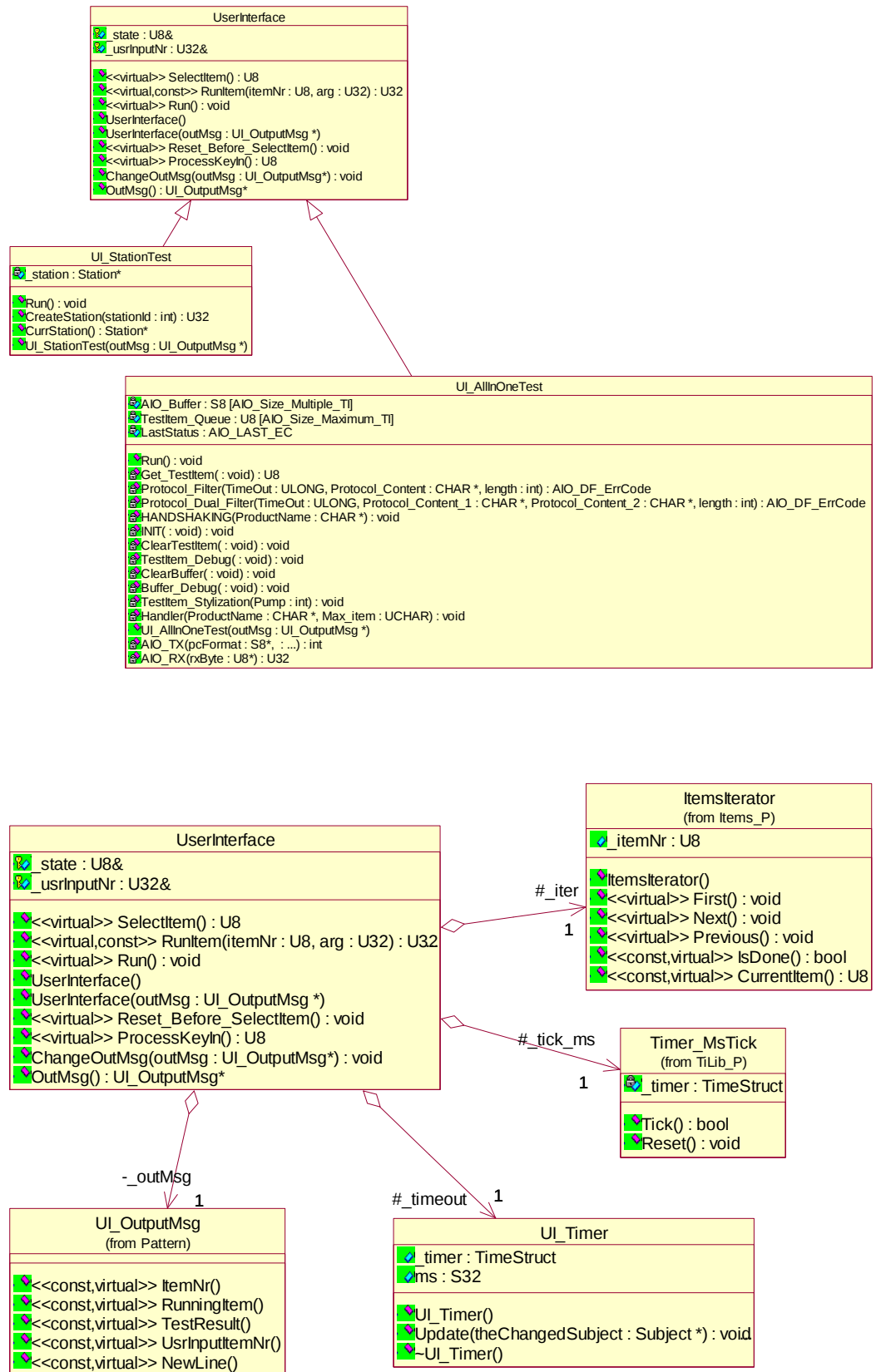
### 1.3. User Interface

For lab, select test item by Remote Controller (IR: Infra Red) or Front Panel (Keypad) or RS232, run single test item for debug (UI\_IR, UI\_RS232, UI\_FrontPanel user interface), (refer Decorator Pattern as above).

In production-line, we using Station Test, UI\_StationTest, currently, and have a plan All In One Test, UI\_AllInOne, in future.







## 1.4. Items

Give a number for each test item, it's purpose is to communicate with PE clearly.



























































For example,

TI000: "Display station information on RS232, setting:(38400,8,O,1,n)"

TI103: "Tuner 1(64QAM, channel=8, pid=8c0 8c0 8c1) A/V test, and 1394 Tx Test"

TI105: "Tuner 1(256QAM, channel=6, pid=310 310 311) A/V test, and 1394 Tx Test"

When operator report to PE, and PE told us the test item number has trouble, eg.103, then we know exactly the TI103: "Tuner1(64QAM)..." has trouble.

Items	
	_process : Process
	TI000() : U32
	TI001() : U32
	TI002() : U32
	TI003() : U32
	AI004() : void
	AI005() : void
	TI006() : U32
	TI007() : U32
	TI008() : U32
	TI009() : U32
	TI010() : U32
	AI011() : void
	AI012() : void
	TI021() : U32
	TI022() : U32
	TI023() : U32
	TI024() : U32
	TI025() : U32
	TI026() : U32
	TI027() : U32
	TI028() : U32
	TI029() : U32
	TI031() : U32
	TI032() : U32
	TI033() : U32
	TI034() : U32
	TI035() : U32
	TI036() : U32
	TI037() : U32
	TI038() : U32
	TI039() : U32
	TI040() : U32
	TI041() : U32
	TI042() : U32
	TI043() : U32
	AI051() : void
	TI052() : U32
	TI053() : U32
	TI054() : U32
	TI055() : U32
	TI056() : U32
	TI057() : U32
	TI061() : U32
	TI062() : U32
	TI063() : U32
	TI064() : U32
	TI071() : U32
	TI073() : U32
	TI074() : U32
	TI082() : U32
	TI083() : U32
	TI084() : U32
	TI085() : U32
	TI087() : U32
	TI088() : U32
	TI089() : U32
	TI090() : U32
	TI091() : U32
	TI092() : U32
	TI093() : U32
	AI201() : void
	AI202() : void
	AI203() : void
	AI205() : void
	AI206() : void
	AI207() : void
	AI208() : void
	CI253(arg : U32) : void

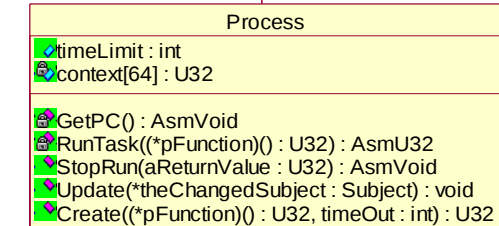
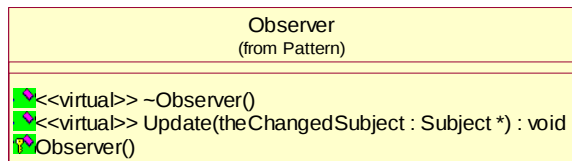
```
<<struct>>
Item
id : U8
type : S8
name : S8*
```

```
enum int type =
{TEST_ITEM,
ACTION_ITEM,
CONTROL_ITEM}

//TEST_ITEM
int Tlxx() {
return SUCCESS/FAIL;
}

// ACTION_ITEM
void Alxx() {
}

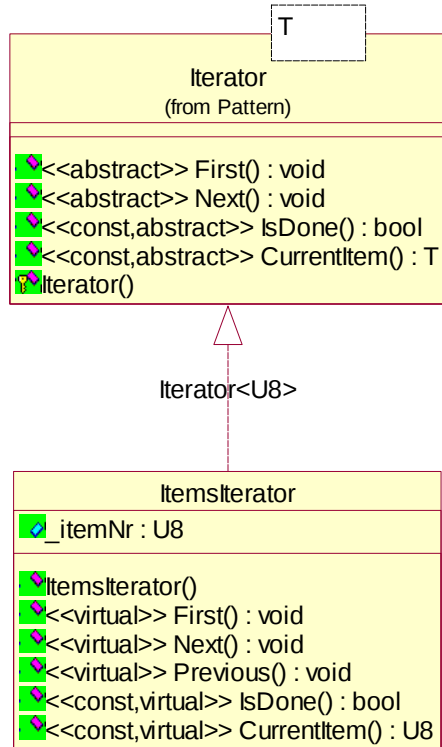
// CONTROL_ITEM
void Clxx(char* arg) {
}
```



By process.Create(pf, timeOut), the function pf, will stop and treat this function run fail. It will avoid the Hang On Forever Problem.

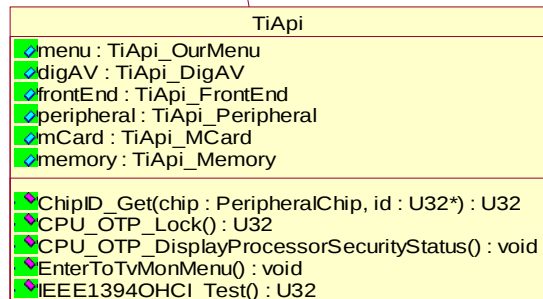
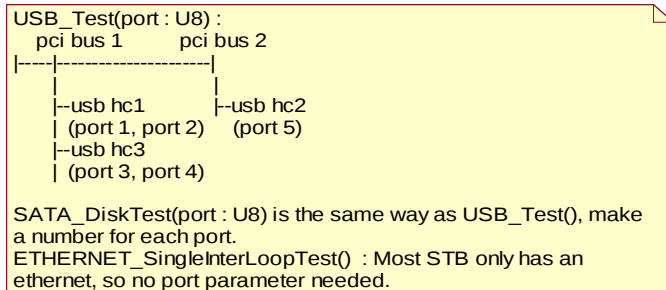
eg.  
U32 (\*pf) = &FLASH\_IDGet\_Test;  
int timeOut = 5; // unit:1s, => 5 seconds  
IError = process.Create(pf, timeOut);

Will stop run FLASH\_IDGet\_Test(), and assign IError to 1 (FAIL) if 5 seconds is run out.



## 1.5. TiApi

The layer be called by layer items or above.



```
<<struct>>
ChanMap
chan : U32
freq : U32
```

```
<<struct>>
PcrPids
pcr : U32
vPids : U32
aPids : U32
```

```
<<struct>>
TS_Msg
tuner : int
isFreq : bool
qamType : int
path : int
```

```
struct TS_Msg {
    int tuner;
    bool isFreq; // frequency or channel
    union {
        U32 freq; // frequency
        U32 chan; // channel
    }
    int qamType; // QAM64, QAM256
    PcrPids pcrPids;
    U32 path; // 0:NORMAL_PATH, 1:RouteToCableCard
};
```

#### TiApi\_DigAV

```
PIP_CreateTransportStream(tuner1 : TS_Msg, tuner2 : TS_Msg) : U32
ChangeChanMap(chanMap : ChanMap) : U32
OpenOutput(flag : int) : U32
OpenOutput_All() : U32
CloseOutput(flag : int) : U32
EnableRemod() : void
Audio_Start_Test_Tone(stream : U32) : void
CreateTransportStream(ts : TS_Msg) : U32
```

#### TiApi\_FrontEnd

```
QAM_SetFreq(tuner : int, freq : U32) : U32
QAM_SetChan(tuner : int, chan : int) : U32
QAM_SetPcrPids(tuner : int, pcrPids : PcrPids) : U32
QAM_256Acq(tuner : int) : U32
QAM_64Acq(tuner : int) : U32
QAM_LockStatus(tuner : int) : U32
OOB_Lock(Freq_Sel : int) : U32
VCXO_Test(selFreq : U8) : U32
QAM_Set_Brcm(tuner : int, freq : U32, qamType : int, tuneMode : U32) : U32
QAM_GetLockStatus_Brcm(tuner : int) : U32
```

#### RFU

(from TiApi\_FrontEnd)

```
Calibration(freq : U32, powerLevelDbmv : U32, foffset : float *) : U32
TestPower(freq : U32, powerLevelDbmv : U32) : U32
ClosePower() : U32
SaveOffsetToNvsram(offset : float) : U32
SaveOffsetToPFD(offset : float) : U32
RFU_Init() : int
```

#### CLink

(from TiApi\_FrontEnd)

```
DeviceProperty_Identify() : void) : U32
ContinuousOutput_ChannelSet(CO_ChNum : U16) : U32
ContinuousWave_Test(CW_ChNum : U16) : U32
```

TiApi_MCard
<ul style="list-style-type: none"> <li>Init() : int</li> <li>PowerOn() : U32</li> <li>IsLoaded() : U32</li> <li>IsLoadedCardVersion() : U32</li> <li>PowerOff() : U32</li> <li>OOBDownStreamTest() : U32</li> <li>AutoPowerOnOff() : void</li> </ul>

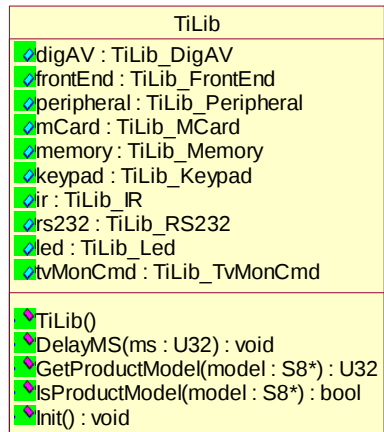
TiApi_Memory
<ul style="list-style-type: none"> <li>FLASH_IDGet(flashID : U16*) : U32</li> <li>FLASH_Protect( : void) : U32</li> <li>FLASH_ErasePfdSector(area : U8) : U32</li> <li>FLASH_dump_content(startAddr : U32 *, endAddr : U32 *) : U32</li> <li>DDR_RW_Test(testPattern : U32*, startAddr : U32, size : U32, Method : U32) : U32</li> <li>DDR_MaxSizeDetect(productName : S8*, getSize : U8*) : U32</li> <li>DMA_DDRTToPCITransfer(pciDev : U8) : U32</li> <li>DMA_PCIToDDRTransfer(pciDev : U8) : U32</li> <li>DMA_DDRTtoDDRTransfer() : U32</li> <li>CACHE_ModeChangeTest() : U32</li> <li>NVSRAM_retention_write_test(pattern : U8) : U32</li> <li>NVSRAM_retention_read_test(pattern : U8) : U32</li> <li>NVSRAM_CopyMtcSudbToNvram() : U32</li> <li>NVSRAM_SetupNvram() : U32</li> <li>FLASH_IsPFDAreaErased() : bool</li> <li>FLASH_TCMTC_SudbCrcCheck() : U32</li> </ul>

TiApi_OurMenu
<ul style="list-style-type: none"> <li>DisplayMenu() : void</li> <li>DisplayLedSubMenu() : void</li> <li>Enter() : void</li> <li>EnterLedSubMenu() : void</li> <li>DumpMemAddr() : void</li> <li>MCardPowerOnOff_Periodically() : void</li> <li>DisplayKeypadValue() : void</li> <li>FLASH_ErasePfdSector() : void</li> <li>EnableTimerInterrupt() : void</li> <li>DisableTimerInterrupt() : void</li> <li>RS232_EnableOutput() : void</li> <li>RS232_DisableOutput() : void</li> <li>DisplayRunningStr(str : char *, keyMatch : U16*, nrKeyMatches : int) : void</li> <li>DisplayLongStr_By4_UntilKeyInMatch(str : char *, keyMatch : U16*, nrKeyMatches : int) : void</li> <li>Led_ErrCodeRun() : void</li> <li>Led_ErrCode4() : void</li> <li>MCard_PowerOnOffPeriodically(ms : int) : void</li> </ul>

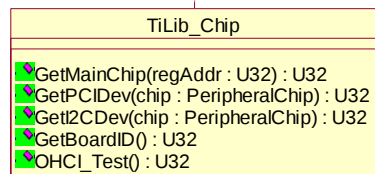
TiApi_Peripheral
<ul style="list-style-type: none"> <li>USB_Test(port : U8) : U32</li> <li>ETHERNET_SingleInterLoopTest() : U32</li> <li>SATA_DiskTest(port : U8) : U32</li> <li>FPIR_Test(key : U8*, timeout : U32) : U32</li> <li>FPKEYPAD_Test() : U32</li> <li>FPLED_Test() : U32</li> <li>ACOUTLET_Test(bStatus : bool) : U32</li> </ul>





























## 1.6. TiLib

The layer be called by layer TiApi or above.



```
enum PeripheralChip
{
    CT_MAIN_CHIP=0, // CT_ : ChipType
    CT_DEMOD,
    CT_1394CHIP,
    CT_MPEG2_ENCODER,
    CT_ENTROPIC,
    CT_BOARD_ID,
    CT_PSOC_FAN,
    CT_PSOC_FP
};
```





TiLib_DigAV	
	hHDMI : BHDM_Handle
	oif : int
	_latestStream : int
	Init() : void
	AUDIO_OpenOutput_Optical() : U32
	AUDIO_OpenOutput_Coaxial() : U32
	AUDIO_SelSrc(stream : int, ulPcrPid : U32, ulAudPid : U32) : U32
	AUDIO_Start1K Tone(stream : U32) : U32
	VIDEO_EnableCompositeAndComponent(stream : int) : void
	VIDEO_SelSrc(stream : int, ulPcrPid : U32, ulVidPid : U32) : void
	VIDEO_PIPDisplay() : void
	VIDEO_SetComponetFormatTo480I(stream : int) : U32
	BypassCableCard() : U32
	RouteToCableCard() : U32
	IsOpen_1394(stream : int) : bool
	OpenOutput_Composite() : U32
	OpenOutput_Component() : U32
	OpenOutput_Hdmi() : U32
	OpenOutput_1394() : U32
	OpenOutput_SVideo() : U32
	OpenOutput_RF() : U32
	OpenOutput_802_11() : U32
	OpenOutput_Ethernet() : U32
	CloseOutput_Composite() : U32
	CloseOutput_Component() : U32
	CloseOutput_Hdmi() : U32
	CloseOutput_RF() : U32
	VidOut_EnablePip(eDisp : etDisplay, eSurf : etSurface, eSrc : etVideoSource) : void





```
enum VCXO_Frequency_Set
{
    VCXO_27_H=0,
    VCXO_27_M,
    VCXO_27_L,
    VCXO_25_GPIO_Output
};
```


#### TiLib\_FrontEnd


 INB\_Parameter : INB\_INIT\_ARG


 OOB\_Parameter : OOB\_INIT\_ARG


 OOB\_Frequency\_Set(Freq\_Sel : eOOB\_Freq\_Sel) : eTiLib\_OOB\_Error


 OOB\_ACQ(: void) : eTiLib\_OOB\_Error


 OOB\_LockStatus\_Get(: void) : eTiLib\_OOB\_Error

 INB\_INIT(: void) : U32

 QAM\_SymboyRate\_Set(DS\_Port : eINB\_Channel\_Sel, DeMod\_Sel : eINB\_DeMod\_Sel) : U32


 QAM\_IF\_AGC\_Control\_Set(Type\_IF\_AGC : int) : U32


 OOB\_INIT(: void) : U32


 OOB\_SymboyRate\_Set(: void) : U32


#### RFU


(from TiLib\_FrontEnd)


 SetFrequency(ulSetFreqKhz : U32) : eRfUpstreamError\_t

 SetPowerLevel(fPowerDbmv : U32) : eRfUpstreamError\_t

 GetPowerLevel(fPowerDbmv : U32 &) : eRfUpstreamError\_t


 Calibrate(offset : float \*) : eRfUpstreamError\_t


 ReadPowerFromPowerMeter(freq : U32, powerLevelDbm : U32\*) : int


 CheckPower(freq : U32, powerLevelDbm : U32) : int


#### CLink


(from TiLib\_FrontEnd)

 CLink\_Parameter : CLink\_INIT\_ARG

 CLink\_Device\_Identify(PCL\_Id : U16 &, Chip\_Version : U16 &) : eTiLib\_CLink\_Error

 CLink\_CO\_Channel\_Tune(CO\_ChNum : U16) : eTiLib\_CLink\_Error

 CLink\_CW\_Channel\_Tune(CW\_ChNum : U16) : eTiLib\_CLink\_Error

 CLink\_INIT(: void) : U32

```

<<struct>>
TiLib_KeyTable
key : KEYTYPE
name : S8 *

```

```

TiLib_Keypad
key : KEYTYPE
longPressed : bool

Read() : KEYTYPE
ReadWithCount(count : U32) : KEYTYPE
SetDebounce(debounce : U8) : void
Init() : void
GetKey() : KEYTYPE
GetKey_U16() : U16
GetKeyWithCount(count : U32) : KEYTYPE
LongPressed() : bool
GetKeyName(key : KEYTYPE) : S8*

```

```

TiLib_IR
hIRChan : BKIR_ChannelHandle
interruptDevice : BKIR_KirInterruptDevice

TiLib_IR()
~TiLib_IR()
Init() : void
GetKey() : U8
ClearData() : void

```

```

TiLib_MCard

IsInit() : bool
DoInit() : U32
OpenSession() : U32
RouteOOBToCableCard() : U32

```

```

TiLib_Memory

DDR_Full_Test(testPattern : U32*, startAddr : U32, size : U32) : U32
DMA_DDRTTo325x() : U32
DMA_325xToDDR() : U32
NVS RAM_CheckMtcSudbInAddr(pSrc : U8*, pDest : U8*, uwBytes : U16) : U32
NVS RAM_CheckMtcSudb() : U32
FLASH_boot_protect(pFlashDriver : FlashDriver *) : U32
DDR_Cross_Test(testPattern : U32*, startAddr : U32, size : U32) : U32
DMA_DDRTToDDR() : U32

```

```

TiLib_Peripheral

USB_CheckVendorDeviceId(str : S8*) : U32
AC_Outlet_Init() : U32
AC_Outlet_Status(bStatus : bool) : U32
WatchDog_Disable() : void

```

```

TiLib_Led
hLed : BLED_Handle

Display_4Digits(uDisplay : U32) : void
TiLib_Led()
Init() : void
FrontOn(bits : char) : void
FrontOff(bits : char) : void
DisplayStr(str : char *) : void
IconOn(iconId : unsigned char) : void
IconOff(iconId : unsigned char) : void
AllOn() : void
AllOff() : void
FastSpin(nn : U32) : void
SlowSpin() : void
DisplayChar(c : char, pos : int) : void

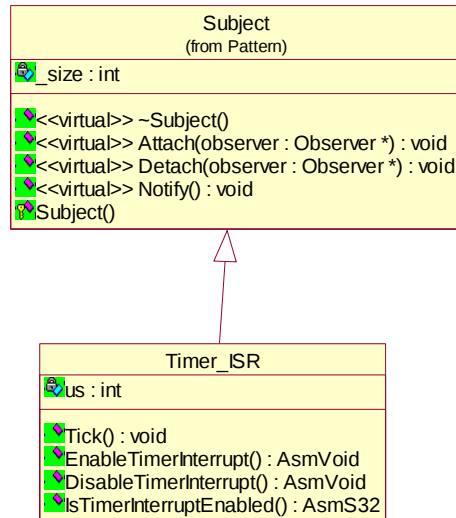
```

```

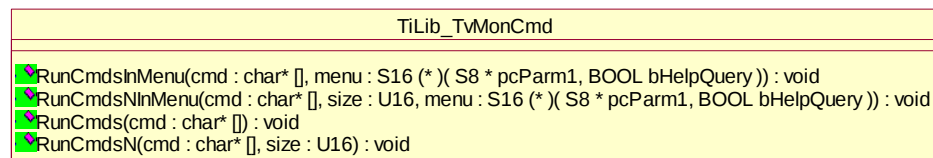
TiLib_RS232
addr : volatile UartChannel *
mode : UartMode
_outputEnable : bool

TiLib_RS232(port : U32)
IsOutputEnabled() : bool
EnableOutput() : bool
DisableOutput() : bool
RxByte(rxByte : U8*) : U32
IsTxRxEnable() : bool
EnableTxRx() : bool
DisableTxRx() : bool
SetUartMode(mode : UartMode) : UartMode
Print(pcFormat : S8*, ...) : int
GetLine(pcBuf : S8*, lBufSize : S32) : void
Print_WithArg(pcFormat : S8*, ap : va_list) : int
GetLine_WithTimeOut(pcBuf : S8*, lBufSize : S32, timeout_us : U32) : bool
GetKey() : U8
ClearData() : void

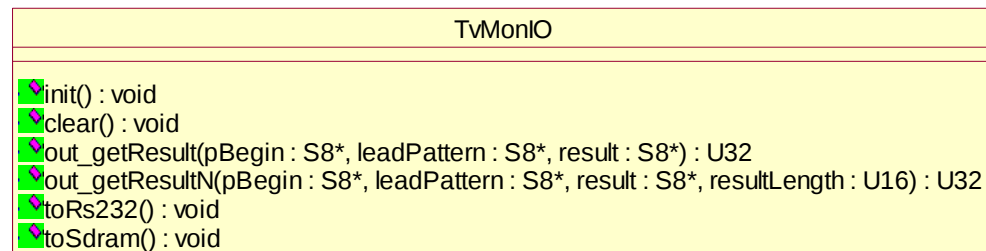
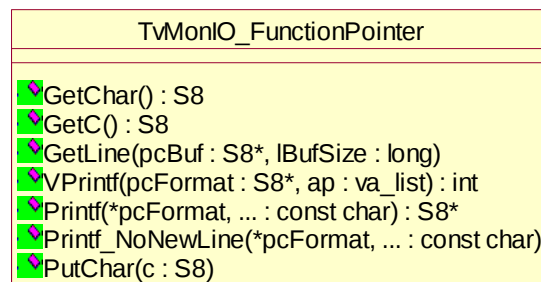
```



## 1.7. TvMonCmd



## 1.8. TvMonIO Function Pointer and TvMonIO (in Broadcom Hal Layer)

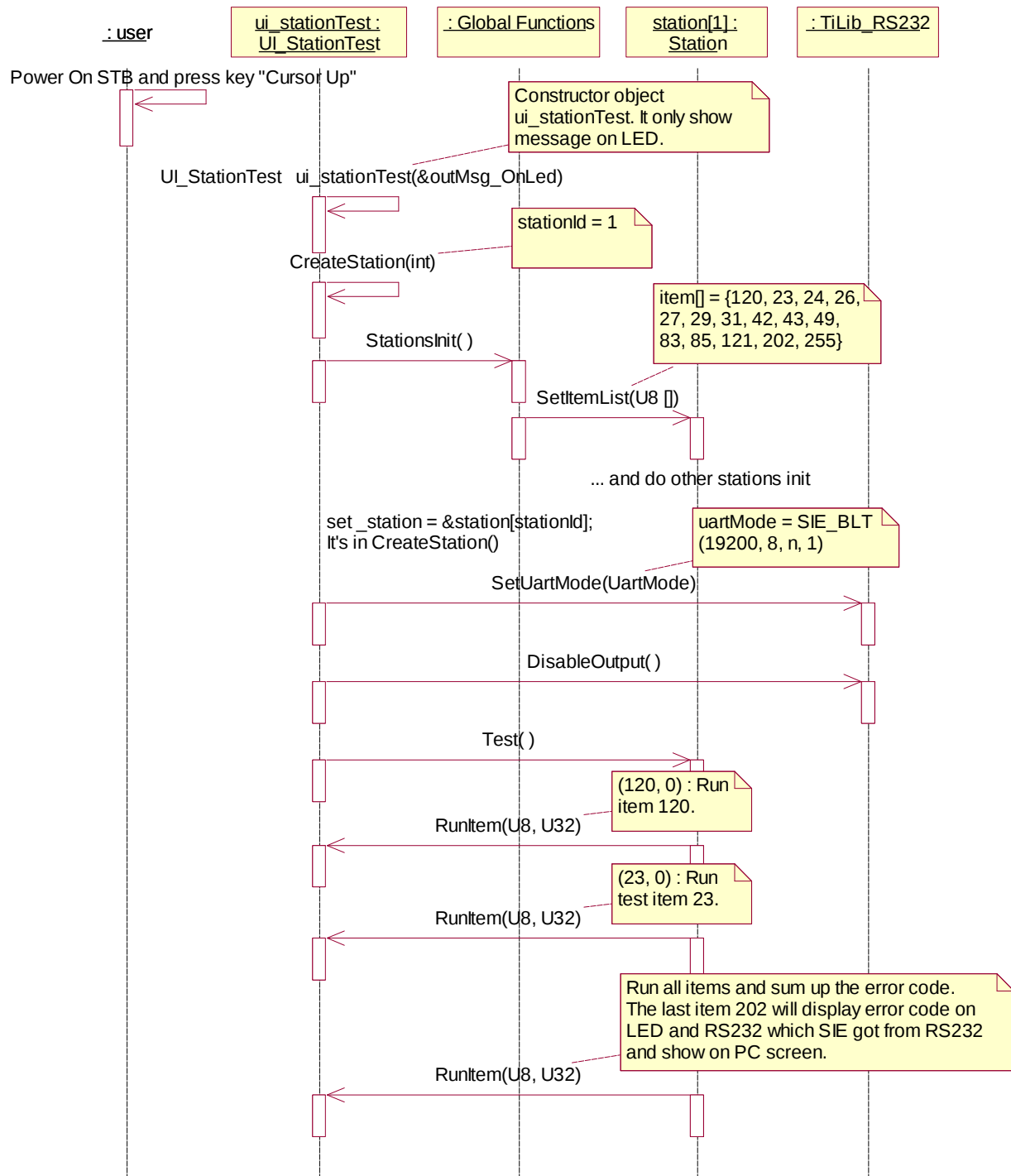


## 2. SEQUENCE DIAGRAM

This section using sequence diagram to describes how objects co-work to finish the system functions. UI\_DebugTest and UI\_StationTest are described in 6.1 User Interface. Some test items are described in 6.2 Items.

### 2.1. User Interface

UI\_StationTest:

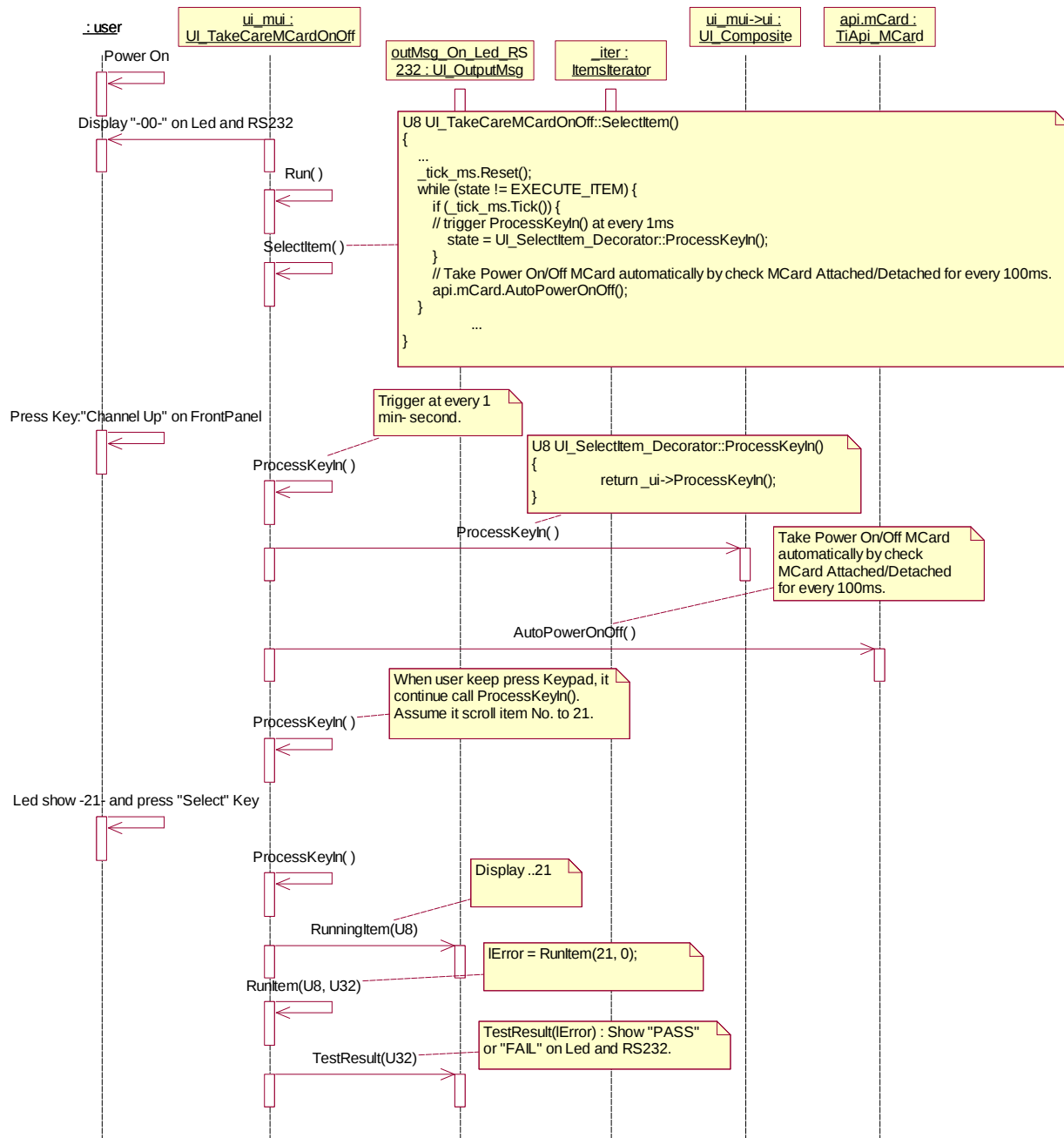


UI\_DebugTest:

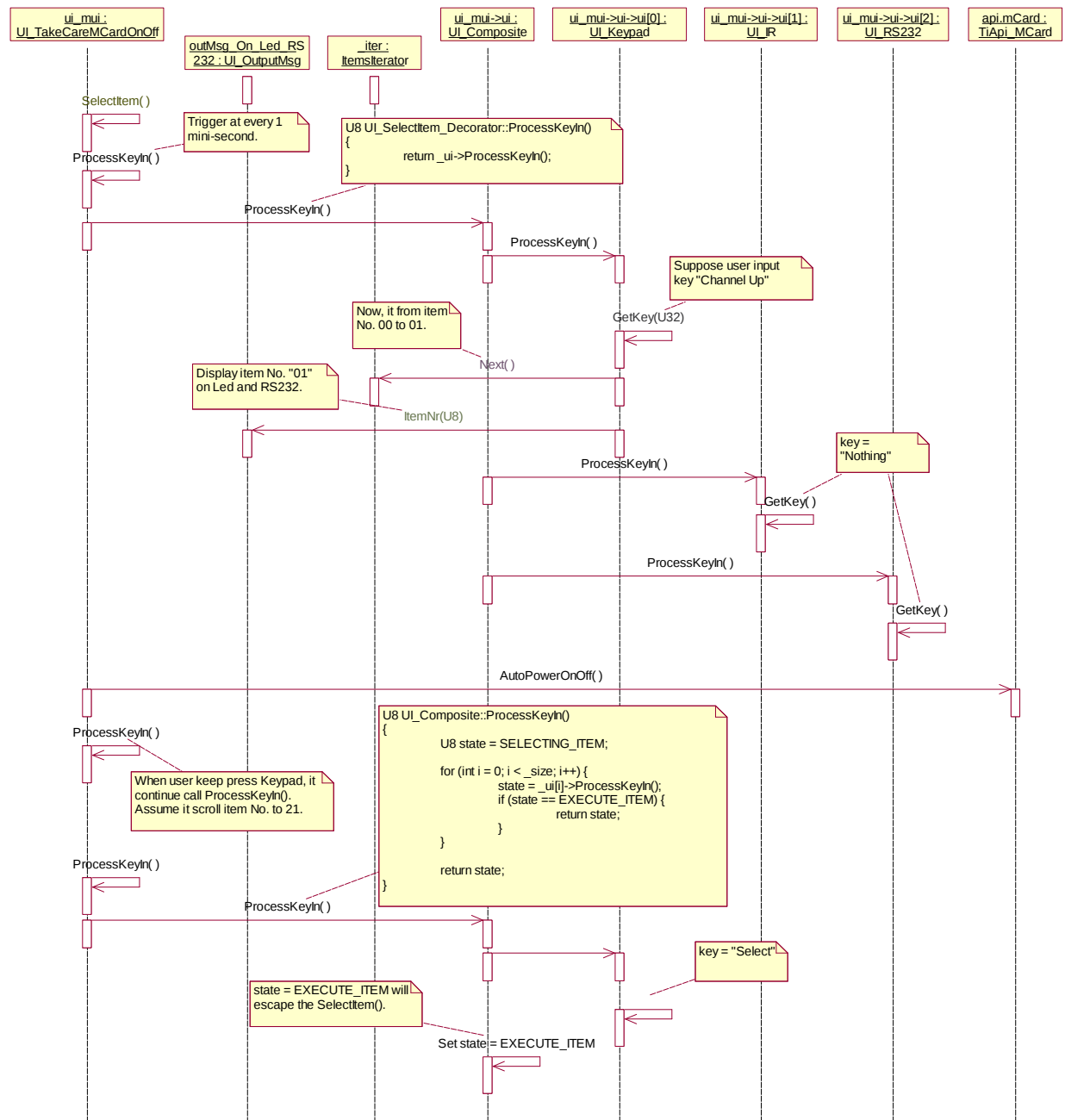
It will damage a few IC in STB during MCard working on AV test. So, add this UI\_TakeCareMCardOnOff to prevent and limit these code in this class. The ui\_mui is an object as type of class UI\_TakeCareMCardOnOff, and include object ui\_mui->ui which include 3 UI objects, UI\_FrontPanel, UI\_IR and UI\_RS232.

SelectItem() will Scroll item No. when user press "Channel Up" or "Channel Down", and escape while loop when press key "Select".

The sequence diagram as follow,



The SelectItem() part as follow,



## 2.2. Items

### Run Item with Time Out:

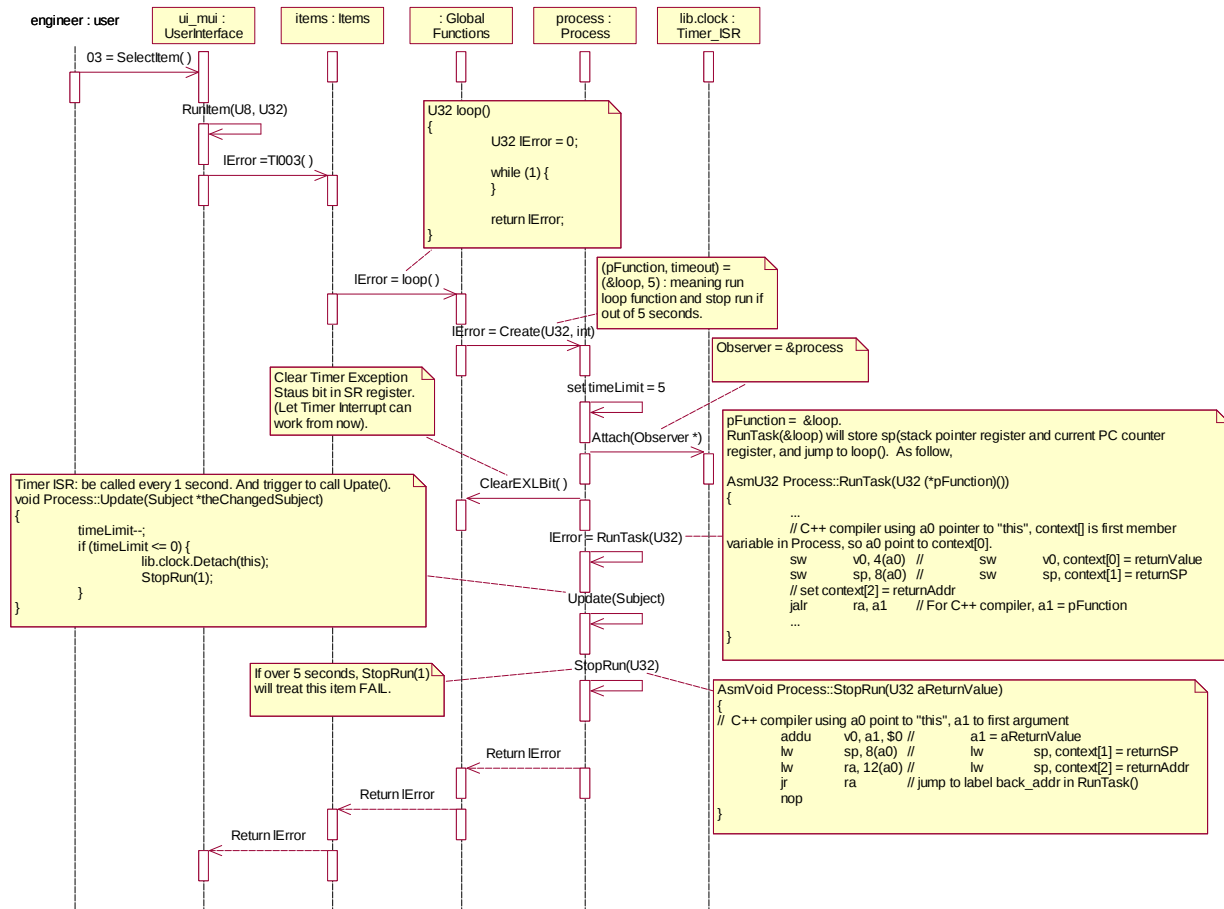
I implement the time out mechanism and used in every test items. BLTC give every test item a time limit to run. If it over, then treat this test item is FAIL. The reasons for time out mechanism as follow,

1. Sometimes especially in EPR or PPR stage (still in implement new test item), BLTC will hang on the test items, for example, the Ethernet Loop Test Item, SATA Test Item, and Flash Vendor ID Test Item will have chance to hang on forever (I

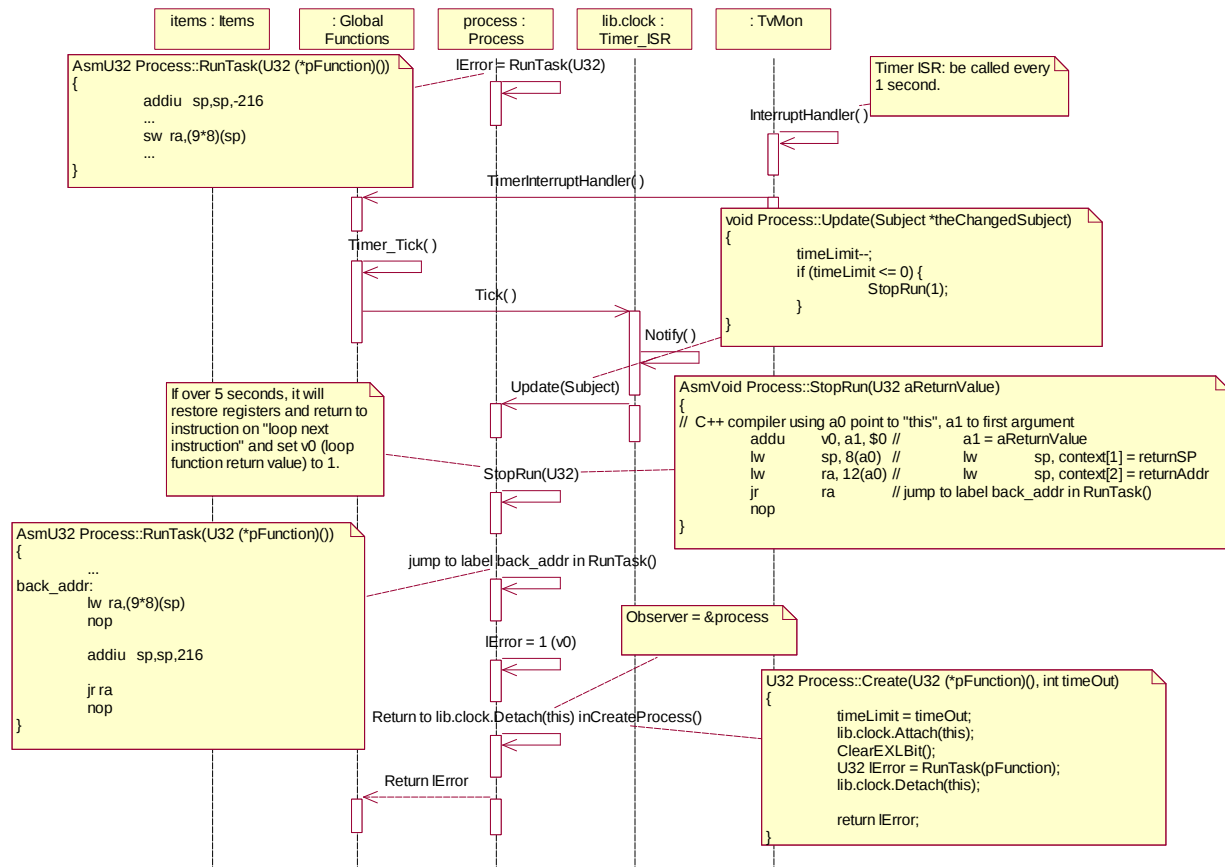


have this experience in DCT34xx and DCH P5 and QIP7216 EPR, and I know it came from the TvMon Layer in some special test order). When it happen, the whole station test cannot go on, and we have no idea to go on. The only way to run the EPR is by skip the whole station test and ask operator to run single item test, one by one. With this mechanism, I don't have to worry about this situation any more (it's a nightmare).

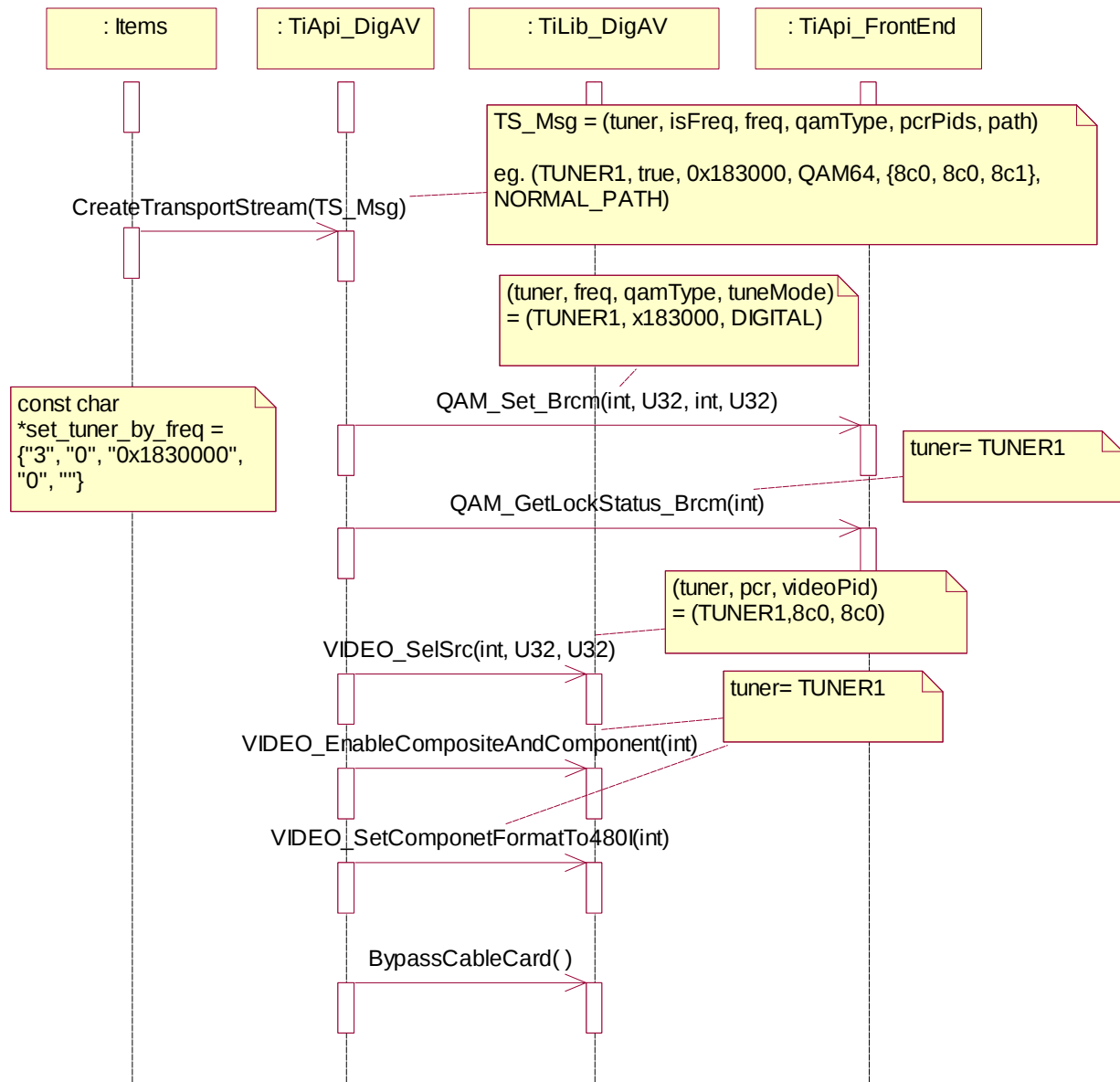
2. BLTC abandoned the solution that stop test as soon as it meet FAIL in run any test item. It run all test items of station and display error code. It's better for rework (find all bad part at one time).



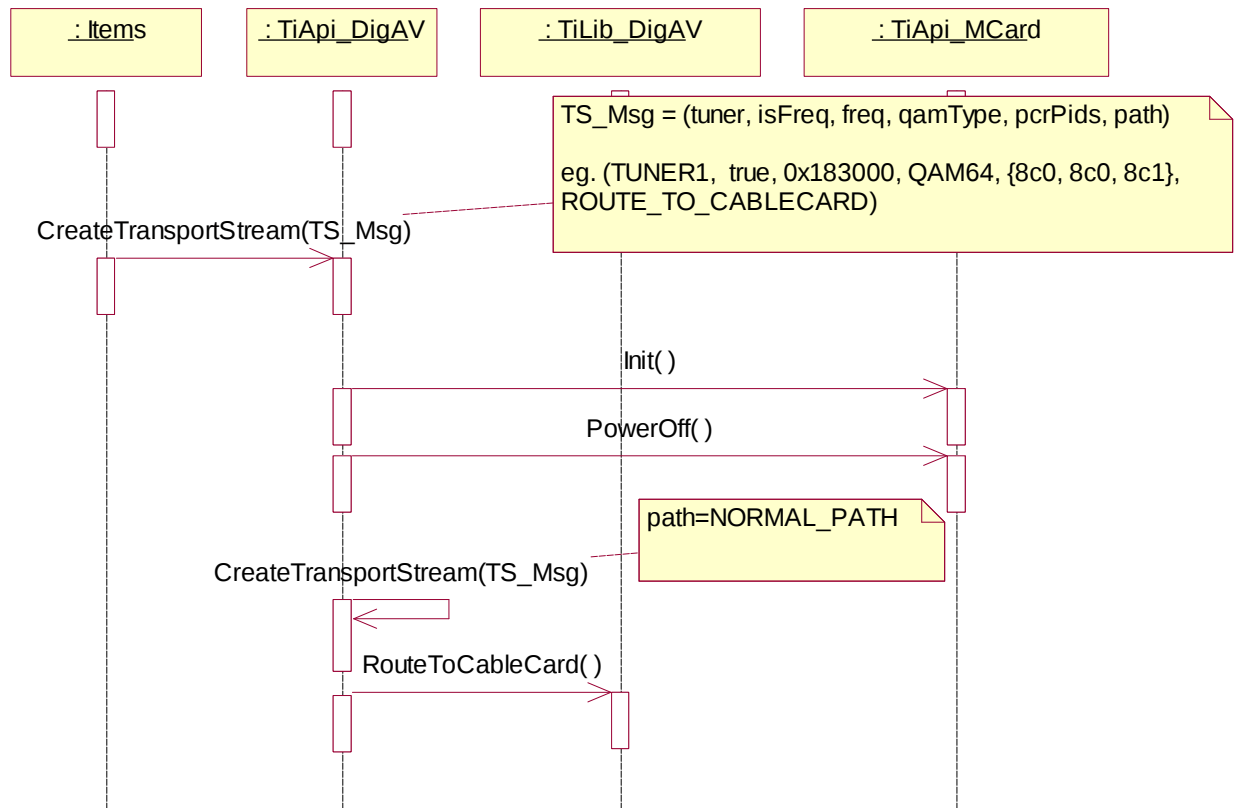
Run Item with Time Out::Process::Update() part,



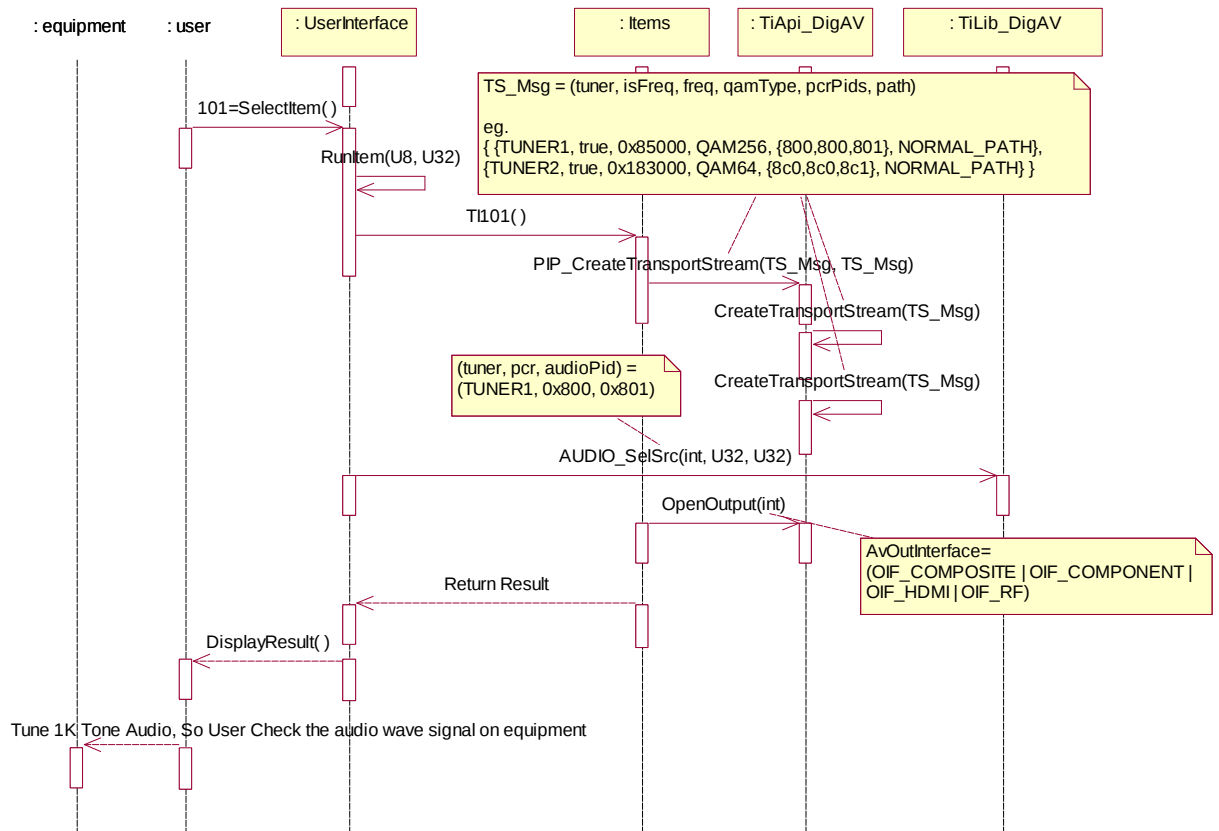
## DigAV\_CreateTransportStream(NORMAL\_PATH)



## DigAV\_CreateTransportStream(ROUTE\_TO\_CABLECARD)



## Digital AV PIP Test (Normal Path)



## Digital AV Test (CableCard and 1394)

