

# PT 5230 Digital Video Generator



## Service Manual – section 2:

### 17-1 Diagrams and component placements.

#### Main Unit

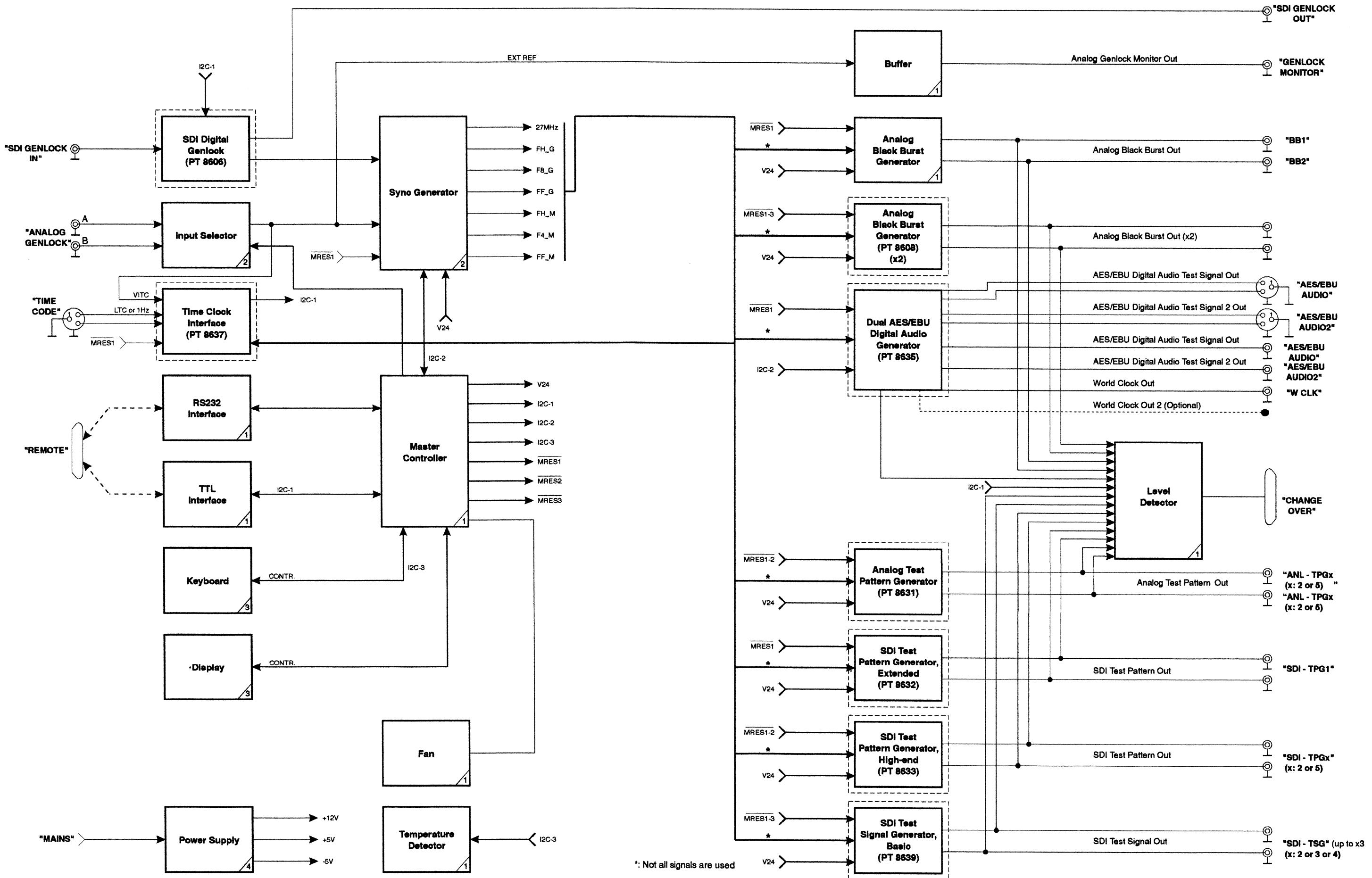
Instrument Block diagram .....	17-2
Power Supply Connections .....	17-3
<b>Unit 1 - Mainboard</b>	
Block diagram.....	17-4
Component Location .....	17-5
Diagrams .....	17-8
<b>Unit 2 - Sync Generator</b>	
Block Diagram .....	17-14
Component Location .....	17-15
Diagrams .....	17-16
<b>Keyboard</b>	
Component Location .....	17-19
Diagrams .....	17-20
<b>Remote Interface</b>	
Component Location & diagram.....	17-21

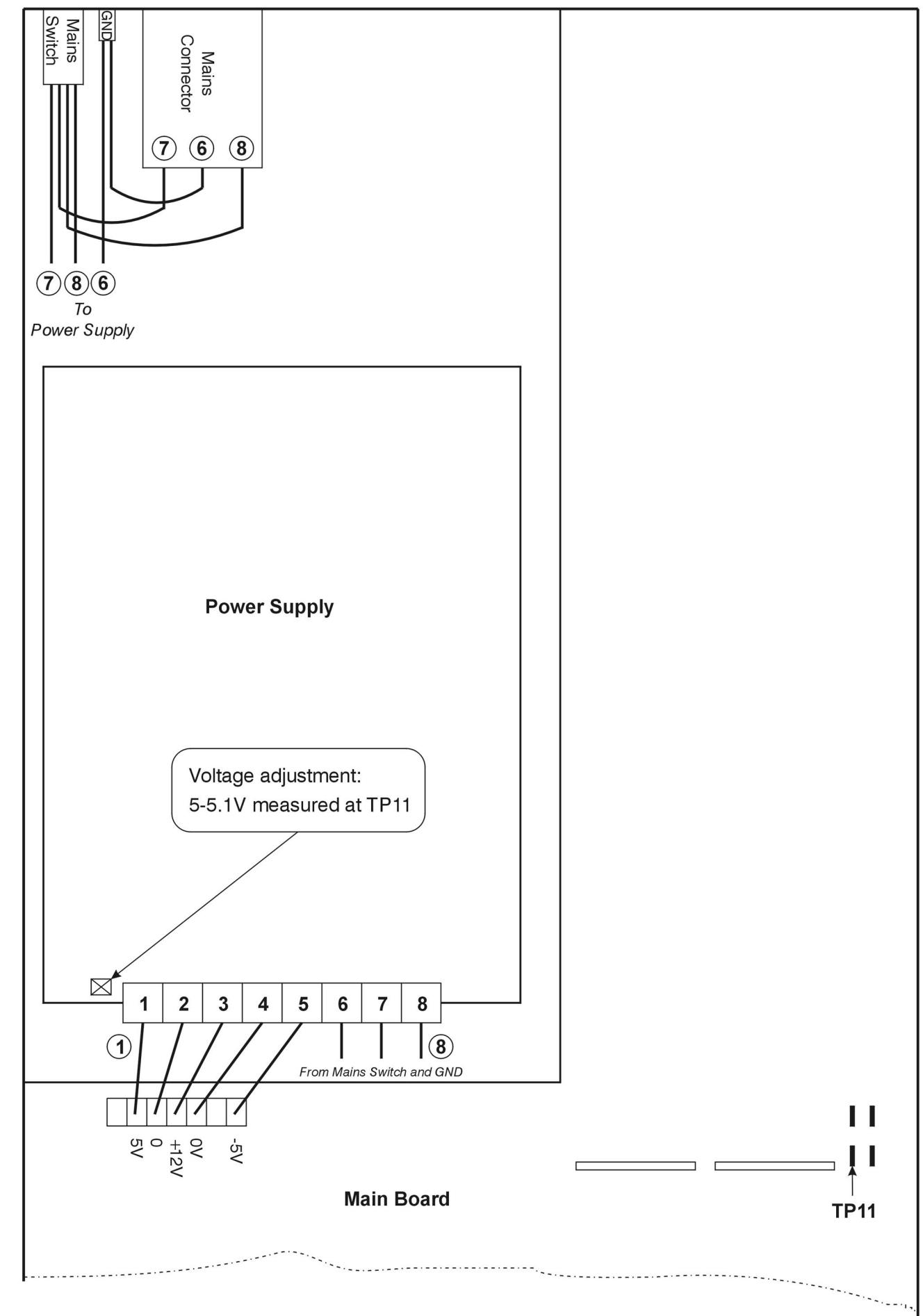
#### OPTIONS

<b>PT8606 – SDI Digital Genlock</b>	
Component Location .....	17-22
Diagram.....	17-23
<b>PT 8631 – Analog Test Pattern Generator</b>	
Block Diagram.....	17-24
Component Location .....	17-26
Diagrams.....	17-27
<b>PT 8632 - SDI Test Pattern Generator</b>	
Block diagram .....	17-29
Component Location .....	17-30
Diagrams.....	17-32
<b>PT 8633 - SDI Test Pattern Generator</b>	
Block diagram .....	17-34
Component Location .....	17-35
Diagrams.....	17-36
<b>PT 8635 - AES/EBU Audio Generator</b>	
Block diagram .....	17-39
Component Location .....	17-40
Diagrams.....	17-41
<b>PT 8637 - Time clock Interface</b>	
Block diagram .....	17-45
Component Location .....	17-46
Diagrams.....	17-47
<b>PT 8639 - SDI Test Signal Generator, Basic</b>	
Block diagram .....	17-49
Component Location .....	17-50
Diagrams.....	17-51

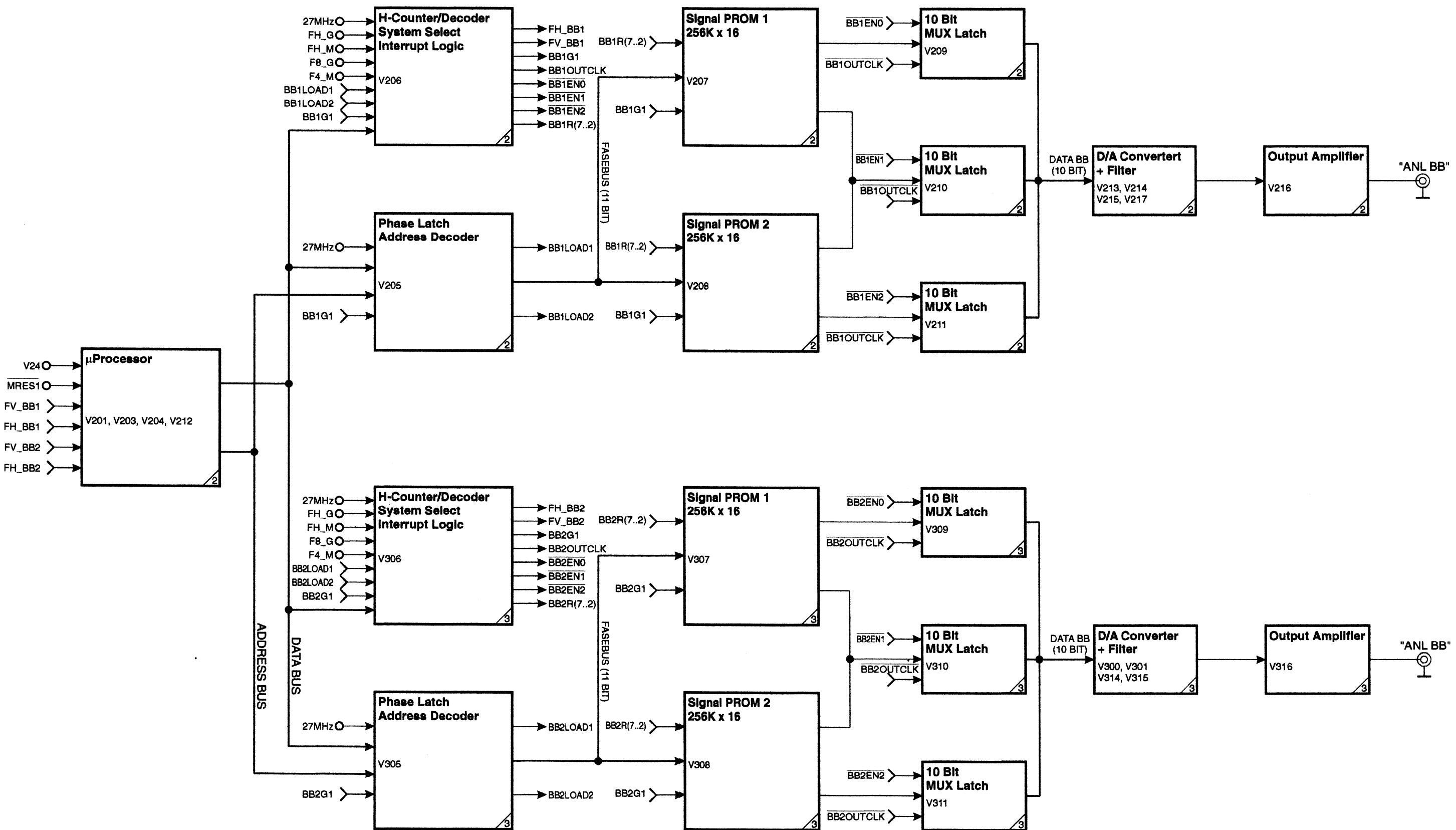
#### Compatible options from PT 5210

<b>PT 8604 - Multiple Black Burst Output</b>	
Component Location .....	17-53
Diagrams.....	17-54
<b>PT 8608 - Analog Black Burst Generator</b>	
Block diagram .....	17-59
Component Location .....	17-60
Diagrams.....	17-61

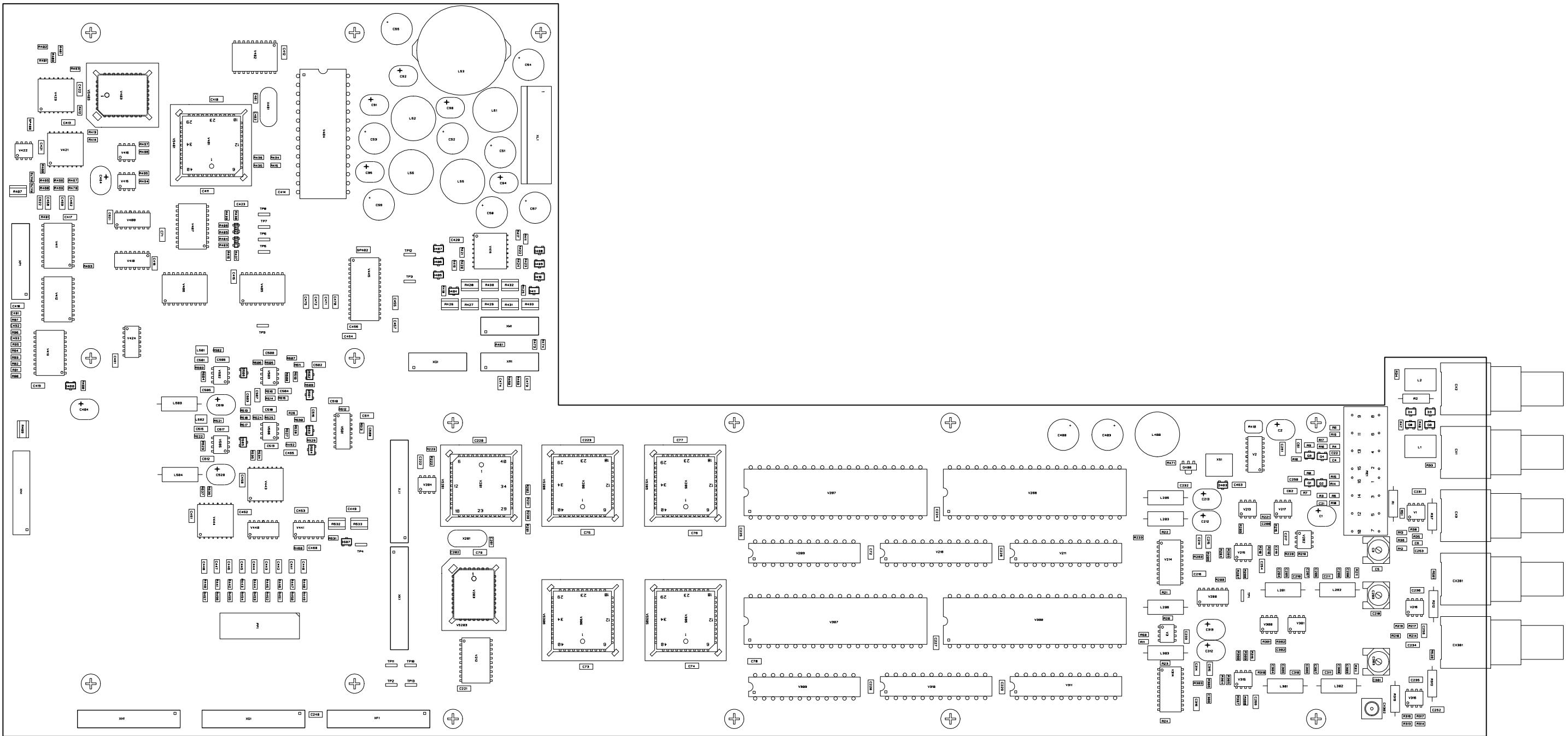




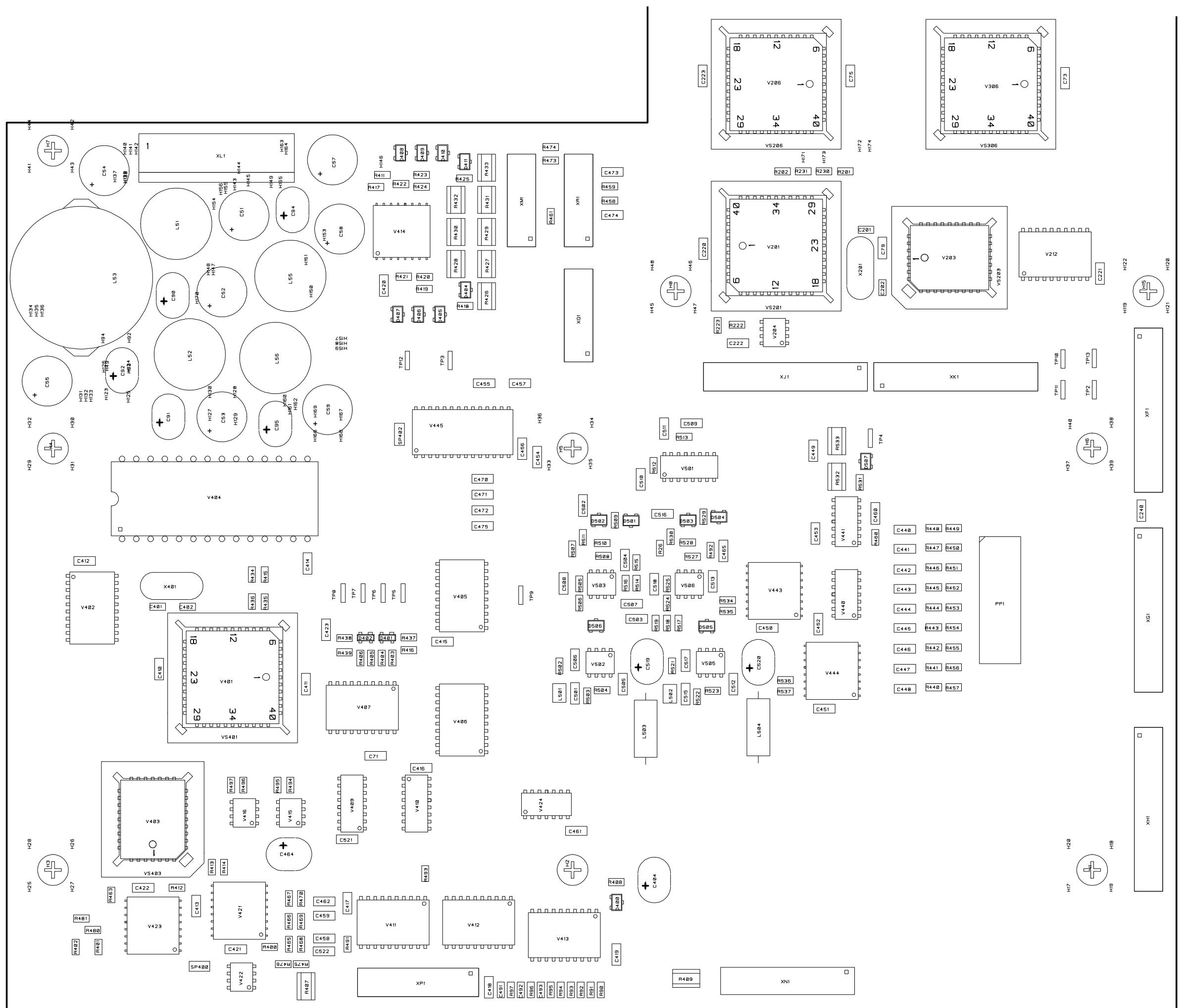
Power supply connections



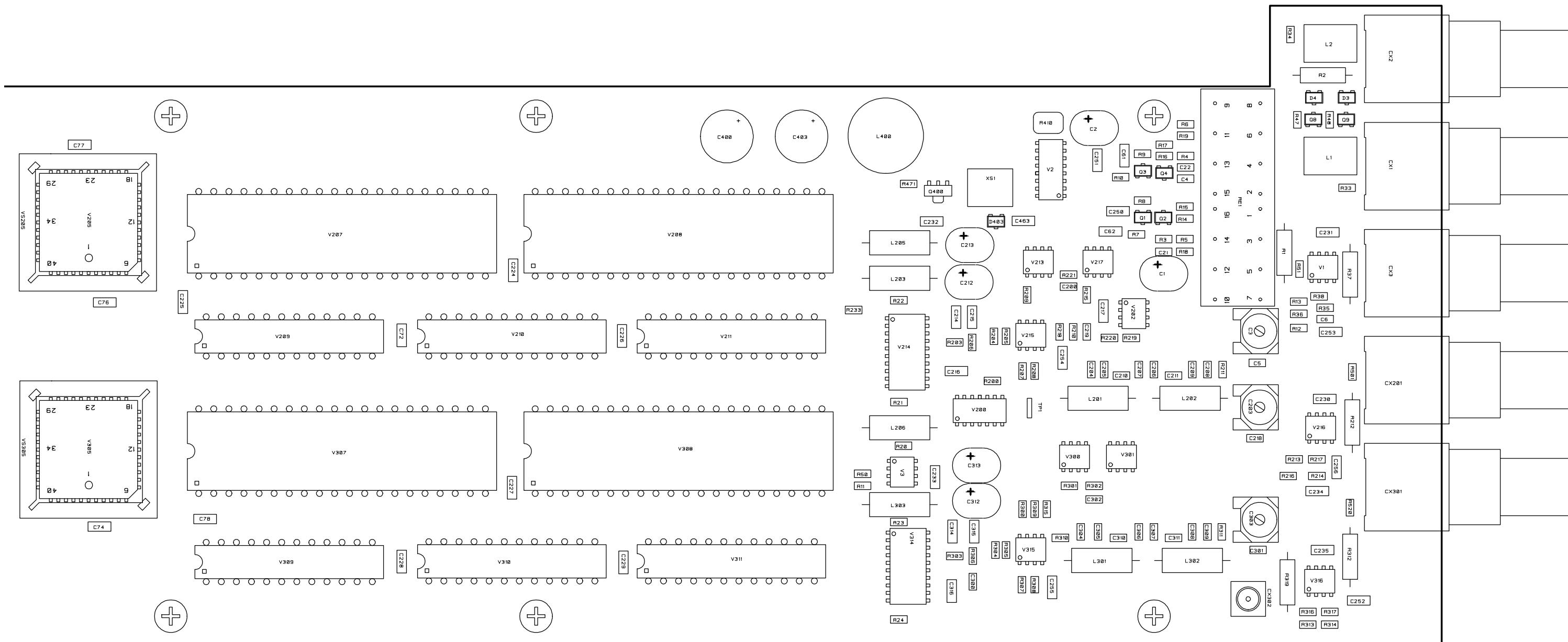
Blockdiagram Mainboard



## Mainboard - full

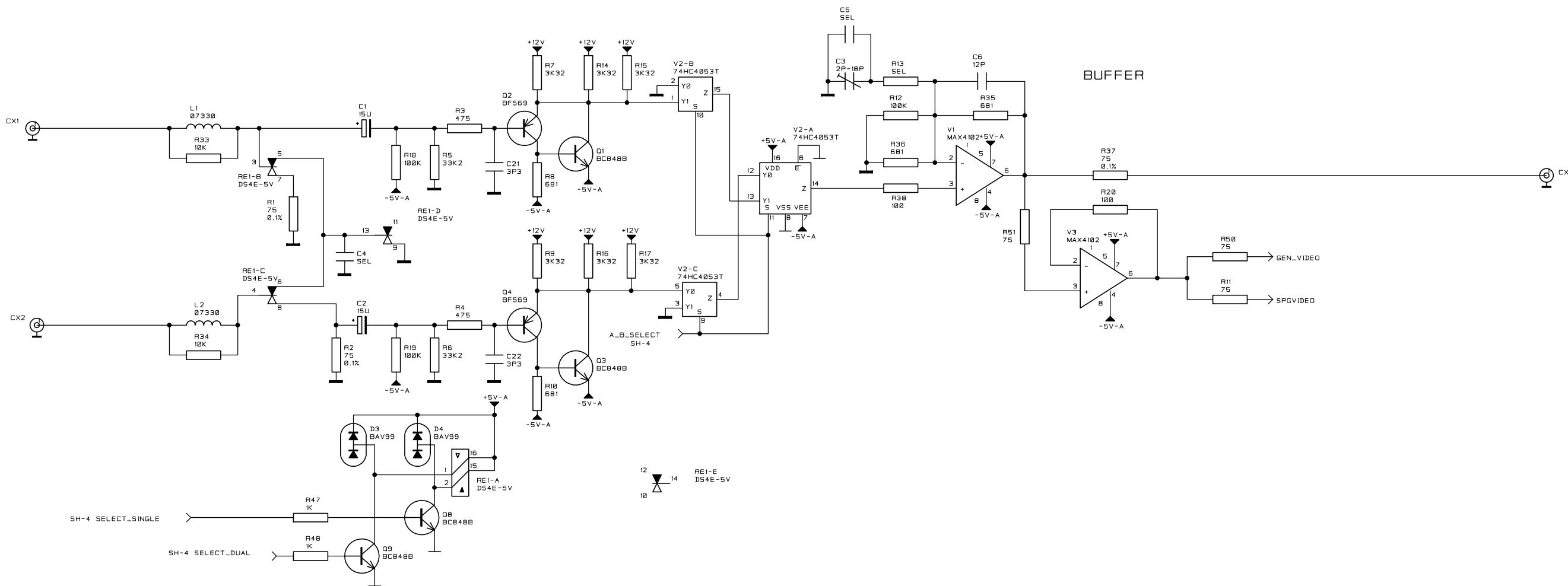


## Mainboard - part 1

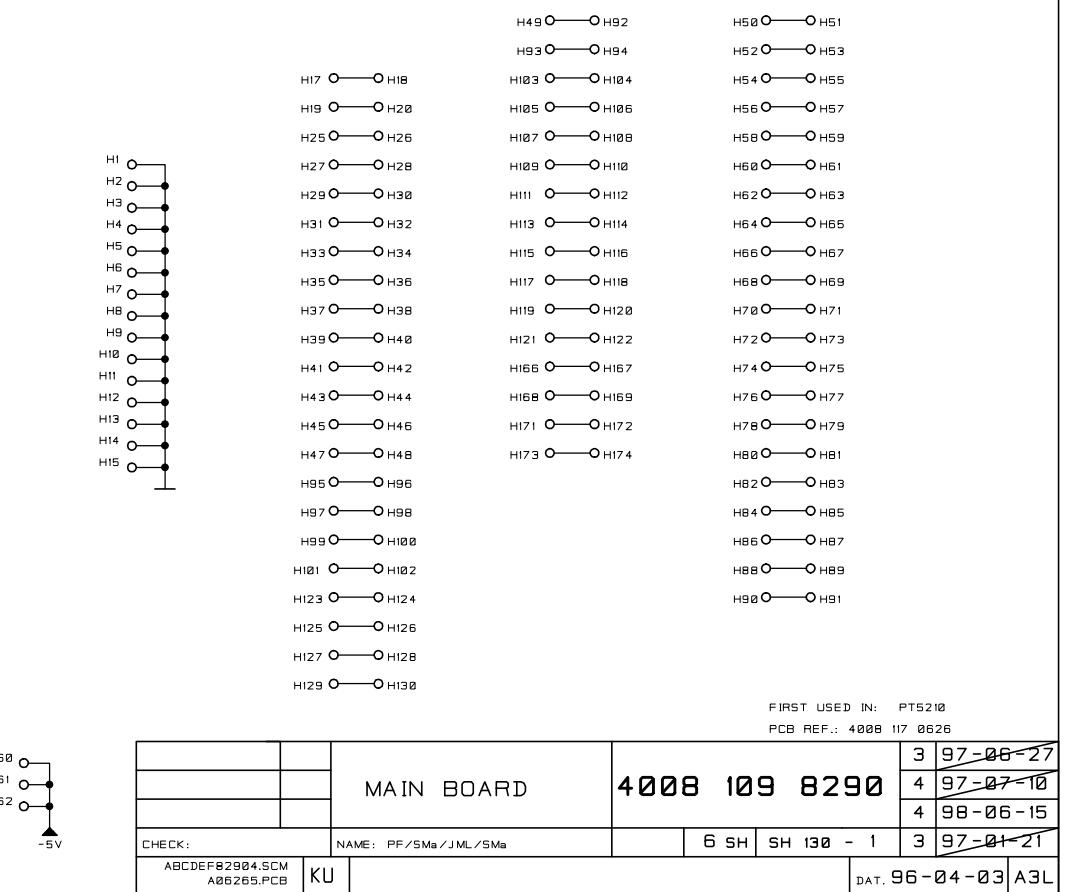
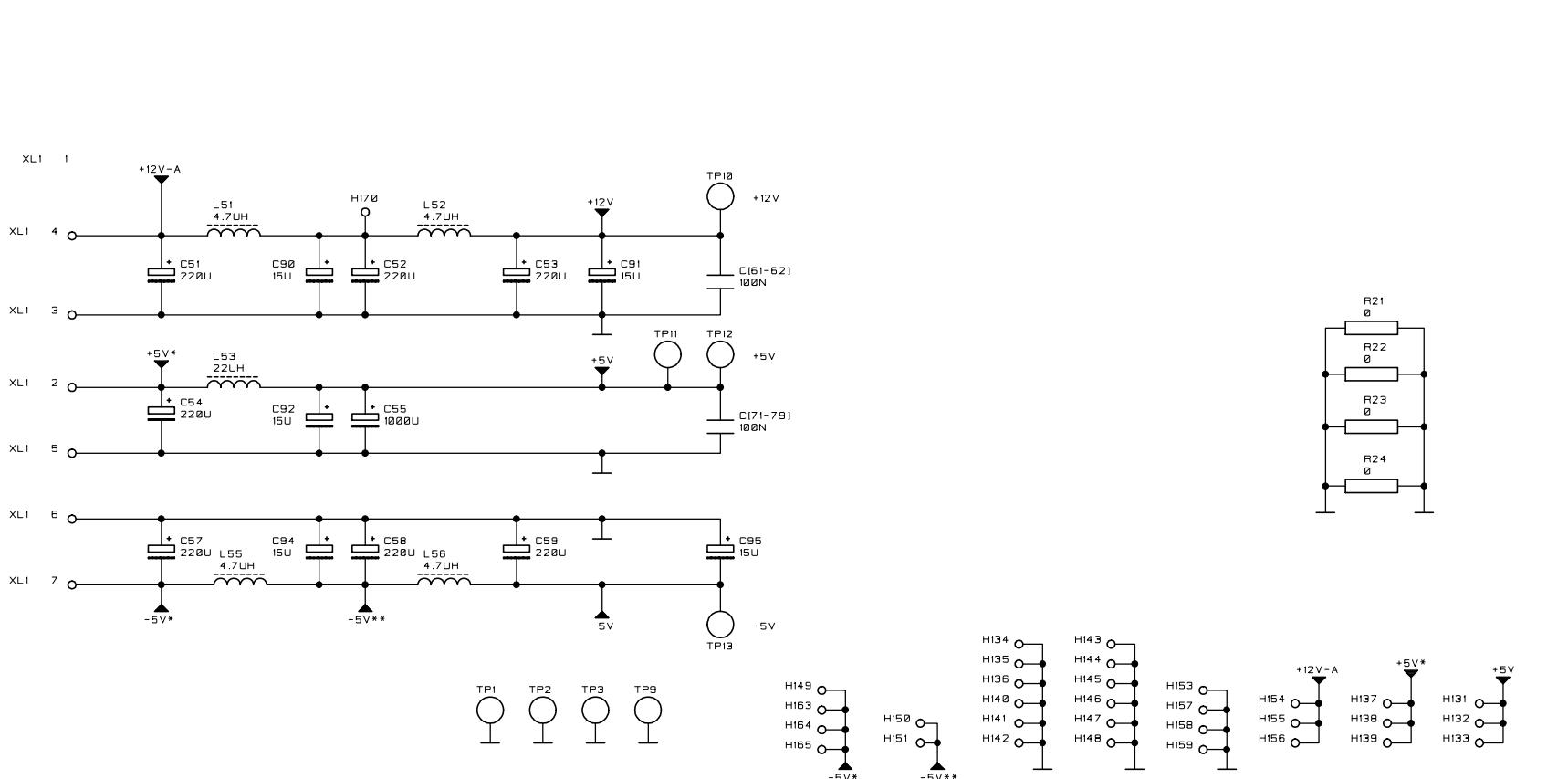


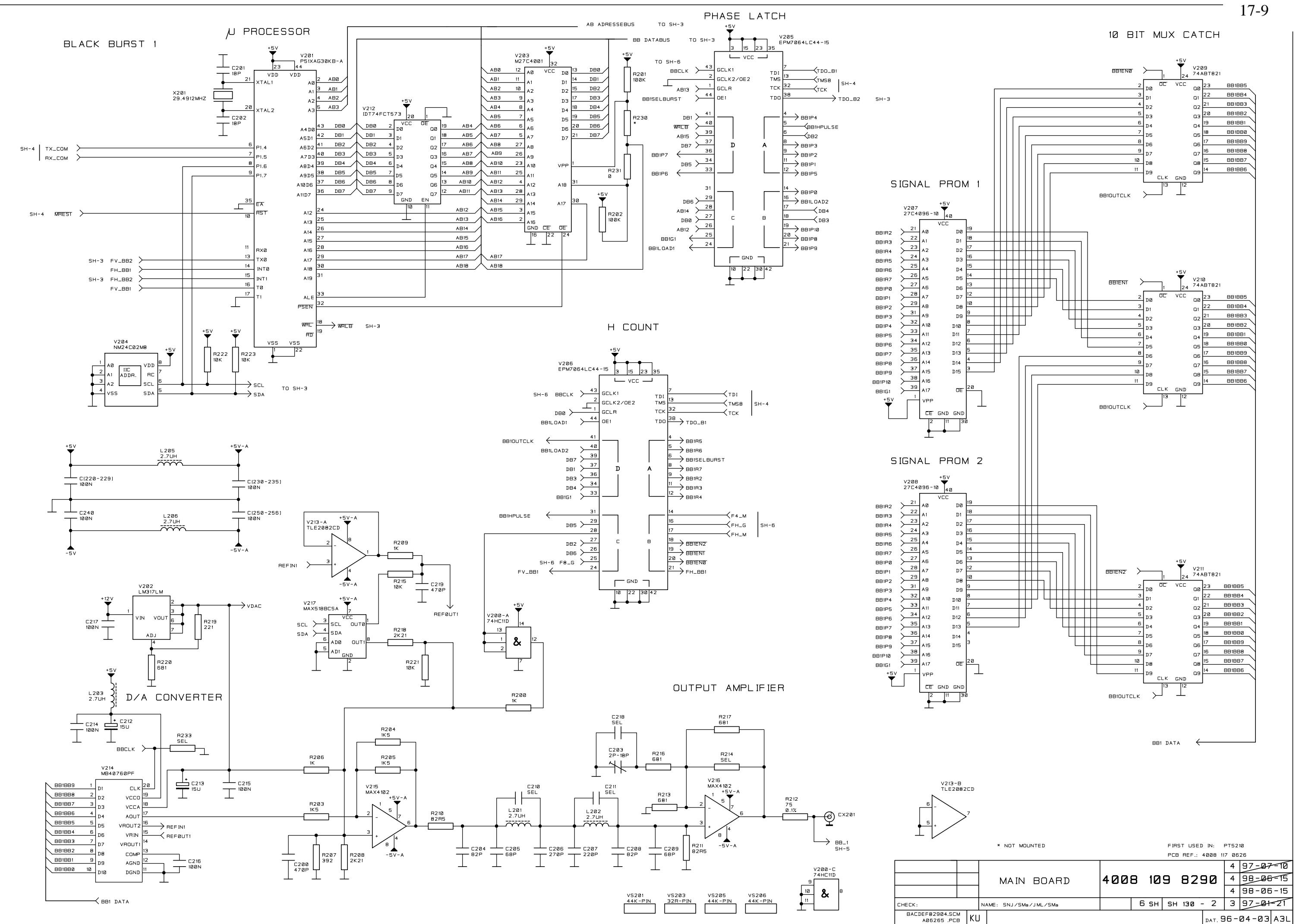
## Mainboard - part 2

## INPUT SELECTOR

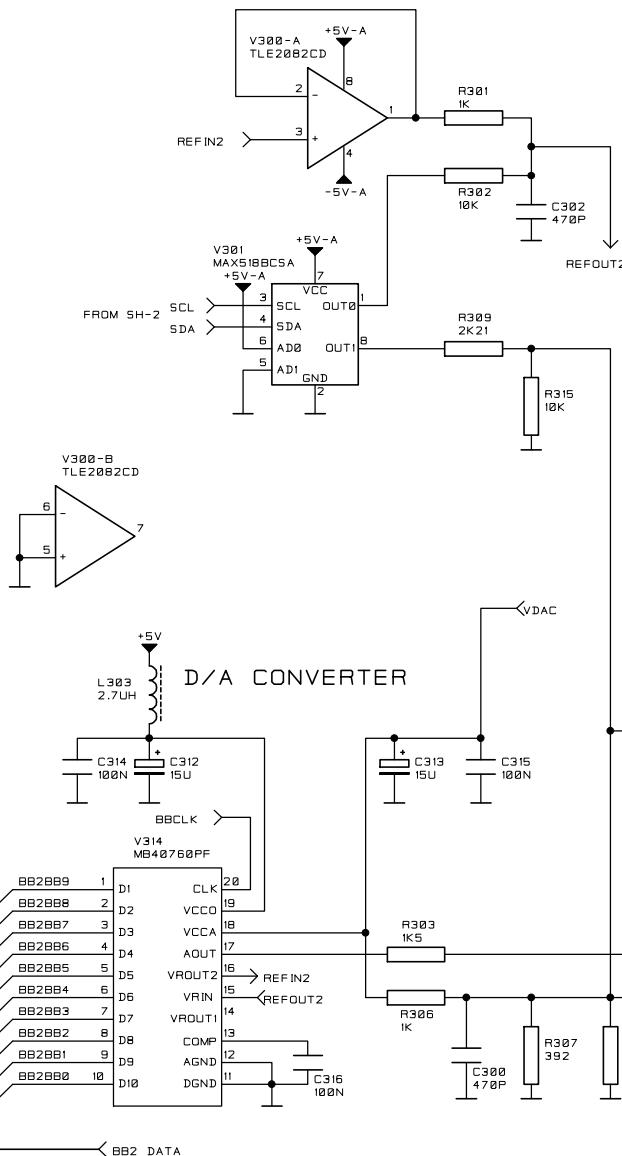


## BUFFER





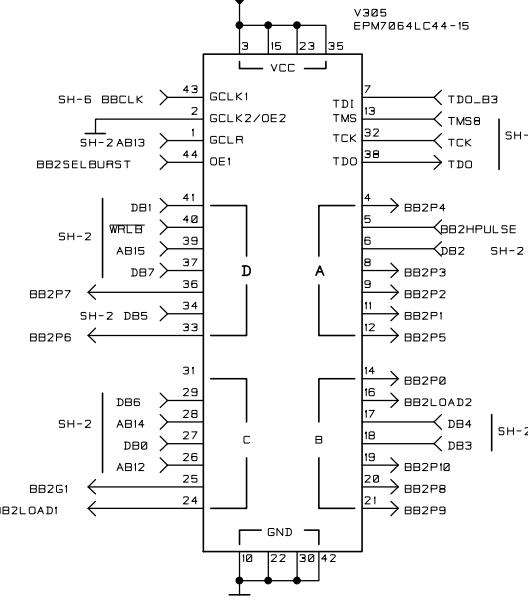
## BLACK BURST 2



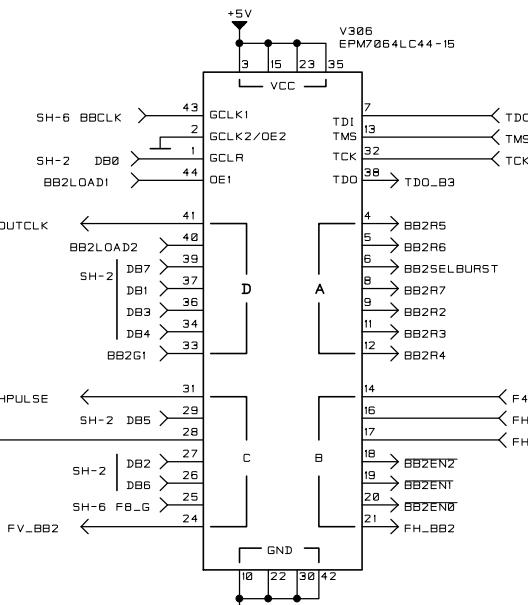
BB DATABUS FROM SH-2

BB ADRESSEBUS FROM SH-2

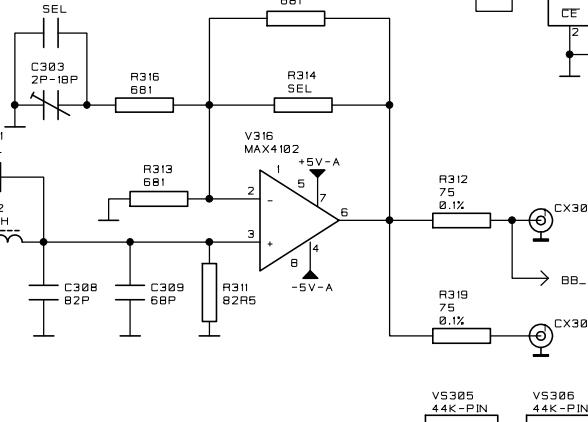
## PHASE LATCH



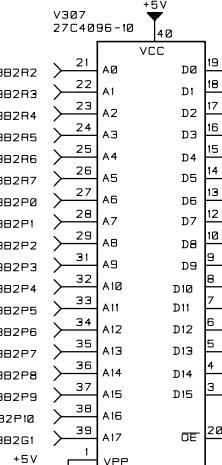
## H COUNT



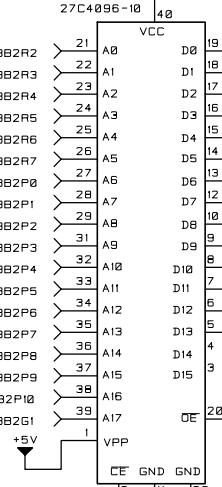
## OUTPUT AMPLIFIER



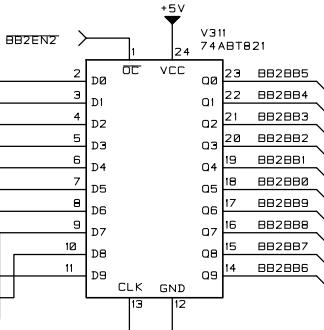
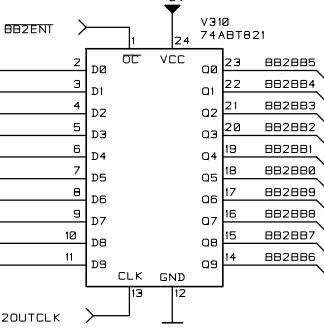
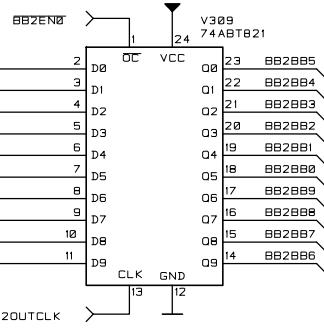
## SINAL PROM 1



## SINAL PROM 2



## 10 BIT MUX LATCH

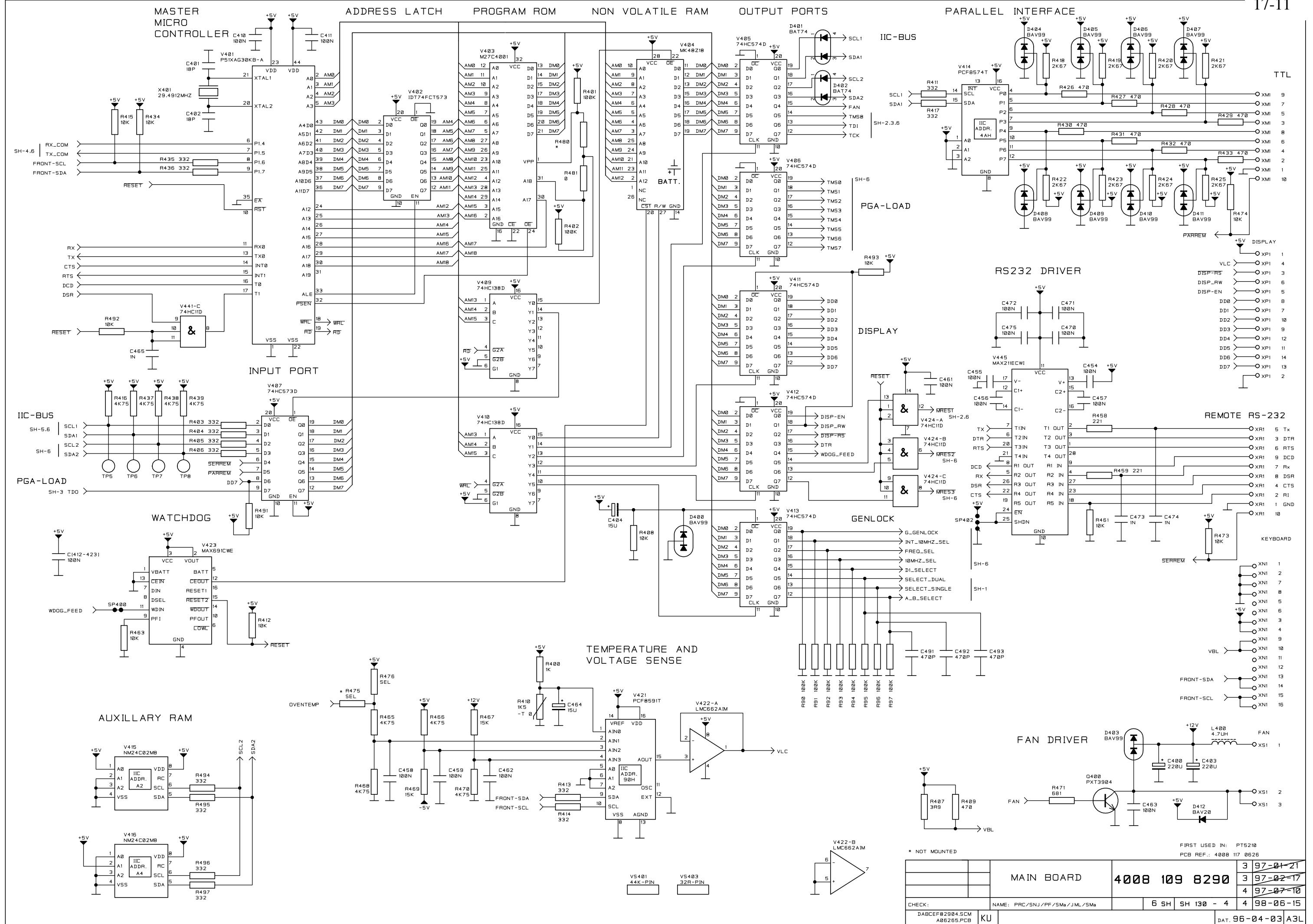


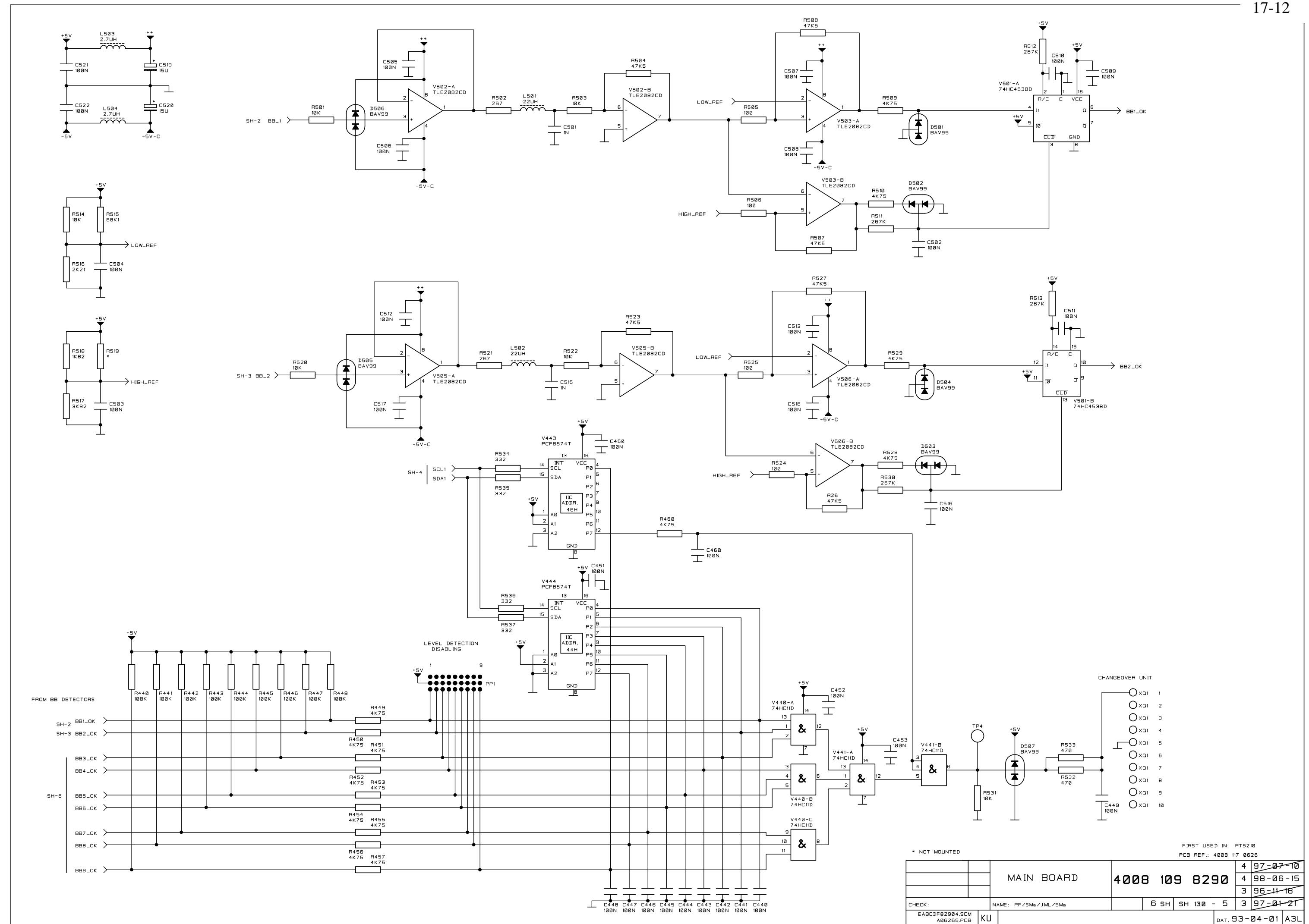
BB2 DATA

FIRST USED IN:	PT5210
PCB REF.:	4008 109 8290
4	97-07-10
4	98-06-15
3	96-11-21
3	97-01-21

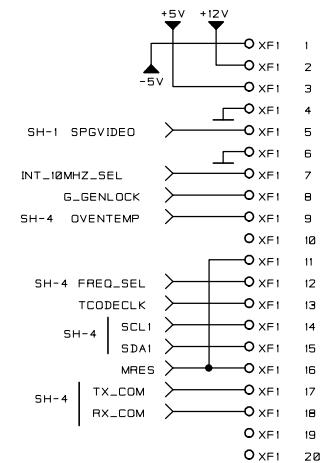
MAIN BOARD 4008 109 8290

CHECK:	NAME: SNJ/SMa / JML / SMa	6 SH	SH 130 - 3	3 97-01-21
CABDEF82984.SCM A80265.PCB	KU			DAT. 96-04-03 A3L

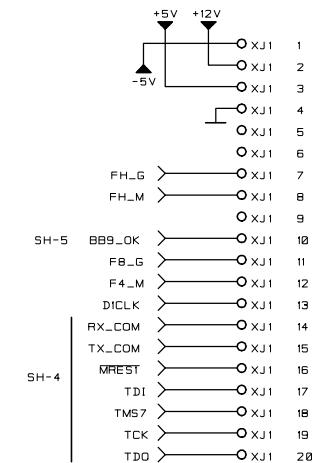




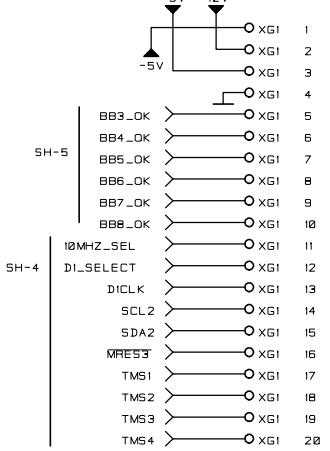
## TO SYNC PULSE GENERATOR



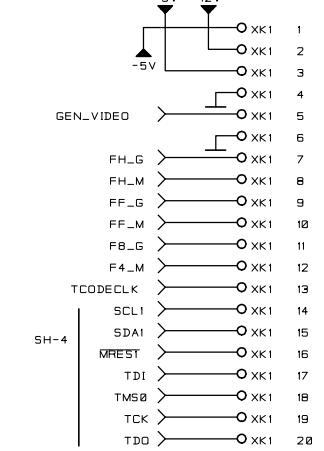
## TO D1 GENERATOR



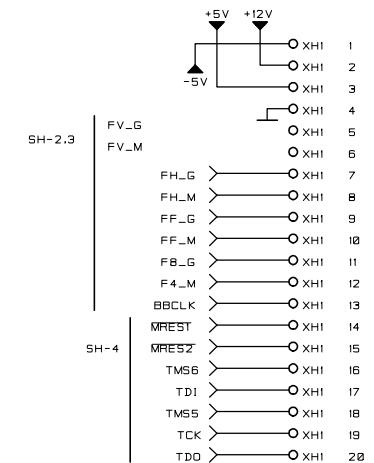
## TO SYNC PULSE GENERATOR



## TO TIMECODE



## TO SYNC PULSE GENERATOR

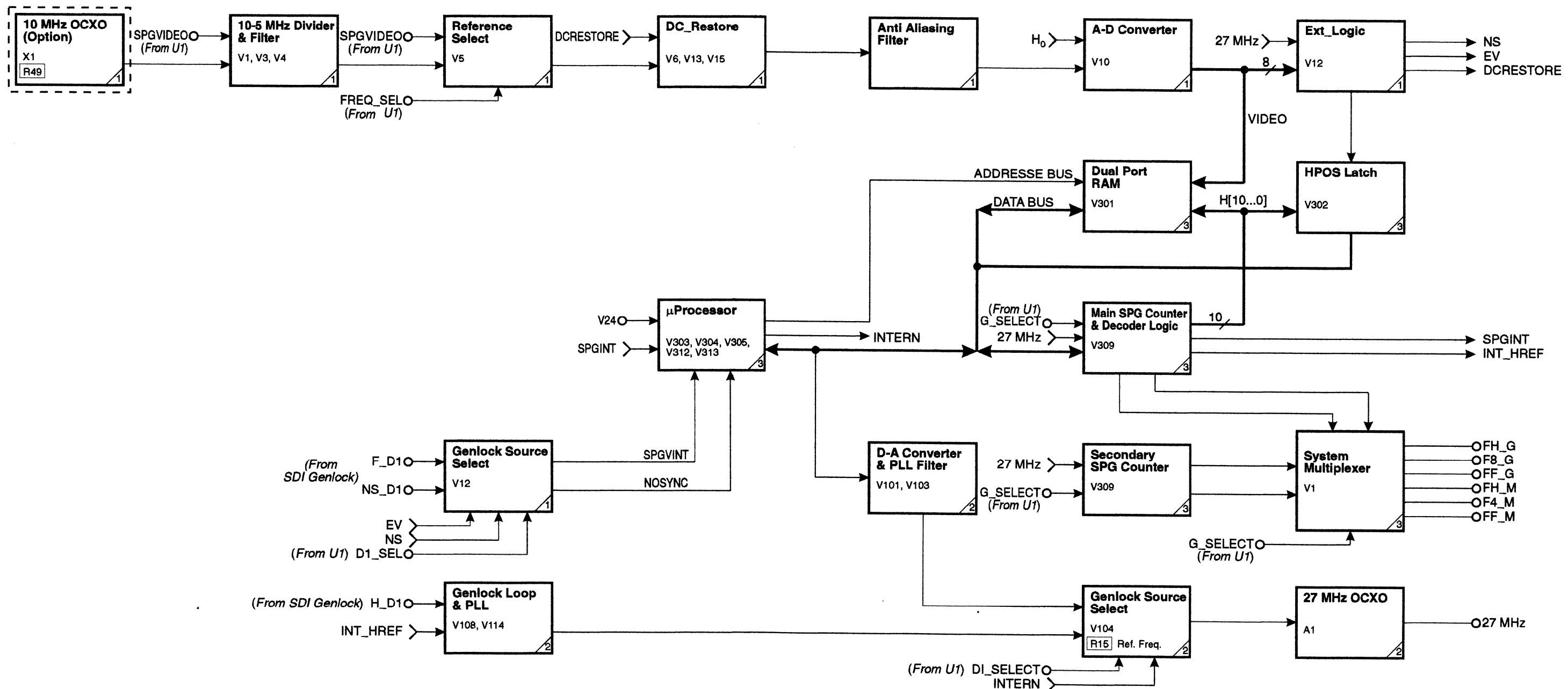


FIRST USED IN: PT5210  
PCB REF.: 4008 109 8290

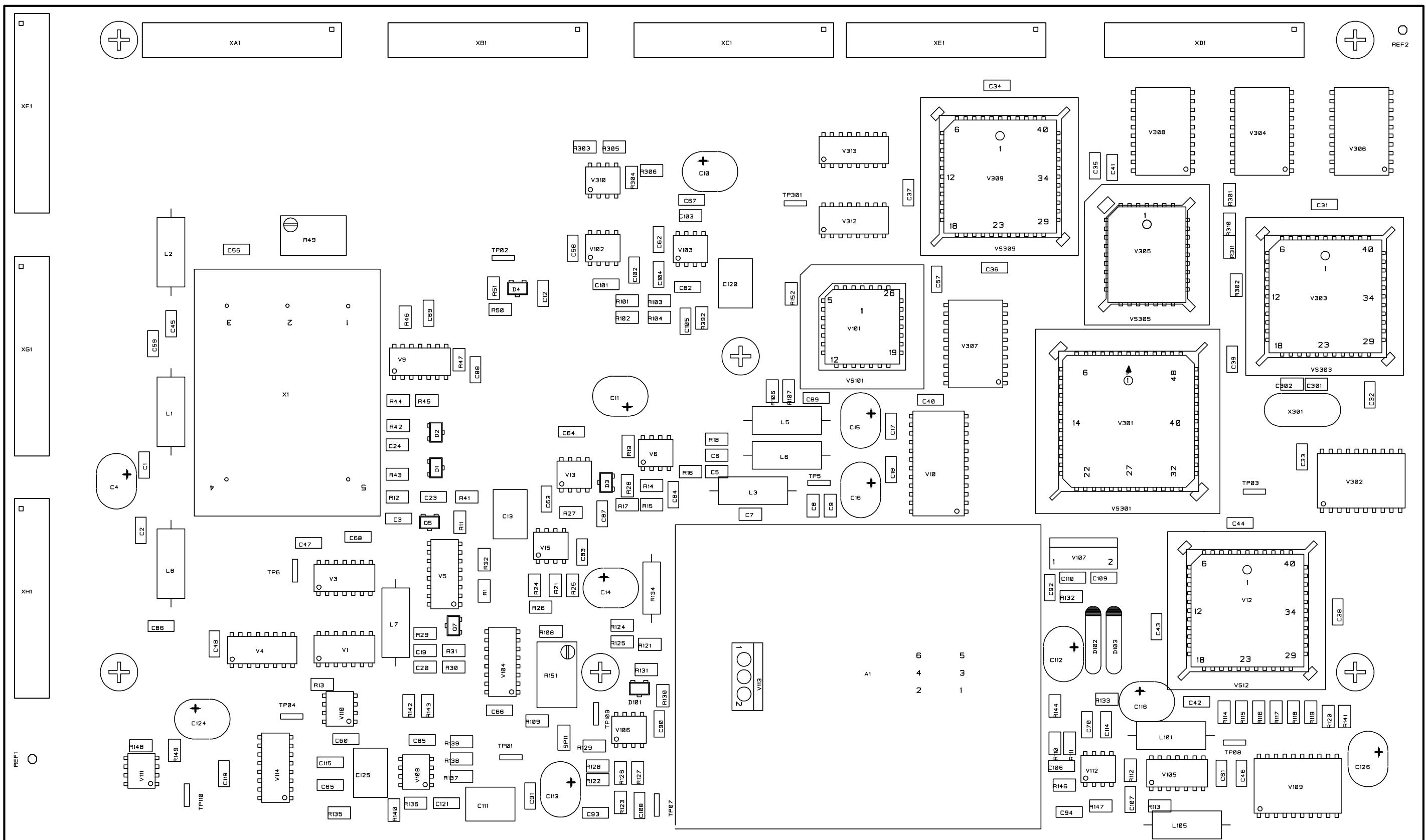
MAIN BOARD

4008 109 8290

			4	97-07-10
			4	98-06-15
			3	96-11-18
CHECK:	NAME: SNJ/SMa/JML/SMa	6 SH	SH 130 - 6	3 97-01-21
FABCDE82984.SCM A80265.PCB	KU			DAT. 96-03-28 A3L



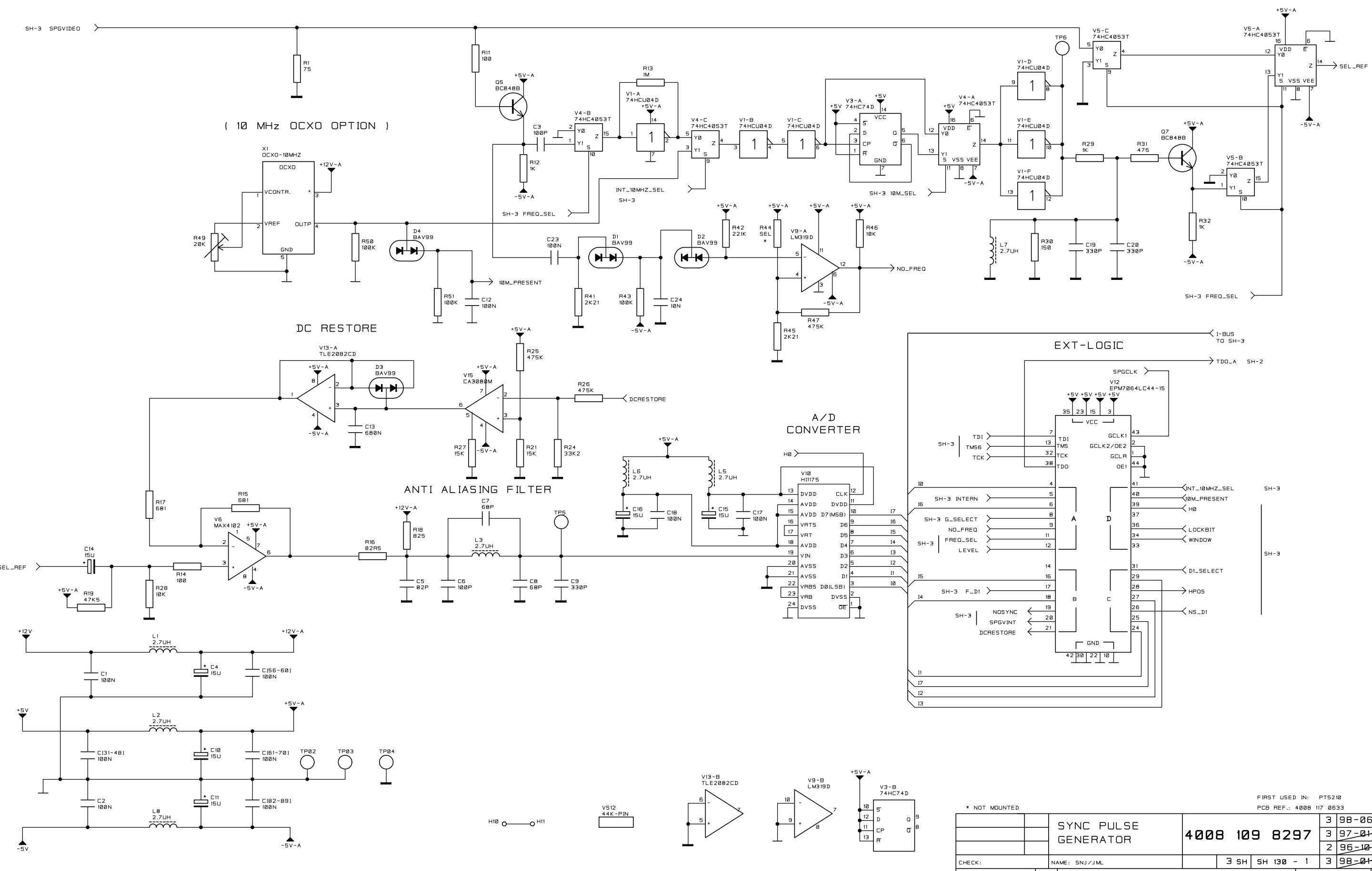
Blockdiagram - sync generator

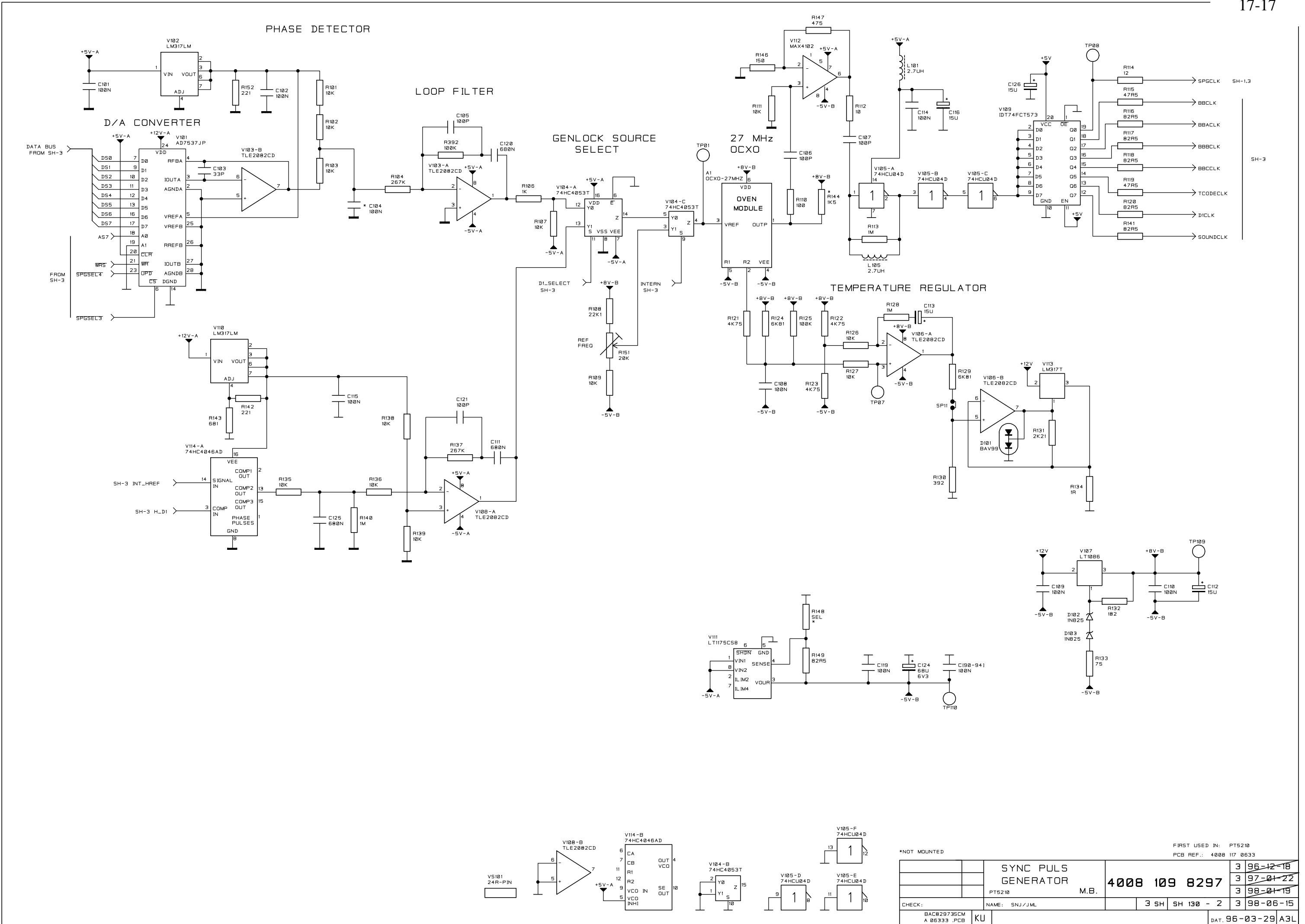


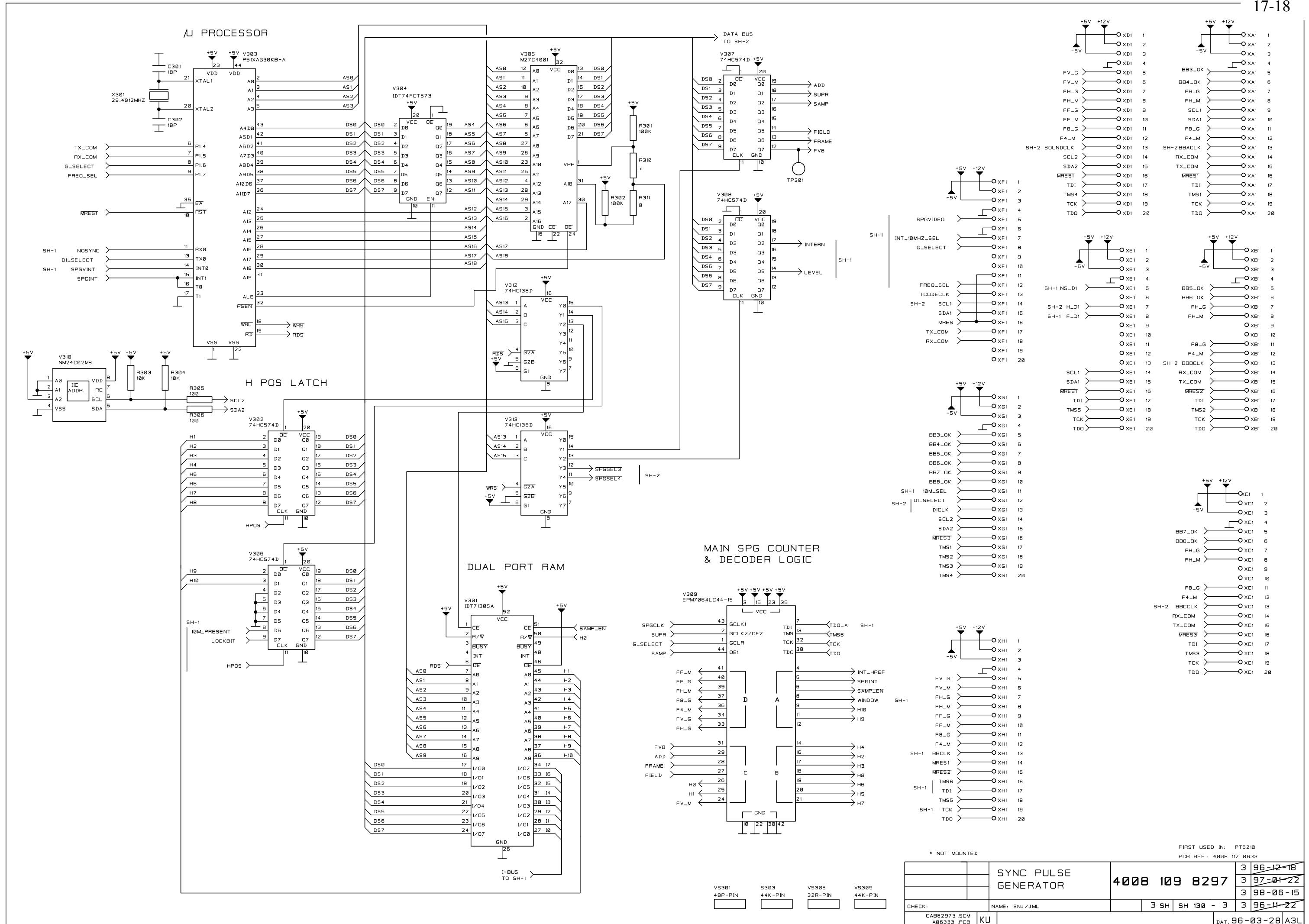
Sync generator

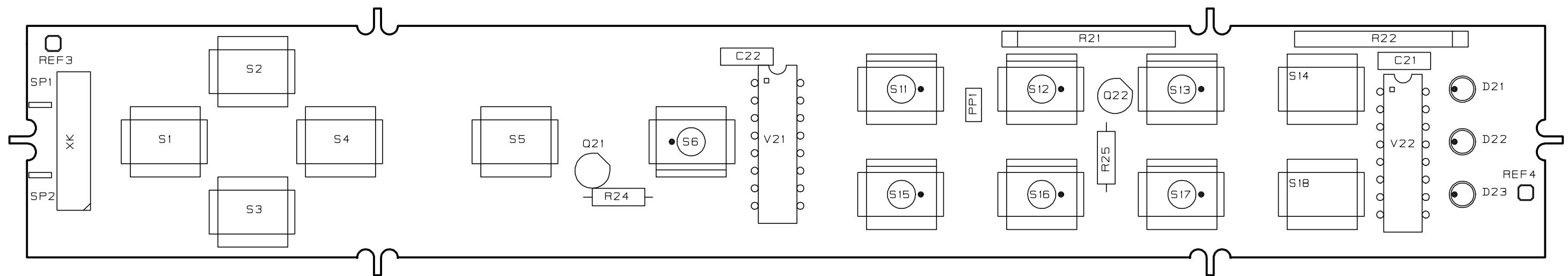
## 10/5 MHz DIVIDER &amp; FILTER

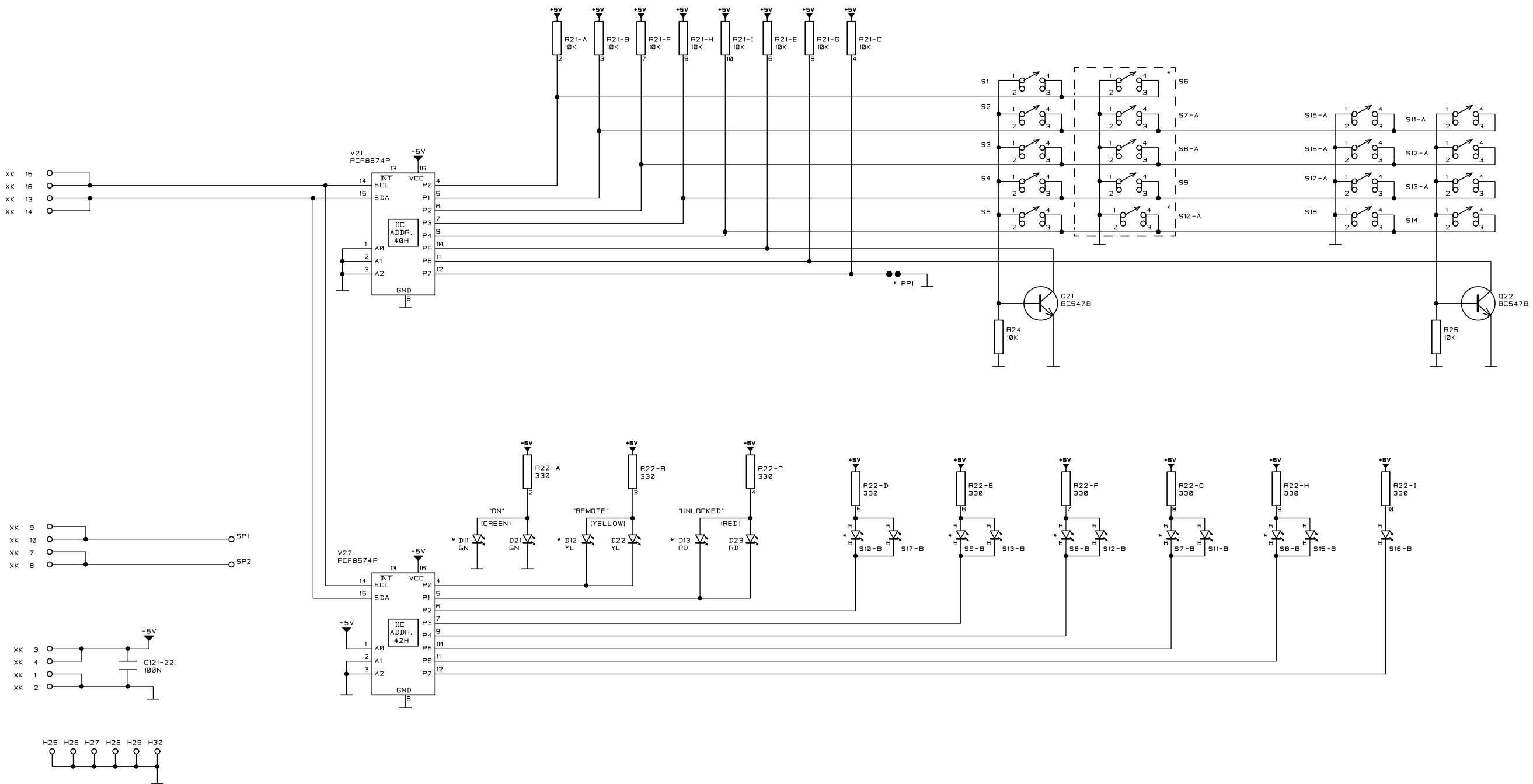
## REFERENCE SELECT



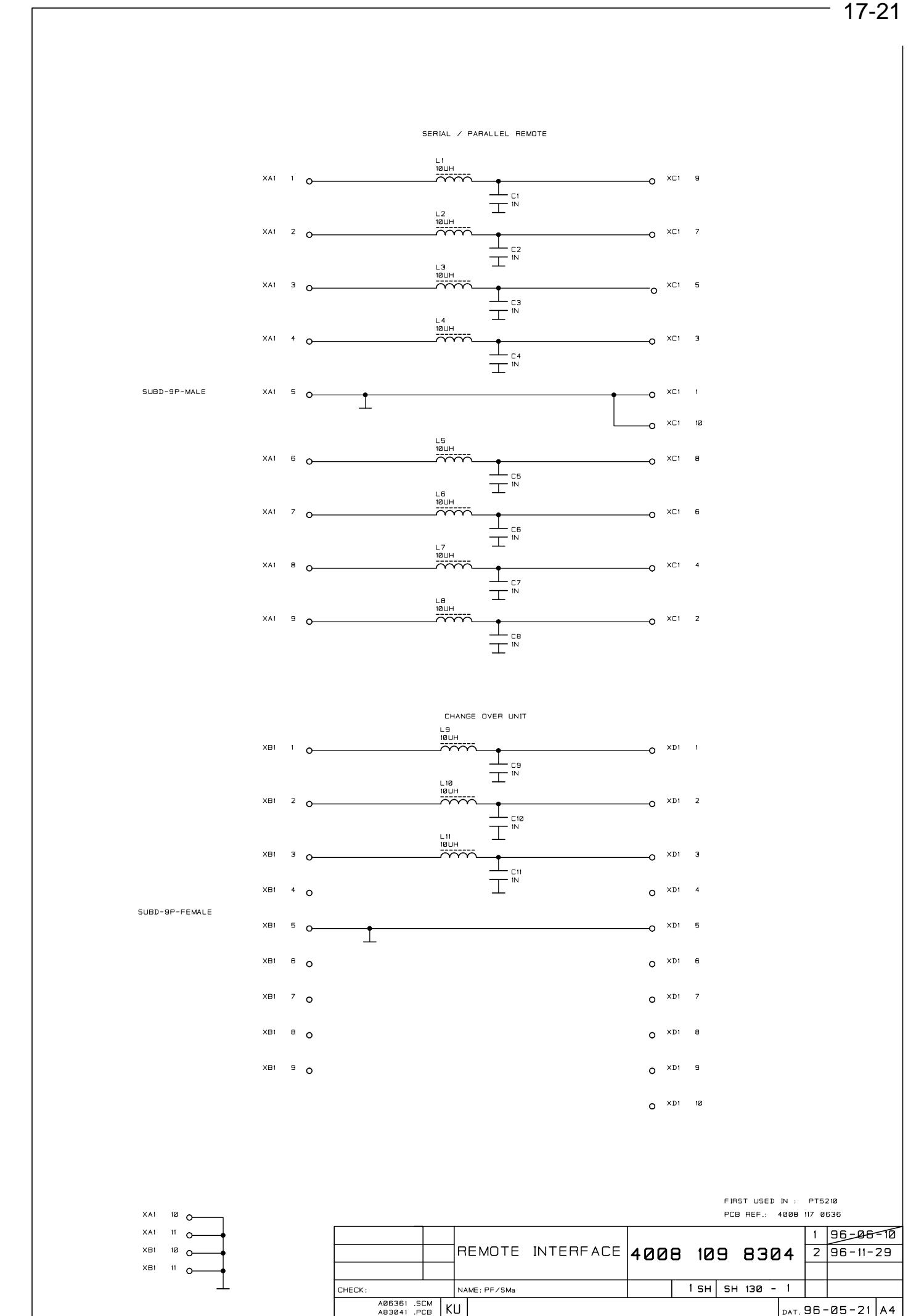
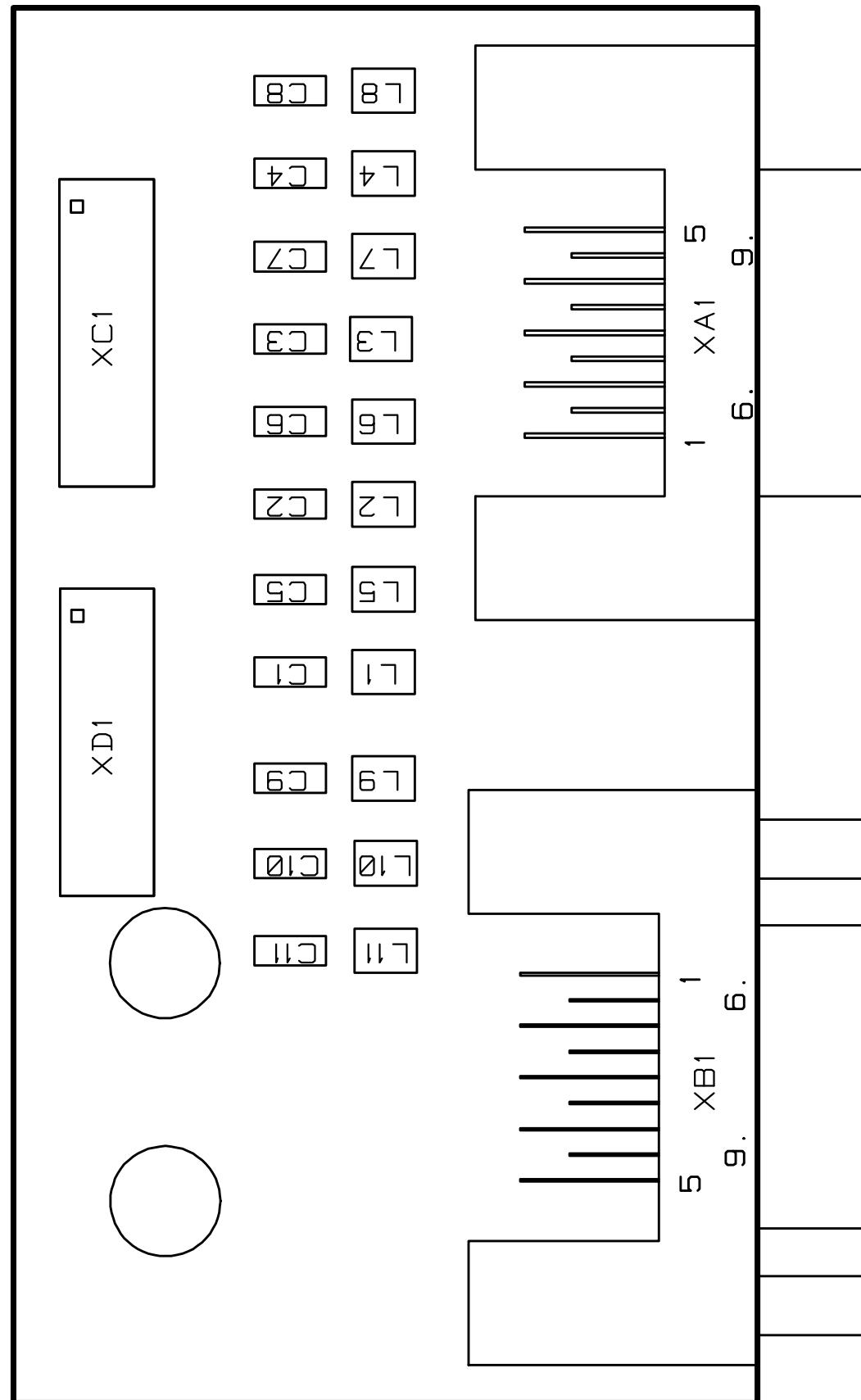


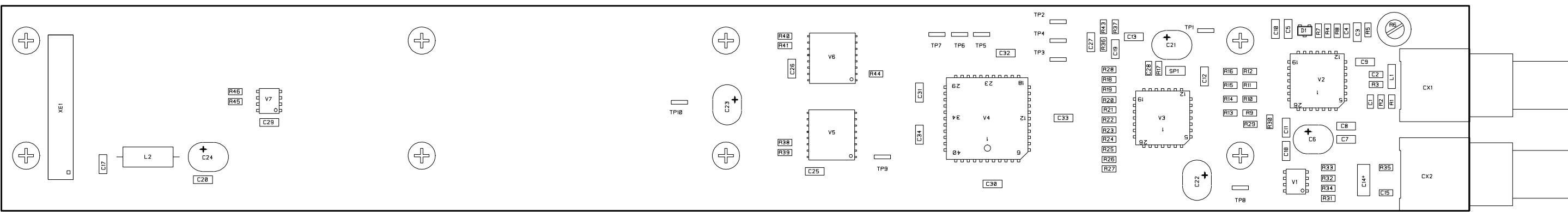


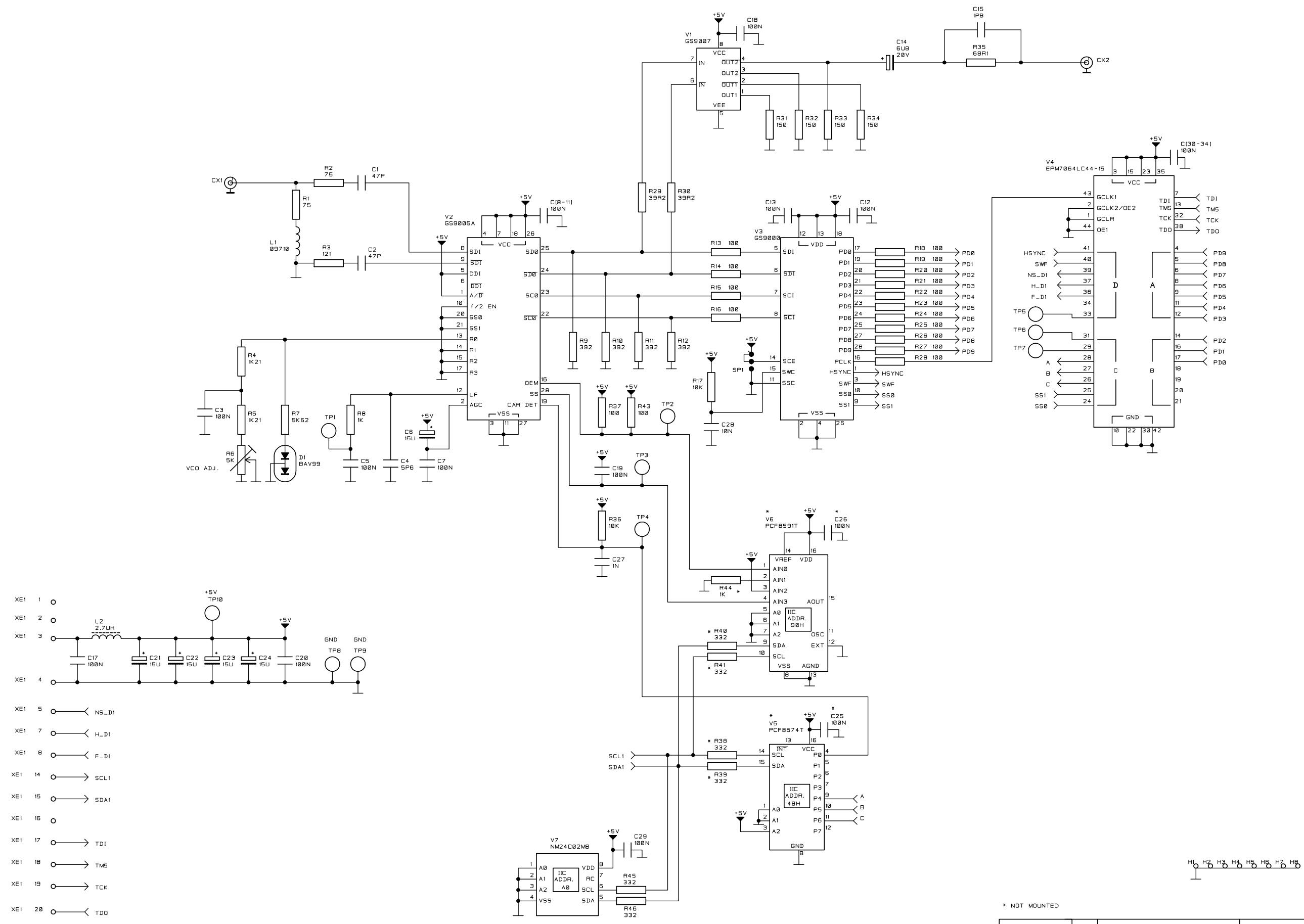




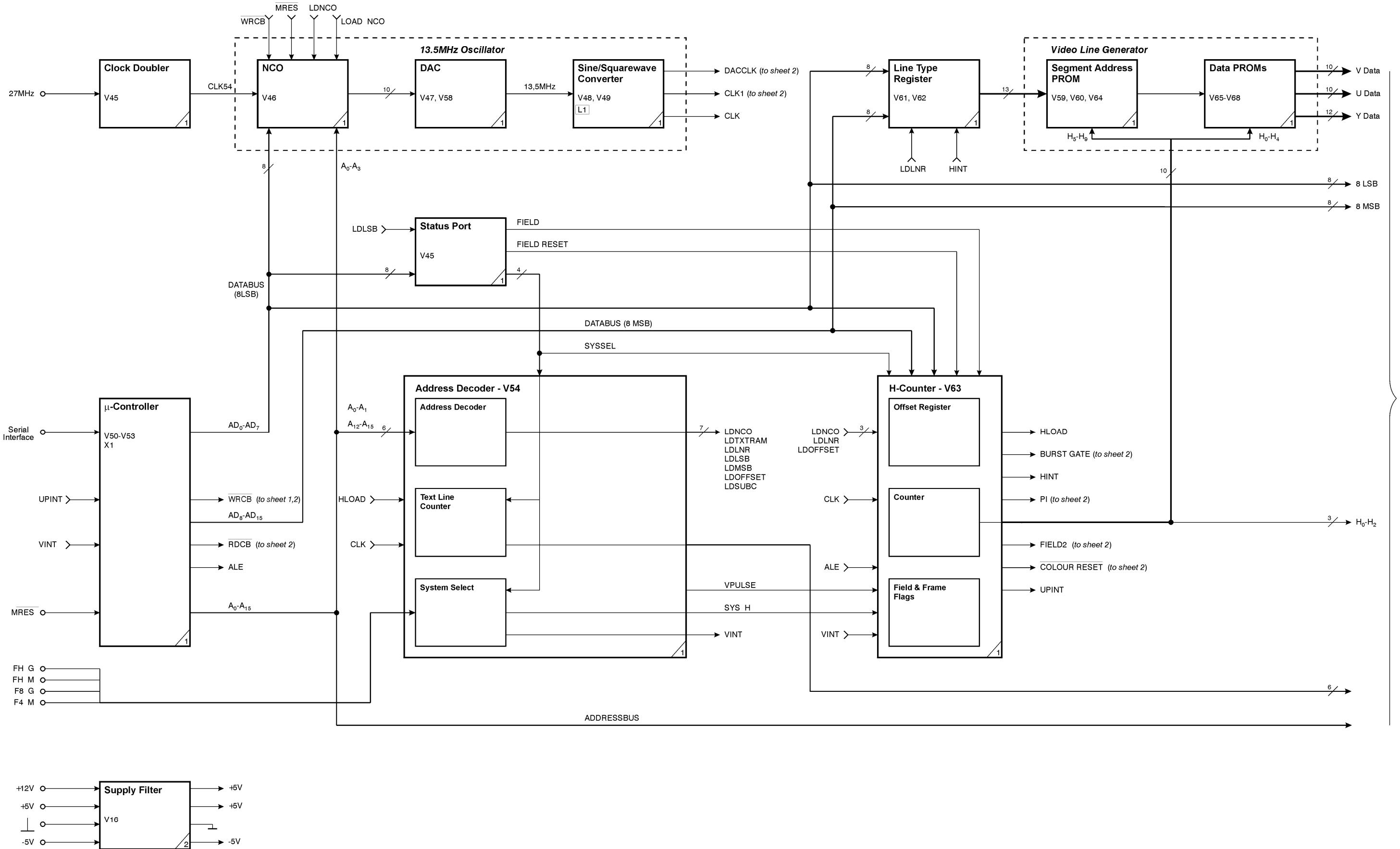
* NOT MOUNTED		1U FRONTPLATE KEYBOARD		4008 109 8470	
		Pt5230 M.B.		M.B.	
CHECK:		NAME: GBG /JPA /JML		1 SH	SH 130 - 1
A84701.SCM A85941.PCB	KU			DAT. 93-10-01	A3L

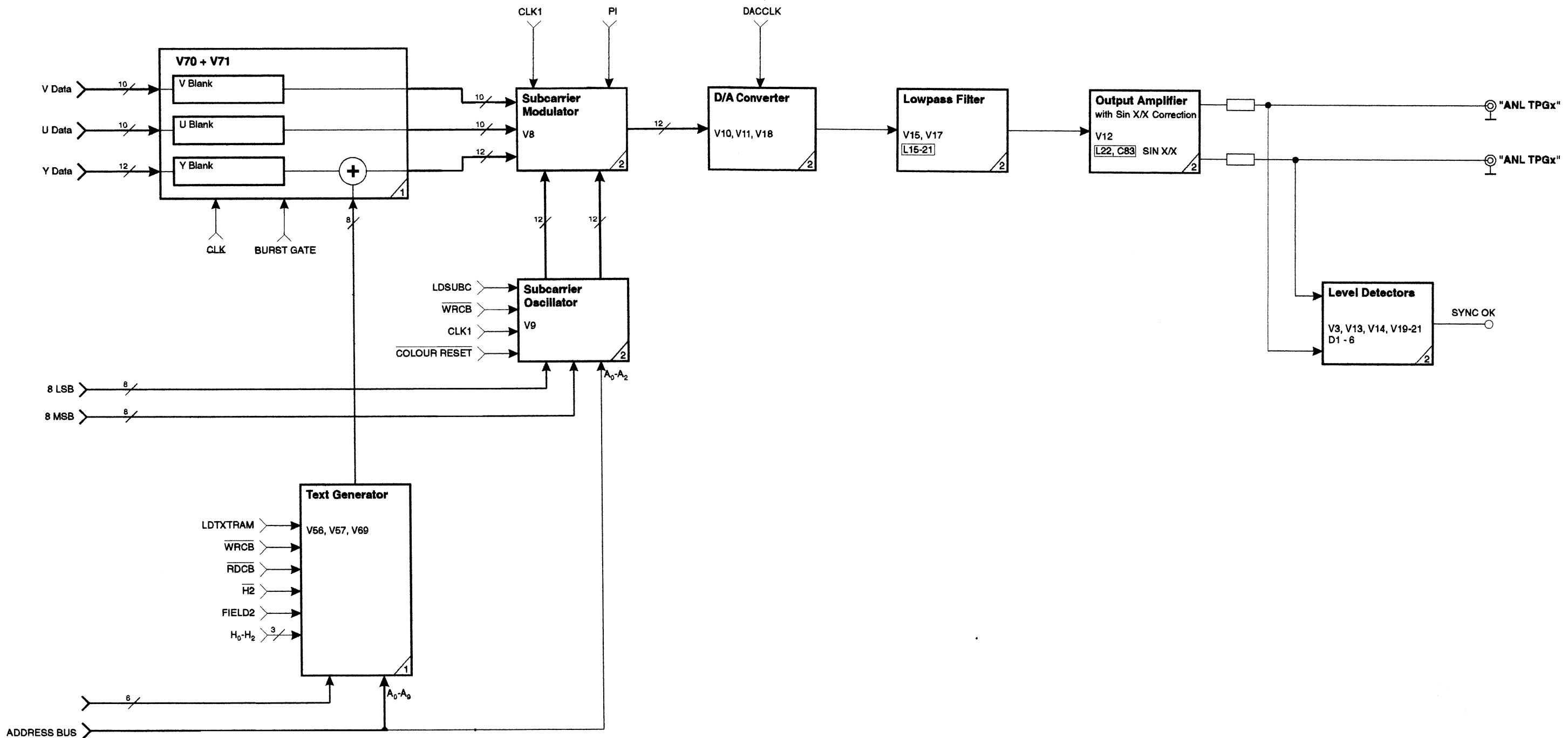


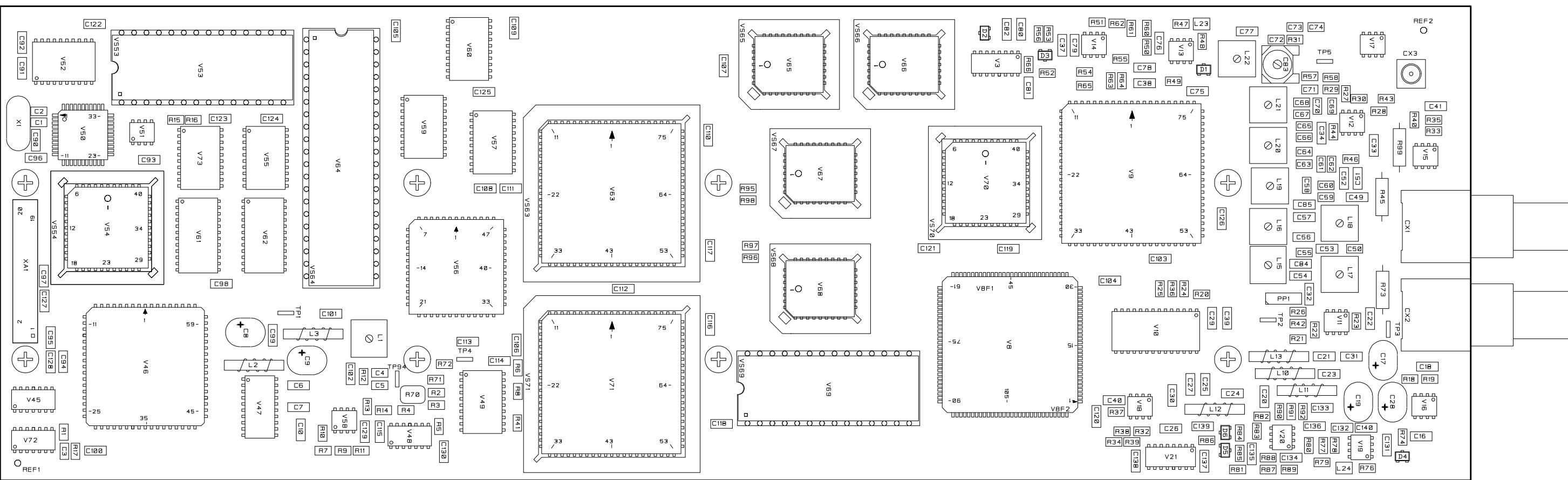


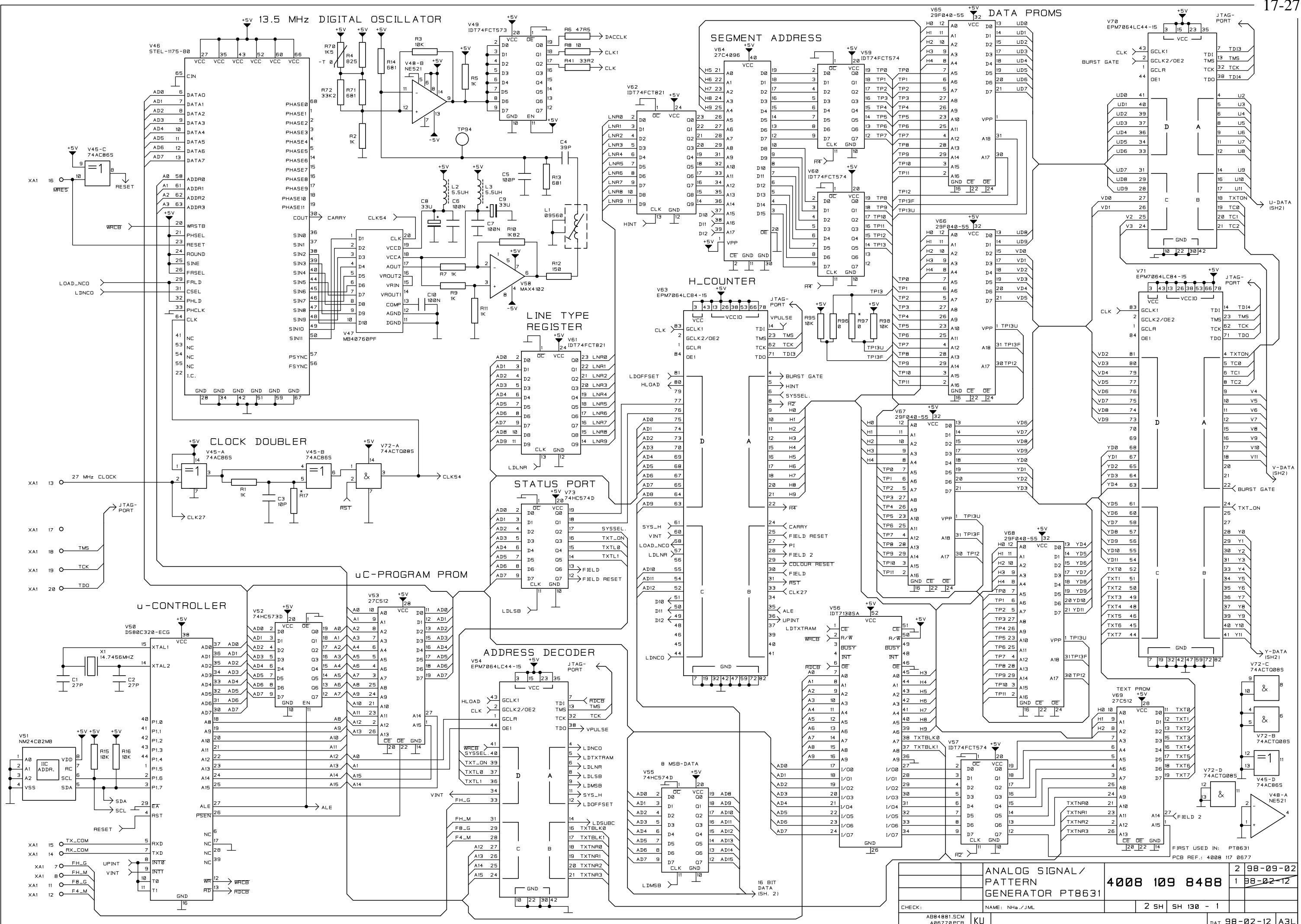


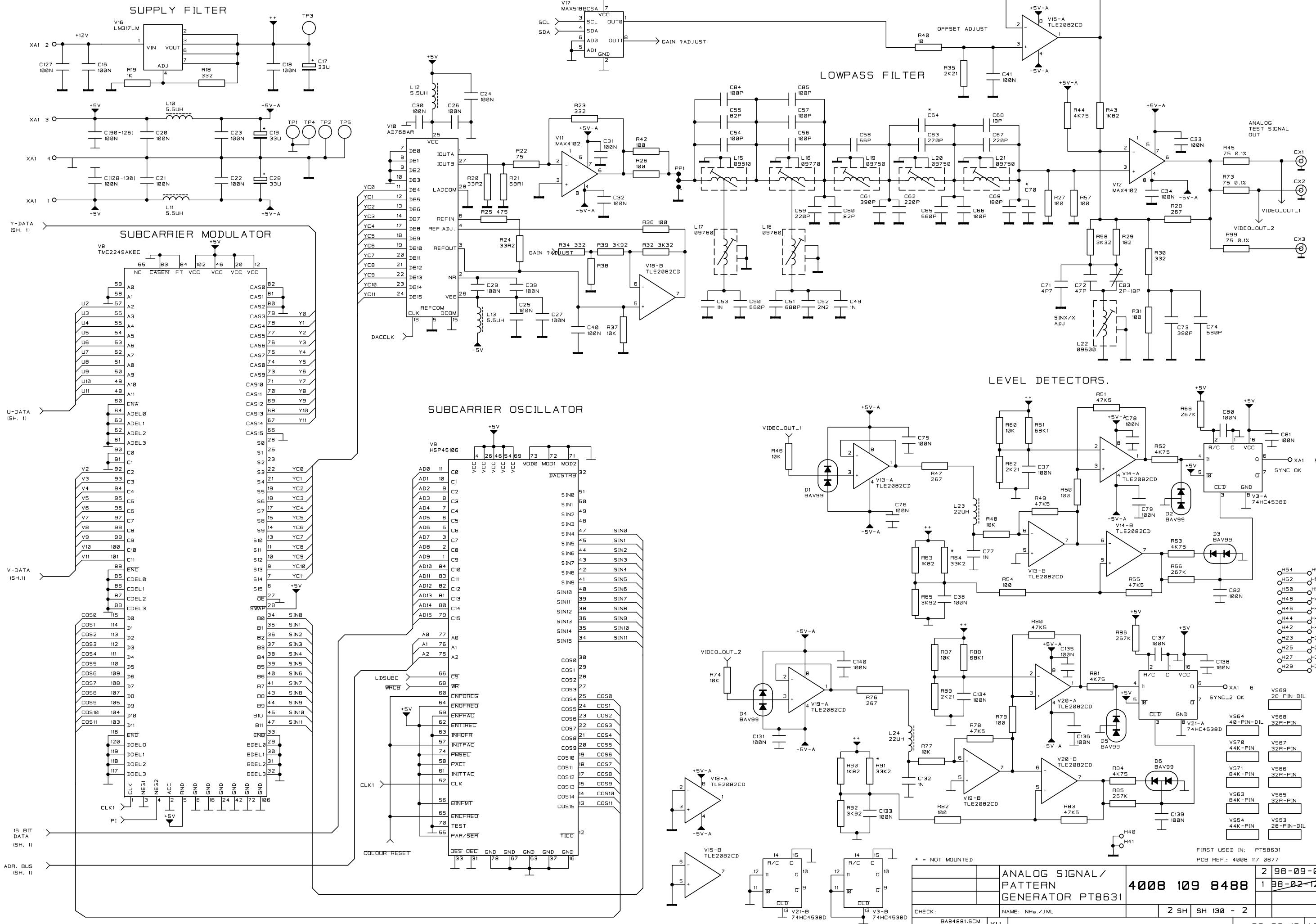
D1 GENLOCK INPUT		4008 109 8292	1 96-06-25
CHECK:	NAME: PF/SMa	1 SH SH 130 - 1	2 97-01-23
A82922 A06282	.SCM .PCB	KU	PHILIPS TV TEST EQUIPMENT DK-2605 BRØNDBY
			DAT. 96-06-12 A3L

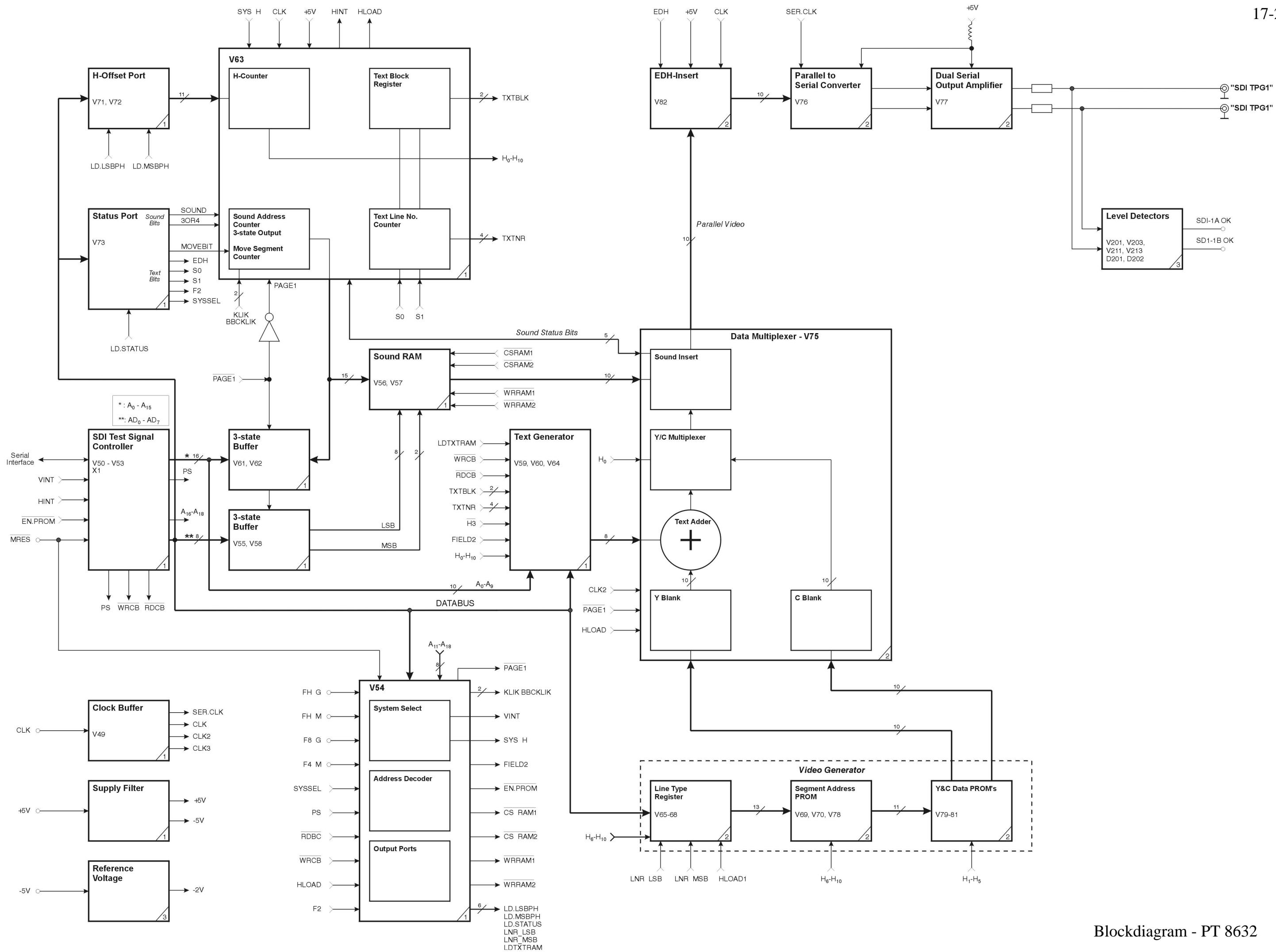




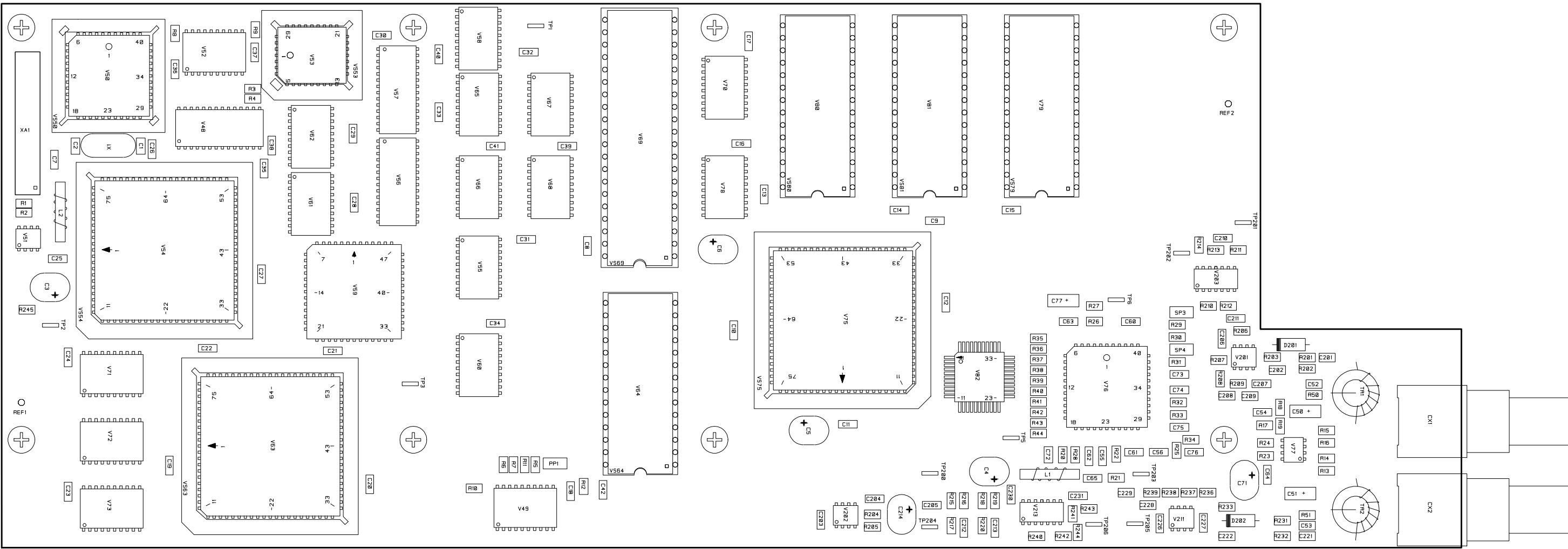


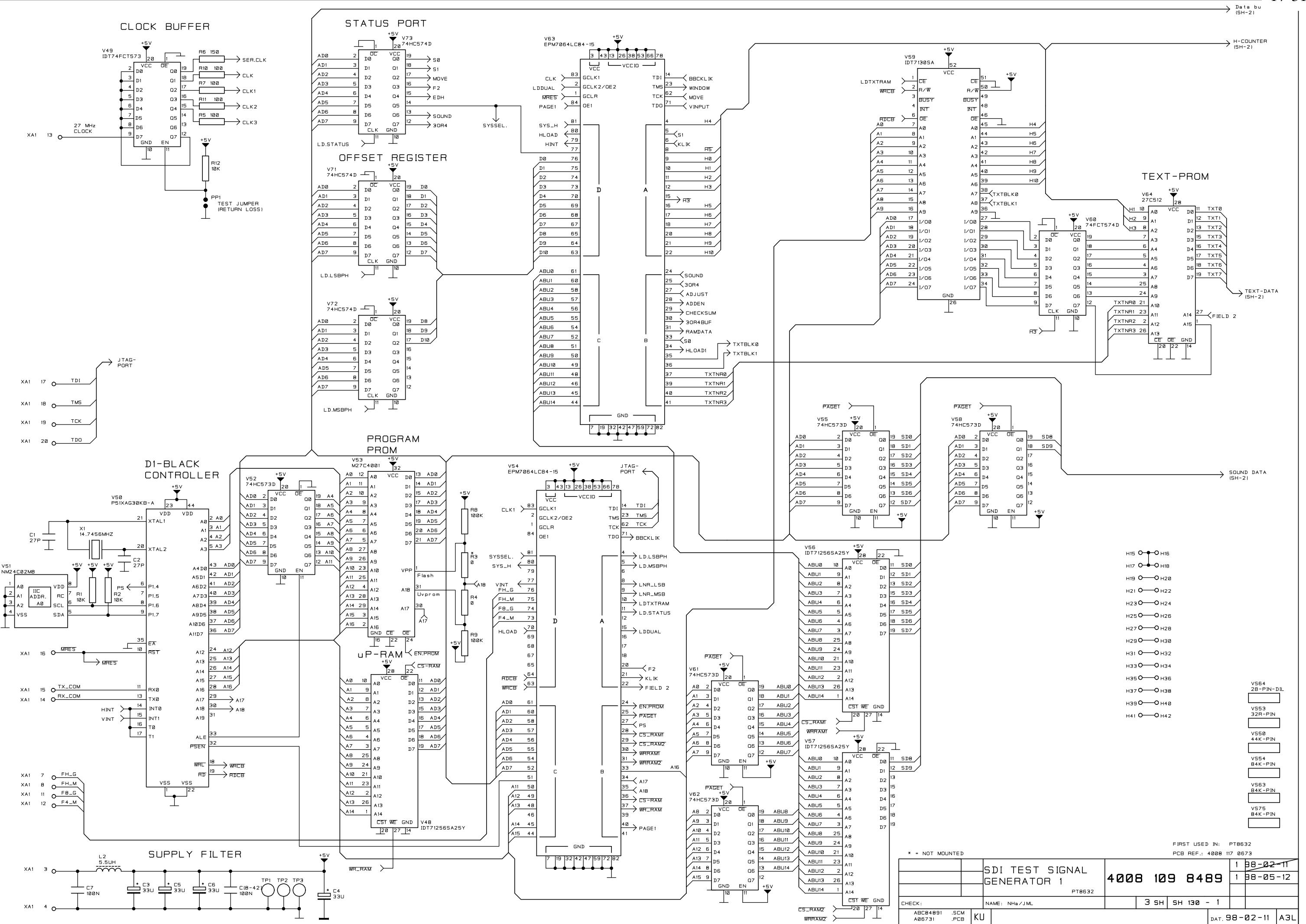


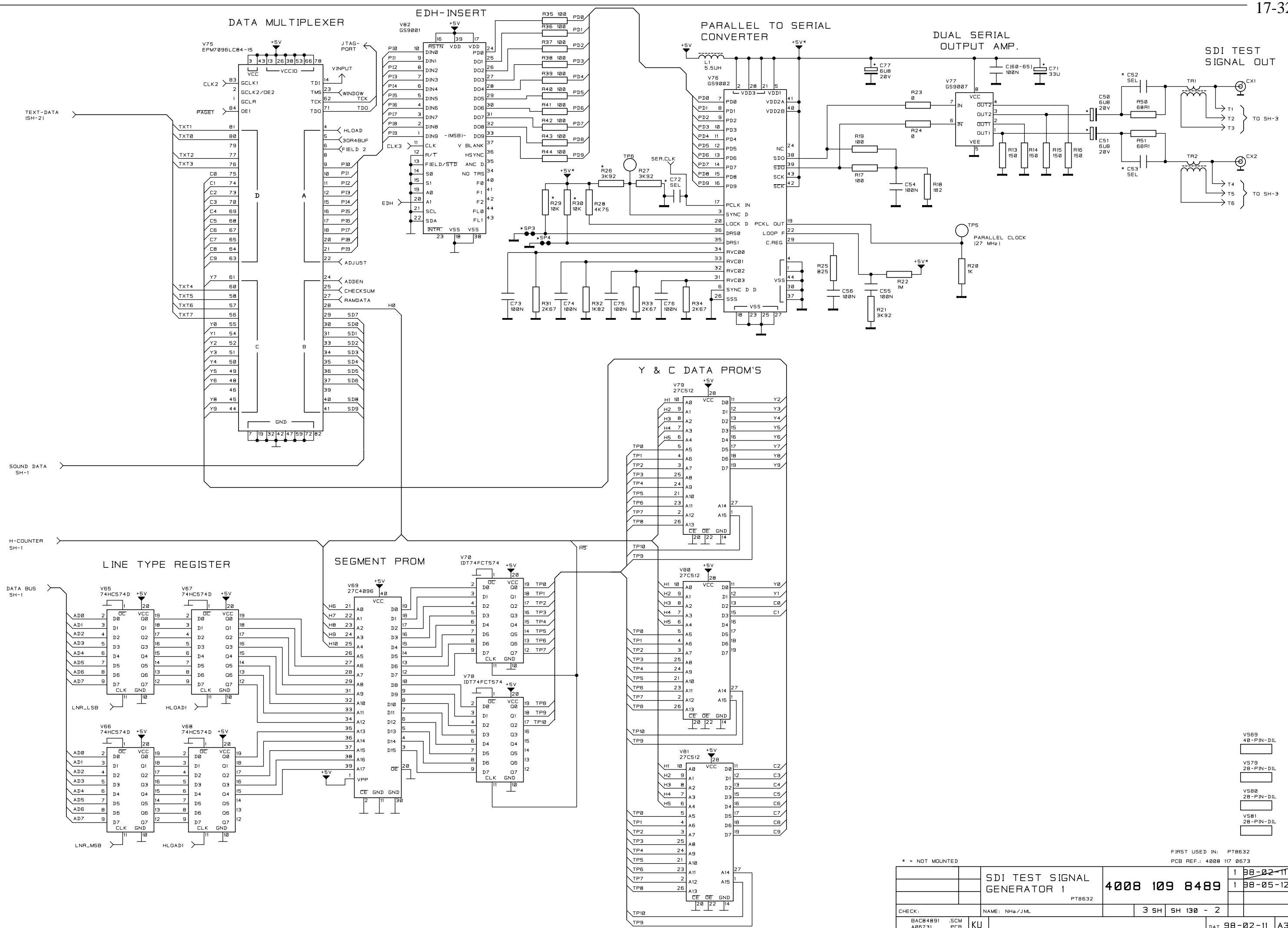


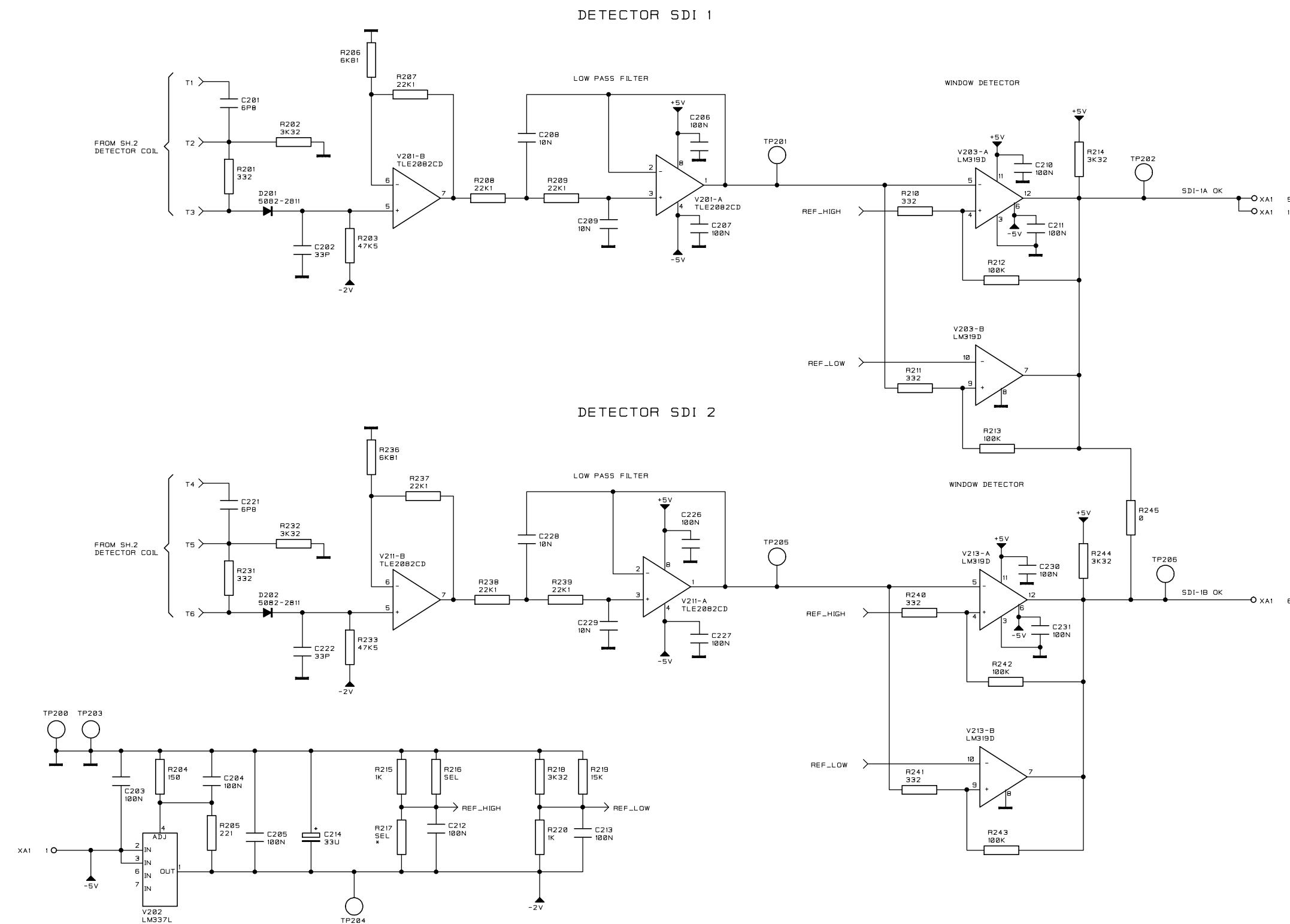


Blockdiagram - PT 8632





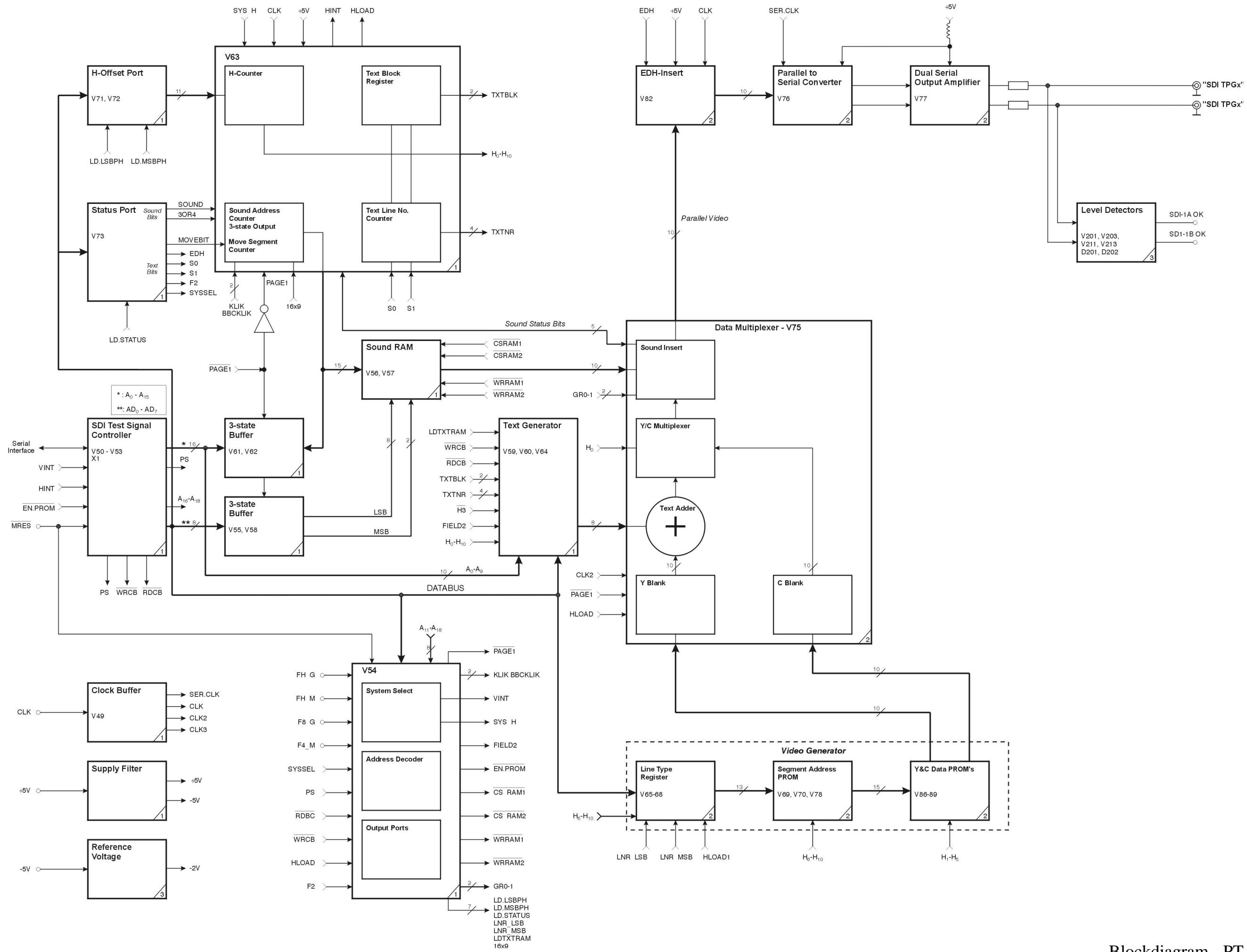




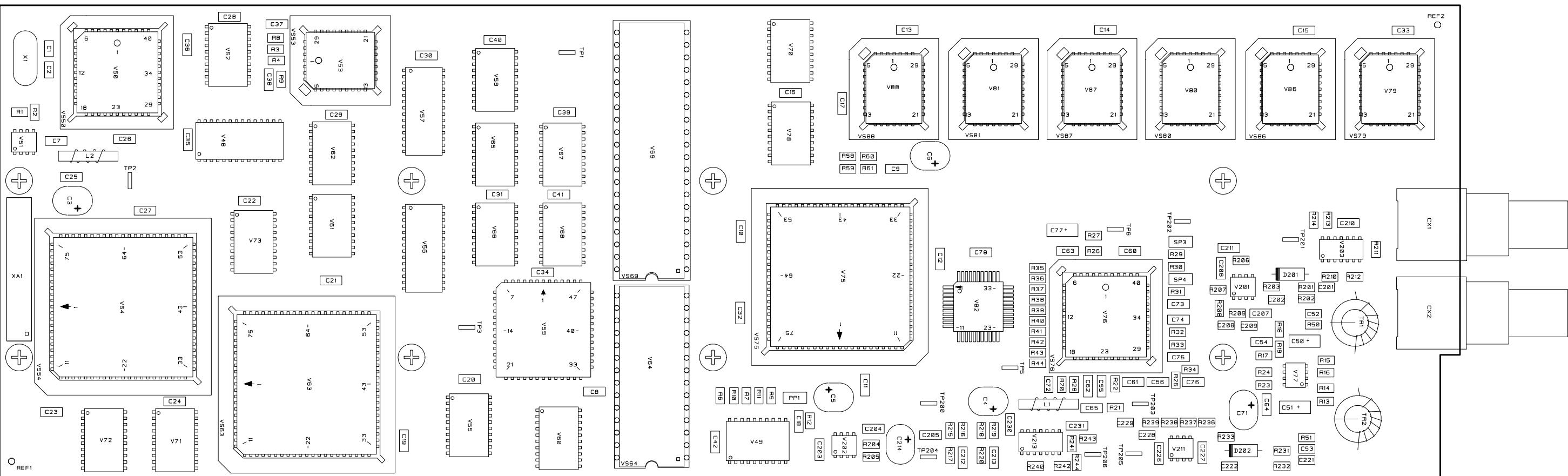
FIRST USED IN: PT8632  
PCB REF.: 4008 117 0673

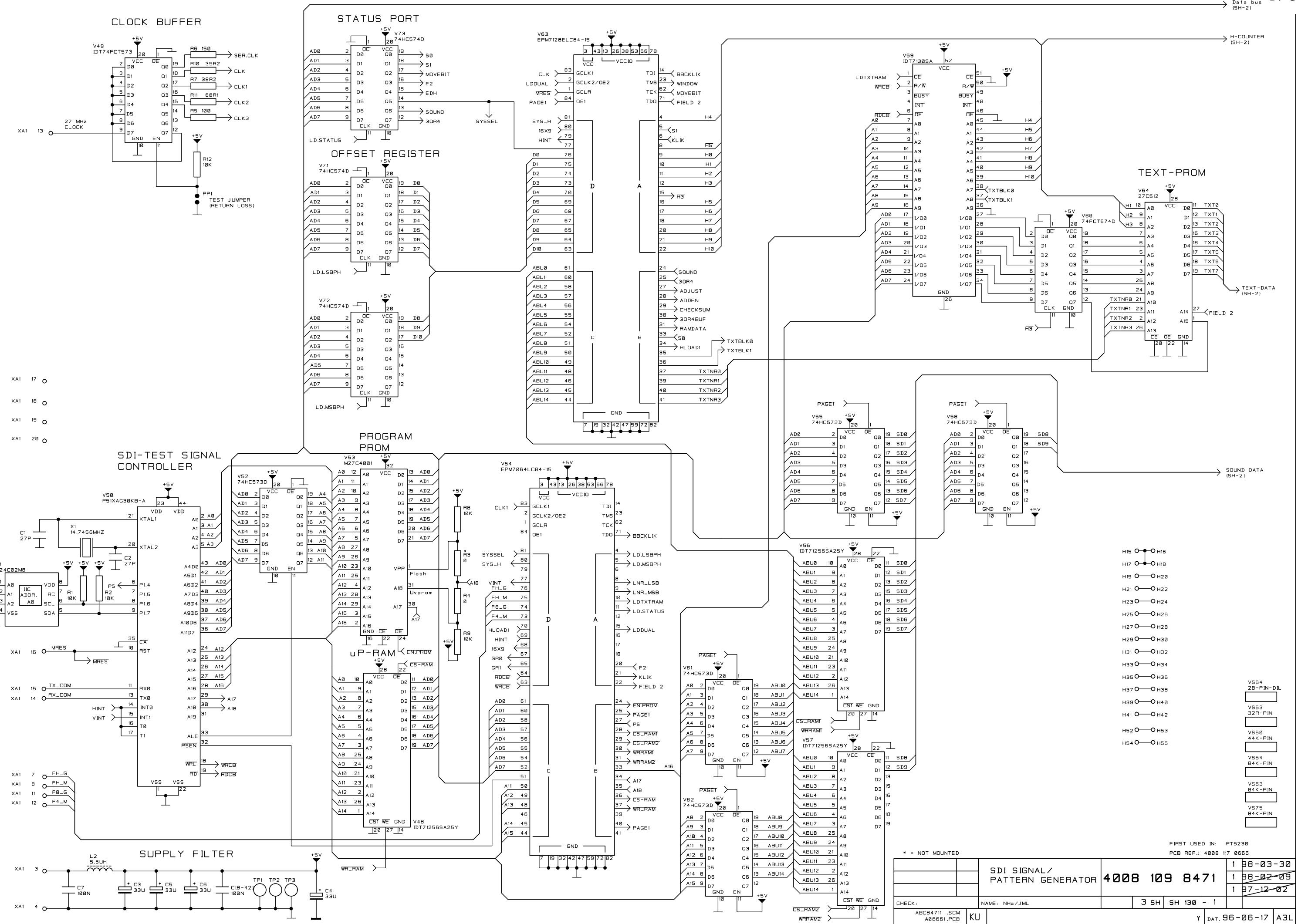
\* NOT MOUNTED

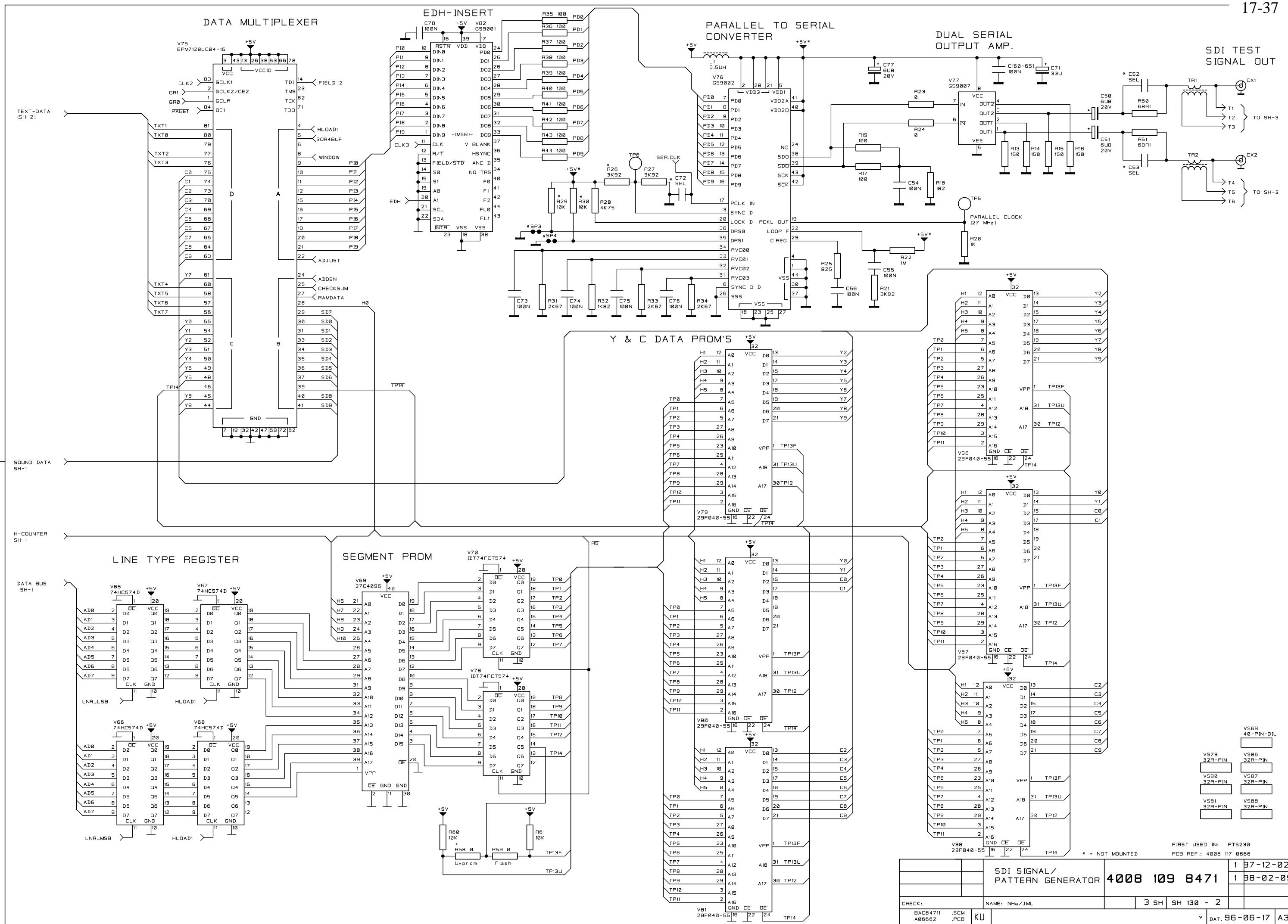
	SDI TEST SIGNAL GENERATOR 1	4008 109 8489	1 98-02-11
		PT8632	1 98-05-12
CHECK:	NAME: PF/JML	3 SH SH 130 - 3	
BAC82953 SCM PCB A06731 KU		DAT. 98-02-11 A3L	

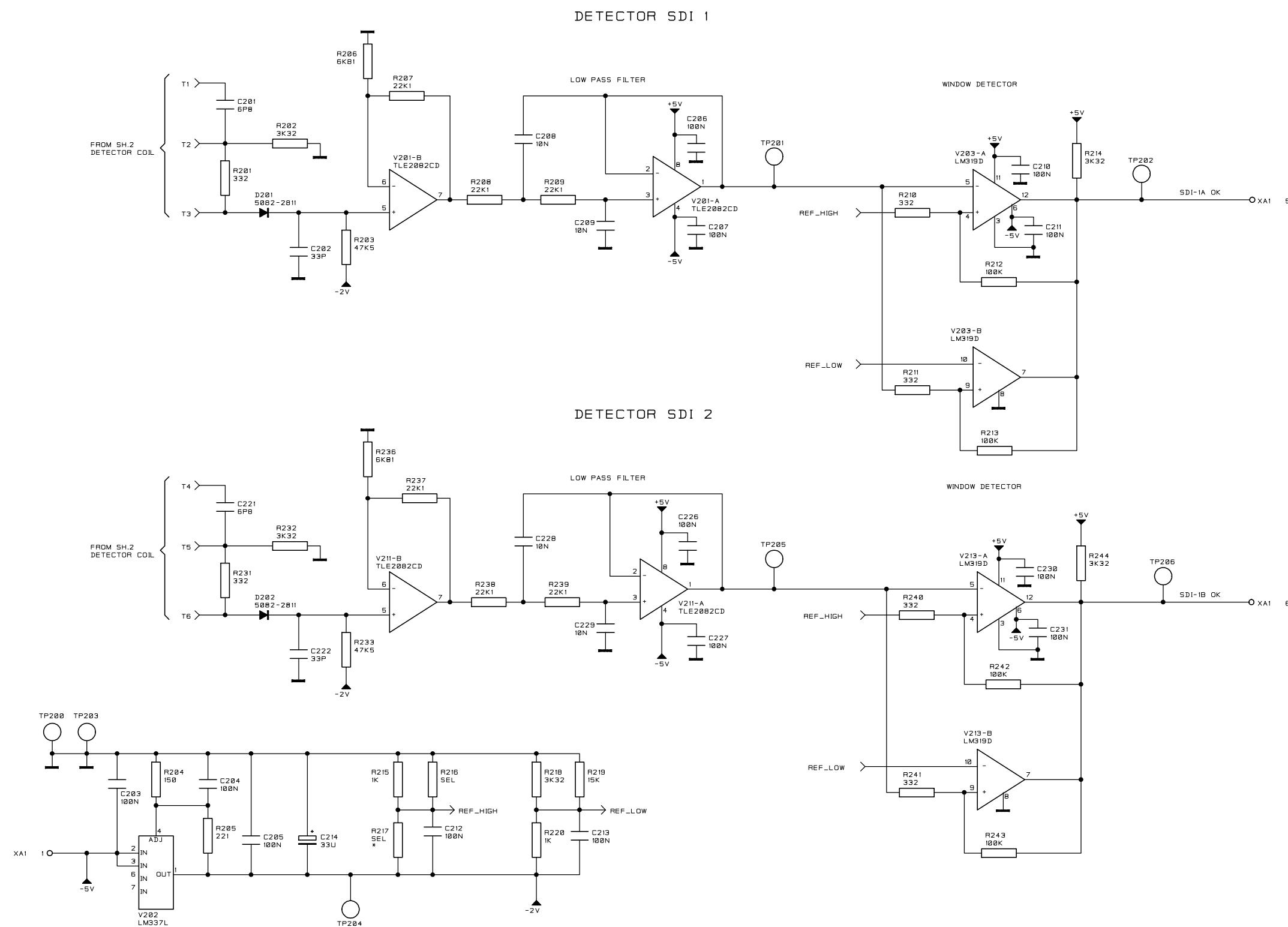


Blockdiagram - PT 8633









* NOT MOUNTED		SDI SIGNAL/ PATTERN GENERATOR	4008 109 8471	1
CHECK:	NAME: PF/JML			3 SH SH 130 - 3
BAC8471I A06661	SCM .PCB	KU		DAT. 96-04-22 A3L