



**KHALEELULLA
KHAN NAZEER**

TOOLS


Cadence Virtuoso , Modelsim
Git, Vim
Eclipse CDT, Qt Designer, PyQt
MATLAB, Octave
LabVIEW, KiCAD
MS Office, Libre Office


COMPETENCIES

Python, Perl, Verilog
C, Embedded C, Assembly
Silicon Validation
PCB Design and Debugging
Analog/Mixed Signal Design (CMOS)
Embedded Hardware Design
Microprocessor Architectures (ARM)
Ubuntu, Windows
LaTeX, XML
Microcontrollers - MSP430, STM32
i2c, SPI, UART
Raspberry Pi

CONTACT

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PROFILE

Gender : **Male**
Age : **27**
Nationality : **Indian**

Seeking opportunities in field of electronics with focus on hands-on product development, test and debug. Visit khaleelkhan.com for more info.



EDUCATION

- Sep 2016-Sep 2018** **Masters Degree**
Information and Communication Engineering
Technische Universität Darmstadt, Germany
Master Thesis : **Design and analysis of a 0.7 to 2.3 GHz adjustable, high spectral purity CMOS oscillator**
- Aug 2009-Sep 2013** **Bachelors Degree**
Electronics and Communication Engineering
Vishweshwaraya Technological University, India



EXPERIENCE

- Jul 2017-Jul 2018** **G.i.N. GmbH, Darmstadt**
Studentische Hilfskraft - Embedded Systems (Hardware)
Key skills acquired : **Python, C, Microcontrollers, Git**
Tools used : **Eclipse, Eagle, Jira, Bitbucket**
- Dec 2016-Feb 2017** **Signal Processing Group (SPG), Darmstadt**
Student Research Assistant
Key skills acquired : **Python, Git**
Tools used : **Pycharm, MATLAB**
- May 2014-Sep 2016** **Texas Instruments, India**
Post Silicon Validation Engineer,
Contractor from Tessolve Semiconductors
Key skills acquired : **Device validation, PCB design, Python, Electronic test and debug,**
Tools used : **Pycharm, LabVIEW**





ACHIEVEMENTS

- Texas instruments Excellence award for "Fast ramp up"
- Texas instruments Excellence award for "Job well done"
- Scored 1.0 in Analog Integrated Circuit Design Lab



LANGUAGES

English 
Hindi 

German 