Quiz 1 and 2

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Points:

11/20

1.Which among the following statements is/are not true regarding 8086 assembler directives?

(1/1 Point)

○ They act during assembly of a program

○ END directive tells the assembler that this is the end of program module

○ PAGE 50, 90 means 90 lines with 50 charecters per line

○ None of the above

2.What must be loaded in PSW to select Bank-3 of 8051?

(0/1 Point)

○ 18F

○ 08F

○ 10F

○ None of the above

3.What will be the state of carry, auxiliary carry and parity flags after executing the following instructions:

MOV A,#9C

ADD A,#64

(1/1 Point)

○ CY=1; AC=0; P=0

○ CY=0; AC=1; P=0

○ CY=1; AC=1; P=1

○ CY=1; AC=1; P=0

4.Assume that the D and E register contain 26H and 00H, respectively. Moreover, the H and L registers contain 26H and 01H. Please bear in mind that the memory locations 2600H and 2601H has 60H and 30H respectively, What will happen at the end of executing LDAX D instruction using 8085?..

(1/1 Point)

○ Accumulator will have 2600H

○ Accumulator will have 30 H

○ Accumulator will have 60 H

○ None of the above

5.Identify the false statement about 8051

(0/1 Point)

○ has 128 bytes of on-chip data memory

○ has two 16-bit timers

○ code bytes can be fetched exclusively from external memory

○ None of the above

6.Assume that the CL register of 8086 contains value 02H and BL register contain AEH, What will be the contents of BL register after executing ROL BL,CL instruction?

(0/1 Point)

○ BAH

○ 5BH

○ 57H

○ None of the above

7.Identify the false statement(s) regarding 8085

(1/1 Point)

○ TRAP interrupt is Maskable but INTR is not

○ Both hardware and software interrupts are supported

○ CMA complements the contents of accumulator

○ STA instruction can be used to store the result in a specified memory location

○ All the above

○ None of the above

8.In 8085, if A and D registers have B8H and B9H respectively, identify the false statement(s) from the following about CMP D instruction:

(0/1 Point)

○ Carry flag will be reset

○ Contents of A and D registers will change

○ Complements the contents of D register

○ All the above

9.If B=00H and C=00H in 8085, what will be the contents of B and C registers after executing DCX B

(1/1 Point)

○ B=FFH; C=00H

○ B=FFH; C=FFH

○ B=00H; C=FFH

○ None of the above

10.Which among the following statements are false regarding 8085?

(1/1 Point)

○ PC stores the address of the next instruction to be fetched

○ SP holds the address of the most recent stack entry

○ There are 8 software interrupts

○ None of the above

11.If accumulator of 8085 has 57H and carry flag is set, what will be the result of executing RLC instruction?

(0/1 Point)

○ accumulator contents become AEH

○ accumulator contents become AFH

○ accumulator contents become ADH

○ None of the above

12.Consider the 8086 instruction Test AL, CL.

Assume that the AL register contains an unknown 8-bit sequence xxxxxxxx. What should be the bit pattern that must be loaded in CL register to test if bit-4 in the contents AL register? ..

(0/1 Point)

○ 08H

○ 10H

○ 11H

○ EFH

13.Identify the false statement(s) about 8086

(1/1 Point)

○ The 20-bit contents of the segment register gives the starting/base address of a particular segment

○ Offset address is needed to identify a specific memory location within a segment

○ Direction flag affects the way strings are processed

○ None of the above

14.Identify the false statement(s) about 8086

(0/1 Point)

○ STC instruction does not affect any flag other than the carry flag

15.Identify the true statement(s) about 8086

(1/1 Point)

○ 8086 has maximum and minimum modes of operation

○ 8086 has 14 16-bit registers

○ Supports word and byte transfer

○ All the above

16.Assume that the D and E register contain 26H and 00H, respectively. Moreover, the H and L registers contain 26H and 01H. Please bear in mind that the memory locations 2600H and 2601H has 60H and 30H respectively, What will happen at the end of executing XCHG instruction using 8085?..

(1/1 Point)

○ (2600)=30H, (2601)=60H

○ D-register will contain 26H; E-register will contain 01H

○ H-register will contain 30H; L-register will contain 60H

○ (2601)=30H, (2600)=60H

17.Identify the false statement regarding CMP instruction of 8086

(0/1 Point)

○ Can compare contents of source and destination registers

○ Can compare immediate number with contents of a register

○ Can compare contents of two memory locations

○ Affects flags but source and destination registers/memory locations remain unchanged

18.Identify the false statement(s) about 8051

(0/1 Point)

○ if A=78H, executing SWAP A instruction results in 87H in the accumulator

○ While executing MOV DPTR,#1234H instruction, DPH will hold 34H and DPL will hold 12H

○ CLR A clears all the bits of accumulator

○ None of the above

19.if A=50 H and B=A0H, what will be the contents of A and B registers of 8051 at the end of MUL AB instruction?

(1/1 Point)

○ A=00H, B=32H

○ A=32H, B=00H

○ A=00H, B=22H

○ A=22H, B=00H

20.If the memory locations 2600H and 2601H has 60H and 30H respectively, What will happen at the end of executing LHLD 2600 instruction using 8085?
(1/1 Point)

○ (2600)=30H, (2601)=60H

○ (2600)=61H, (2601)=31H

○ L=60H; H=30H

○ L=30H; H=60H