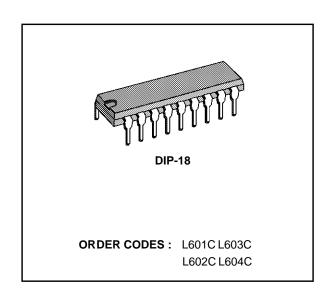
# **DARLINGTON ARRAYS**

- EIGHT DARLINGTONS PER PACKAGE
- OUTPUT CURRENT 400 mA PER DRIVER (500 mA PEAK)
- OUTPUT VOLTAGE 90 V (V<sub>CE (sus))</sub> = 70 V)
- INTEGRAL SUPPRESSION DIODES FOR INDUCTIVE LOADS
- OUTPUTS CAN BE PARALLELED FOR HIGHER CURRENT
- TTL / CMOS / PMOS / DTL COMPATIBLE INPUTS
- INPUTS PINNED OPPOSITE OUTPUTS TO SIMPLIFY LAYOUT



The four versions interface to all common logic families:

L601	General purpose
L602	14 - 25 V PMOS
L603	5 V TTL
L604	6 - 15 V CMOS

These versatile devices are useful for driving a wide range of loads, including solenoids, relays DC motors, LED displays, filament lamps, thermal printheads and high power buffers.

#### **DESCRIPTION**

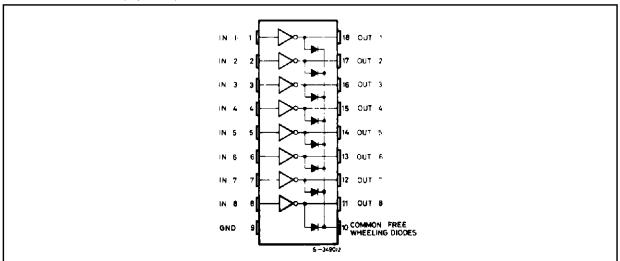
The L601, L602, L603 and L604 are high voltage, high current darlington arrays each containing eight open collector darlington pairs with common emitters. Each channel is rated at 400 mA and can with stand peak currents of 500 mA. Suppression diodes are included for inductive load driving and the inputs are pinned opposite the outputs to simplify board layout.

#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
V <sub>CEX</sub>	Collector Emitter Voltage (input open)	90	V
lc	Collector Current	0.4	Α
I <sub>C</sub>	Collector Peak Current	0.5	Α
$V_{i}$	Input Voltage (for L602 L603 and L604)	30	
l <sub>i</sub>	Input Current (for L601 only)	25	mA
P <sub>tot</sub>	Total Power Dissipation a T <sub>amb</sub> = 25°C	1.8	W
T <sub>op</sub>	Operating Junction Temperature	-25 to 150	°C

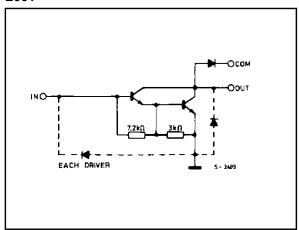
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## PIN CONNECTION (top view)

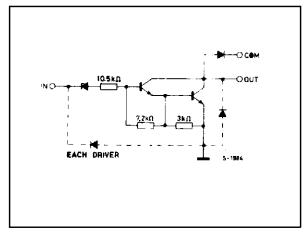


### **SCHEMATIC DIAGRAMS**

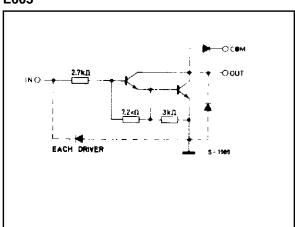
#### L601



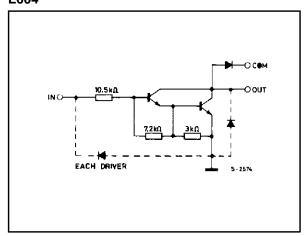
#### L602



### L603



## L604



## THERMAL DATA

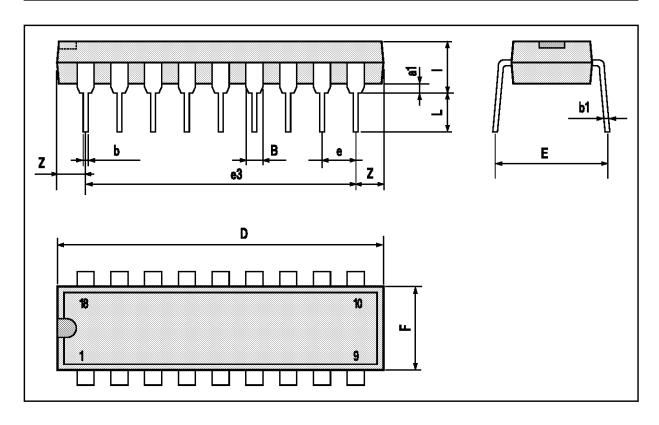
Symbol	Parameter	Value	Unit
R <sub>th-j-amb</sub>	Thermal resistance Junction-ambient Max	70	°C/W

# **ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25 \, ^{\circ}C$ , unless otherwise specified)

Symbol	Parameter	Test con	ditions	Min.	Тур.	Max.	Unit
I <sub>CEX</sub>	Output Leakage Current	V <sub>CE</sub> = 90 V				10	μΑ
VCE (sat)	Collector Emitter Saturation Voltage	I <sub>C</sub> = 300 mA I <sub>C</sub> = 200 mA I <sub>C</sub> = 100 mA	$I_B = 500 \mu\text{A}$ $I_B = \mu\text{A}$ $I_B = 250 \mu\text{A}$			2 1.7 1.2	>>>
h <sub>FE</sub>	DC Forward Current Gain (L601 only)	V <sub>CE</sub> = 3 V	I <sub>C</sub> = 300 mA	1000			-
Vi	Minimum Input Voltage (ON condition)	V <sub>CE</sub> = 3V for L602 for L603 for L604	I <sub>C</sub> = 300 mA			11.5 2.5 5	V V V
Vi	Maximum Input Voltage (OFF condition)	V <sub>CE</sub> = 90 V for L601 for L602 for L603 for L604	Ι <sub>C</sub> = 25 μΑ	0.55 7 0.75 1			V V V
IR	Clamp Diode Reverse Current	V <sub>R</sub> = 90 V				50	μΑ
V <sub>F</sub>	Clamp Diode Forward Voltage	I <sub>F</sub> = 300 mA			2	2.4	V
t <sub>on</sub>	Turn-on Delay	0.5 V <sub>i</sub> to 0.5 V <sub>o</sub>			0.4		μs
t <sub>off</sub>	Turn-off Delay	0.5 V <sub>i</sub> to 0.5 V <sub>o</sub>			0.4		μs

## **DIP18 PACKAGE MECHANICAL DATA**

DIM.	mm			inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
a1	0.254			0.010			
В	1.39		1.65	0.055		0.065	
b		0.46			0.018		
b1		0.25			0.010		
D			23.24			0.915	
E		8.5			0.335		
е		2.54			0.100		
e3		20.32			0.800		
F			7.1			0.280	
I			3.93			0.155	
L		3.3			0.130		
Z		1.27	1.59		0.050	0.063	



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