

Efficient Analysis of Parametric Hybrid Systems using HYMITATOR

Étienne André¹, Laurent Fribourg², Ulrich Kühne³ and Romain Soulat²

¹LIPN, CNRS UMR 7030, Université Paris 13, France

²ENS Cachan, CNRS, LSV, UMR8643

³Universität Bremen, Germany

Abstract. \Leftarrow to do \Rightarrow

Keywords: Real-Time Systems, Hybrid Automata, Verification, Parameter Synthesis

\Leftarrow Version avec commentaires \Rightarrow

1 Motivation

\Leftarrow short introduction \Rightarrow

In [2], we proposed the inverse method for Timed Automata, a subclass of hybrid systems whose variables (named clocks) all have constant rates equal to 1. Different from CEGAR-based methods, this original semi-algorithm for parameter synthesis is based on a “good” parameter valuation π_0 instead of a set of “bad” states. *IM* synthesizes a constraint K_0 on the parameters such that, for all parameter valuation π satisfying K_0 , the trace set, i.e., the discrete behavior, of \mathcal{A} under π is the same as for \mathcal{A} under π_0 . This preserves in particular linear time properties, and provides the system with a criterion of robustness. By iterating the inverse method on all integer points within a bounded reference parameter domain, we get a set of constraints (“tiles”) such that, for every point in each such constraint, the time-abstract behavior is the same: this gives a behavioral cartography of the system [3].

A basic implementation named IMITATOR (for *Inverse Method for Inferring Time AbstracT behaviOR*) has first been proposed, under the form of a Python script calling HYTECH [7]. The tool has then been entirely rewritten in IMITATOR II [1], under the form of a standalone OCaml program making use of the Parma Polyhedra Library (PPL) [5]. A number of case studies containing up to 60 timing parameters could be efficiently verified in the purely timed framework.

The inverse method and the behavioral cartography have then been extended to hybrid systems in [6], and implemented in a prototype “fork” of IMITATOR II. \Leftarrow small description to add \Rightarrow We present in this paper HYMITATOR, an extension of that prototype, performing parameter synthesis on hybrid systems.

\Leftarrow Laurent a dit : \Rightarrow - le principe de la methode inverse pour les TA a été implémenté de façon basique (IMITATOR 1 [ICTAC09]) puis de façon sophistiquée (IMITATOR 2 [Infinity10])

- la méthode a été étendue pour les HA et implémentée de façon basique (Imitator 3 [RP11])
- il s'agit "ici" de décrire une implémentation sophistiquée (intégrant notamment des extensions du merging des TA à la [NFM 12] aux HA)

2 Architecture and Features

HYMITATOR takes as input a network of hybrid automata synchronized on shared actions. The input syntax, inspired by the input syntax of HYTECH, allows the use of analog variables (i.e., any variable such as temperature, time, etc., evolving in a non-linear manner), rational-valued discrete variables, and parameters (i.e., unknown constants).

The core of the program is written in the object-oriented language OCaml, and interacts with PPL. Exact arithmetics is used. A constraint is output in text format; furthermore, the set of traces computed by the analysis can be output under a graphical form (using Graphviz).

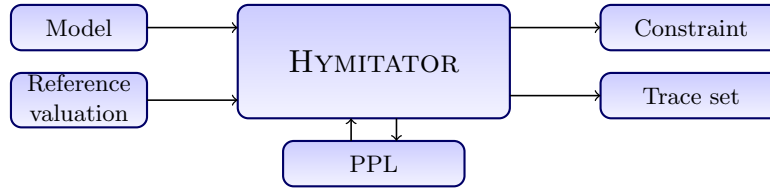


Fig. 1. Architecture of HYMITATOR

HYMITATOR implements the following algorithms for hybrid systems:

Full reachability analysis Given a model, it computes the set of symbolic reachable states.

Inverse method Given a model and a reference parameter valuation π_0 , it computes a constraint on the parameter guaranteeing the same time-abstract behavior as under π_0

Behavioral cartography Given a model and a bounded parameter domain for each parameter valuation, it computes a set of constraints and their corresponding trace sets.

HYMITATOR uses several algorithmic optimizations initially developed for IMITATOR. In particular, the efficient merging presented in [4] has been successfully extended to the hybrid case: we merge any two states sharing the same discrete part (location and value of the discrete variables) and such that the union of their constraint on the analog variables and parameters is convex. This optimization preserves the correctness of all our algorithms; better, the constraint output by the inverse method in that case may be weaker, i.e., covers a larger set of parameter valuations.

⇐ un mot sur la cartographie puisque, je crois, elle a été modifiée dans le cas des systèmes hybrides ⇒
⇐ un mot sur l’algo CEGAR-like de Ulrich ⇒

3 Applications

⇐ **Laurent a dit :** ⇒
- sampled data hybrid systems (cf [6])
- synthesis schedulability regions (cf [Cimatti-Palopoli-Ramadian08], [Astrium EADS])
⇐ **experiences ?** ⇒
⇐ **rapport d’études de cas à rédiger** ⇒

4 Related Work

⇐ **to do (citer HyTech, Phaver)** ⇒
A graphical user interface for the input model is currently under construction, based on a generic platform. ⇐ **utile ?!** ⇒

References

1. É. André. IMITATOR II: A tool for solving the good parameters problem in timed automata. In *INFINITY’10*, volume 39 of *EPTCS*, pages 91–99, 2010.
2. É. André, T. Chatain, E. Encrenaz, and L. Fribourg. An inverse method for parametric timed automata. *International Journal of Foundations of Computer Science*, 20(5):819–836, 2009.
3. É. André and L. Fribourg. Behavioral cartography of timed automata. In *RP’10*, volume 6227 of *LNCS*, pages 76–90. Springer, 2010.
4. É. André, L. Fribourg, and R. Soulat. Enhancing the inverse method with state merging. In A. Goodloe and S. Person, editors, *NFM’12*, volume 7226 of *LNCS*, pages 100–105. Springer, 2012. To appear.
5. R. Bagnara, P. M. Hill, and E. Zaffanella. The Parma Polyhedra Library: Toward a complete set of numerical abstractions for the analysis and verification of hardware and software systems. *Science of Computer Programming*, 72(1–2):3–21, 2008.
6. L. Fribourg and U. Kühne. Parametric verification and test coverage for hybrid automata using the inverse method. In G. Delzanno and I. Potapov, editors, *RP’11*, volume 6945 of *Lecture Notes in Computer Science*, pages 191–204. Springer, 2011.
7. T. A. Henzinger, P. H. Ho, and H. Wong-Toi. Hytech: A model checker for hybrid systems. *Software Tools for Technology Transfer*, 1:460–463, 1997.

Appendix

Example of Trace Set

⇐ ajouter un trace set en mode fancy ⇒

An Example of Model

⇐ ajouter un modele en entree avec sa representation graphique (pour
decrire la syntaxe) ⇒