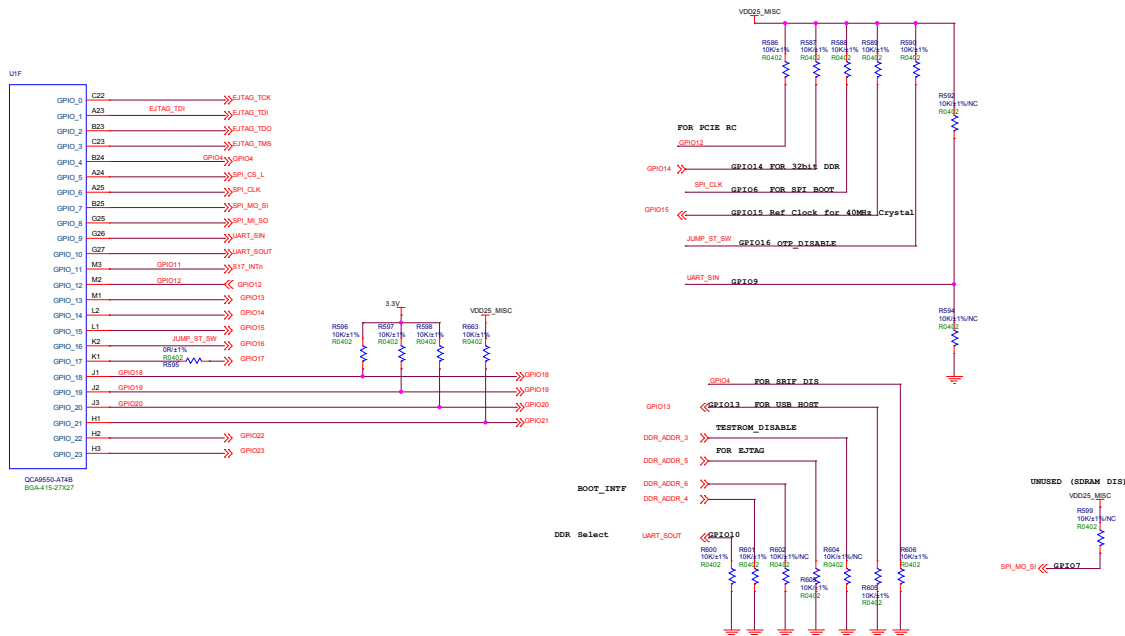
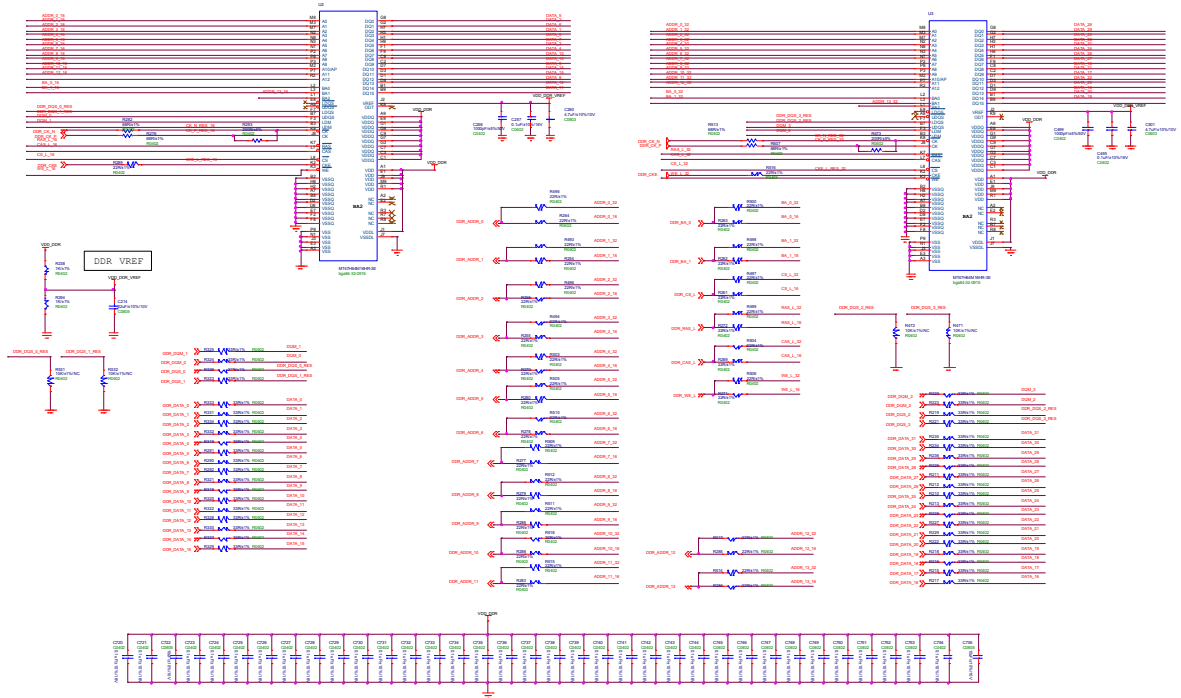


Boot Strap Options

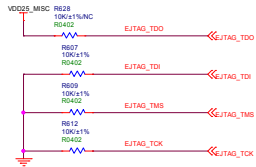


PIN	NAME	DESCRIPTION
GPIO2	AC_SELECT	0 - SPI2 CS (default) 1 - SPI2 CS
GPIO4	SDR_WDTR	0 - 15bit 1 - 32bit
GPIO6	BOOT_SELECT	0 - Select from ROM 1 - From SPI Flash
GPIO8	REF_CLK	0 - 33MHz 1 - 40MHz
GPIO4	SRP_ENABLE	0 - Disabled(default) 1 - Enabled
GPIO1	USB_MODE	0 - Host Mode (default) 1 - Device Mode
GPIO16	DISABLE_OTF_ACCESS	0 - OTF access enabled (default) 1 - OTF access disabled
GPIO7	SRAM_SELECT	0 - SRAM enable (default) 1 - SRAM enable
GPIO10	DOR_SELECT	0 - Selects DOR 2 (default) 1 - Selects DOR 1
DOR A0	TESTROM_DISABLE	0 - Disabled 1 - Disabled (default)
DOR A25	JTAG_MODE	1-JTAG (default) 0 - ATJAG
DOR A24	BOOT_WTF	00 - USB 01 - SPI CS 10 - SDR MEMO 11 - SDR FLASH (default) Valid only if bit 2 (Boot from ROM) is 0

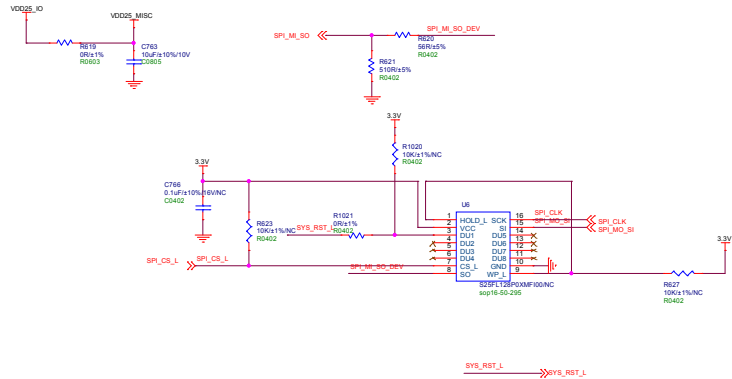
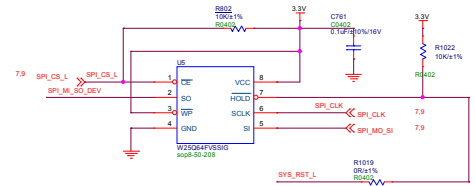
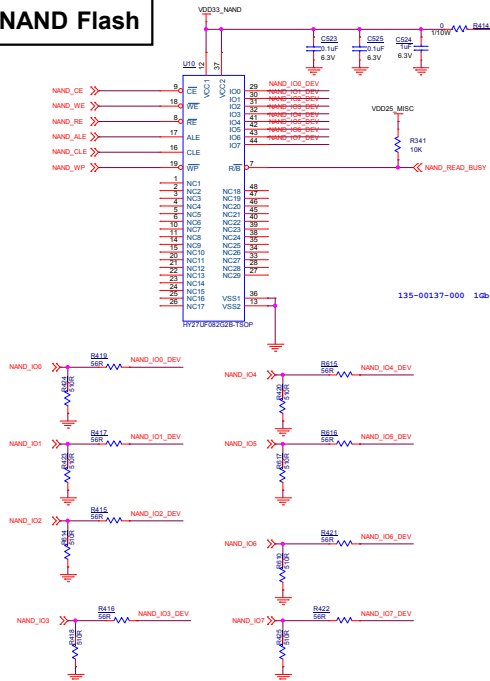
DDR2

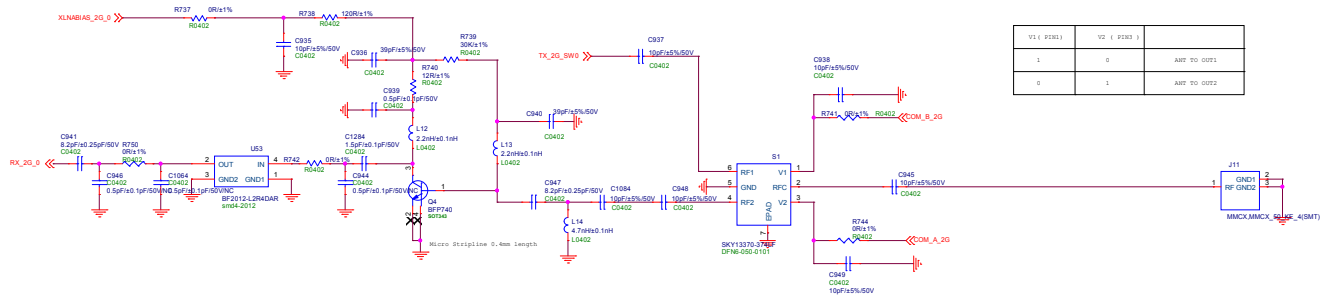


EJTAG TESTPOINT



NAND Flash





V1 (P200)	V2 (P202)	
1	0	ANT TO GND1
0	1	ANT TO GND2

