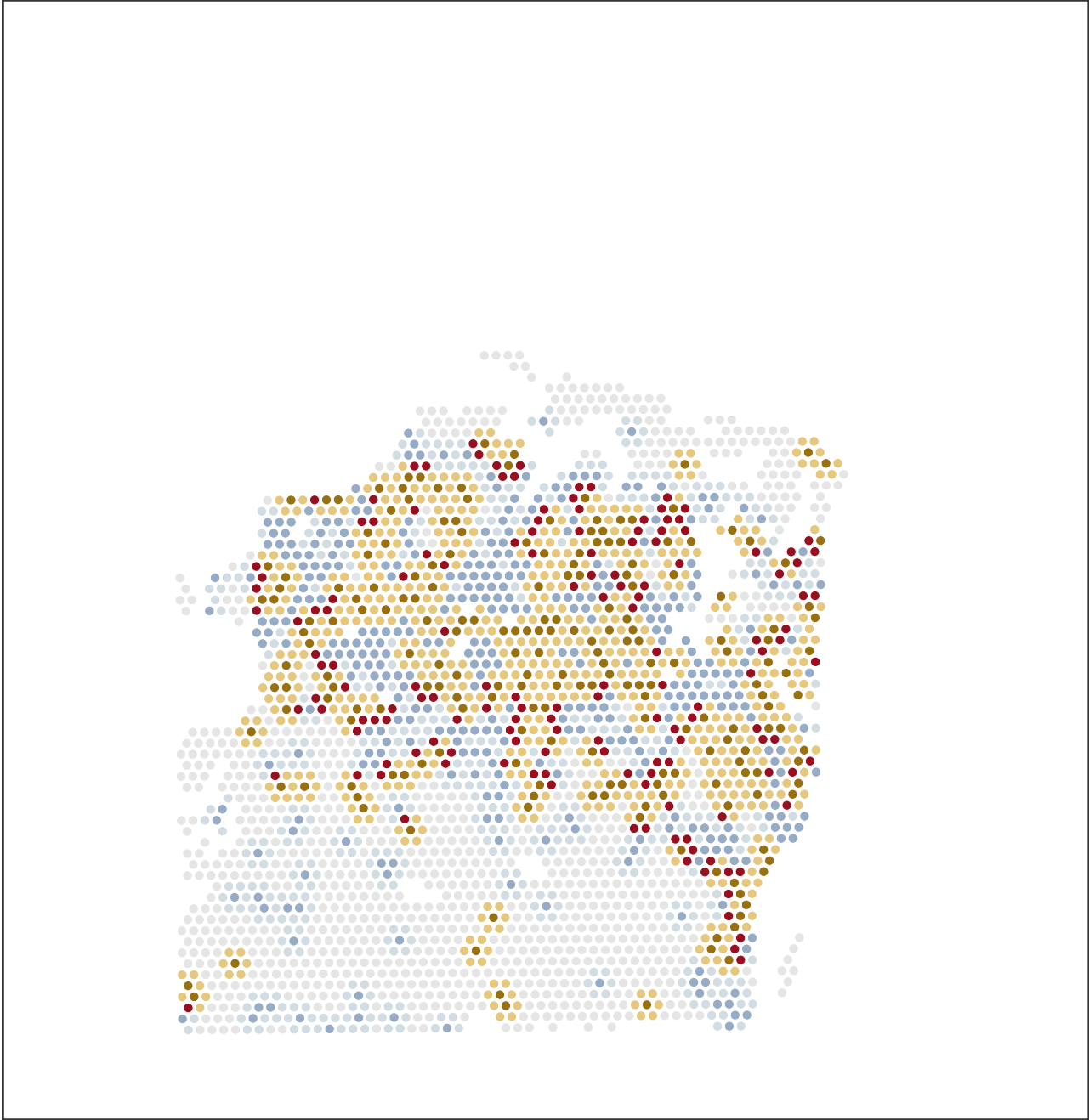


V10A27106_A1_Br3874



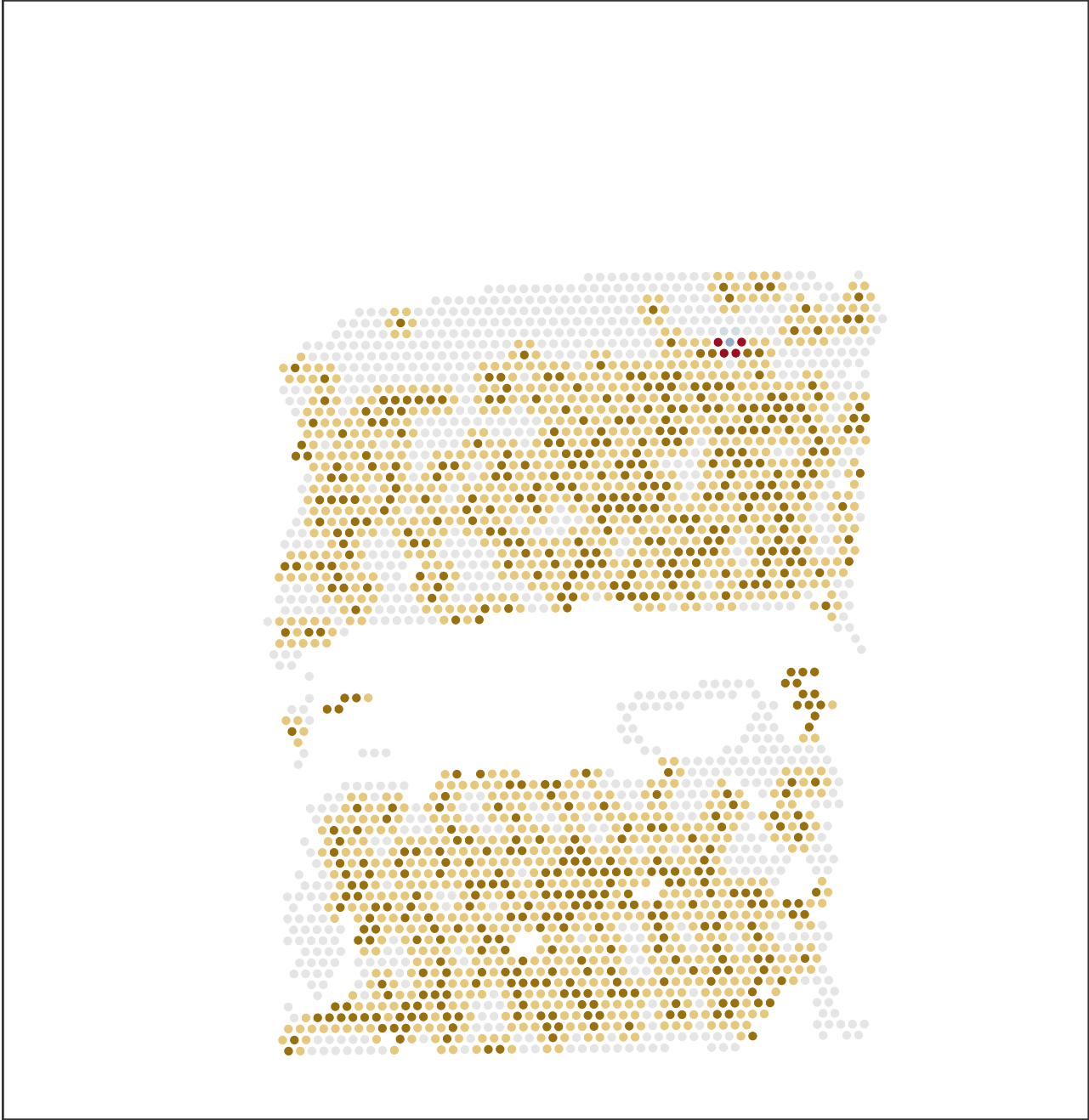
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10A27106_B1_Br3854



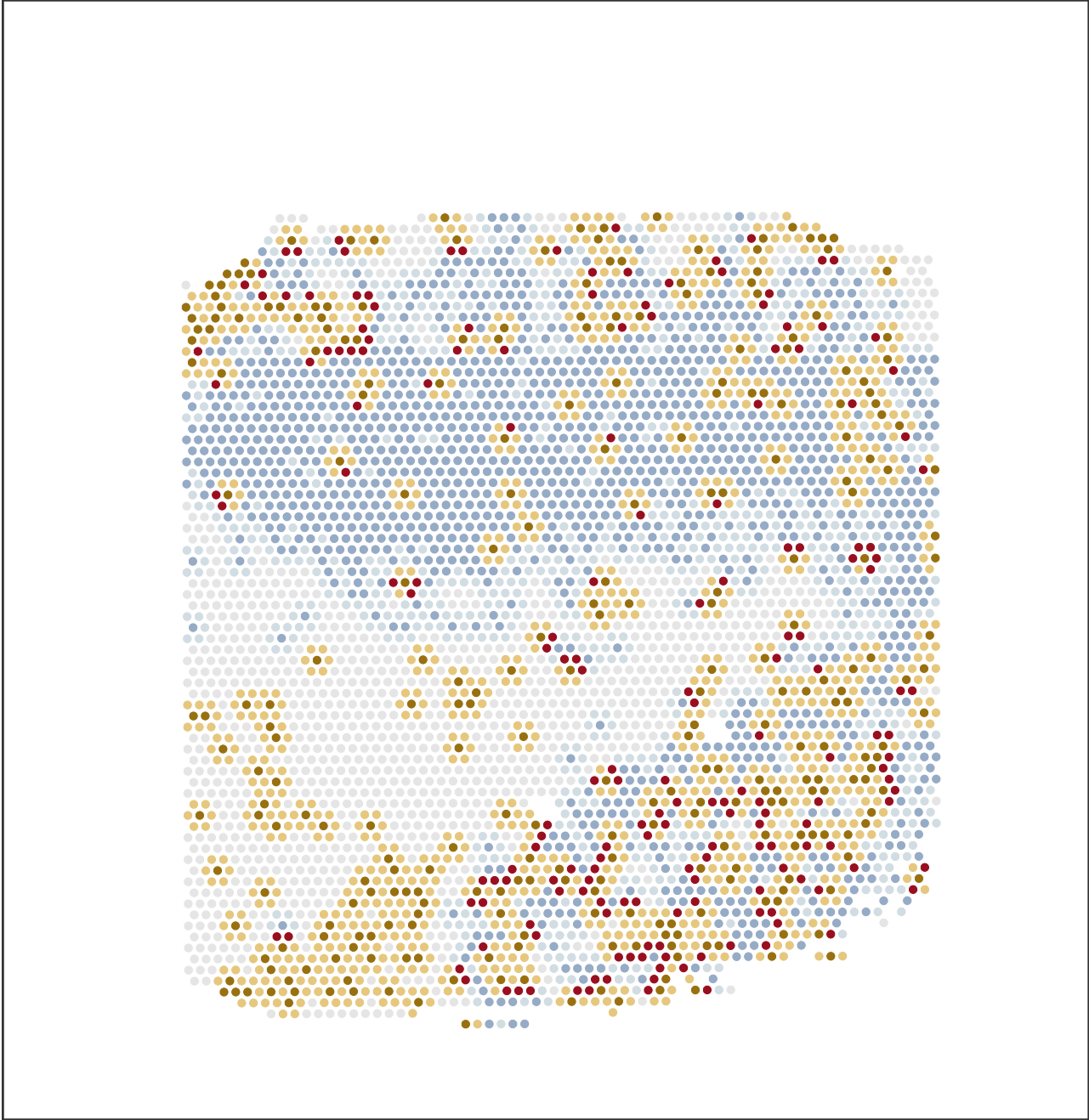
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10A27106_C1_Br3873



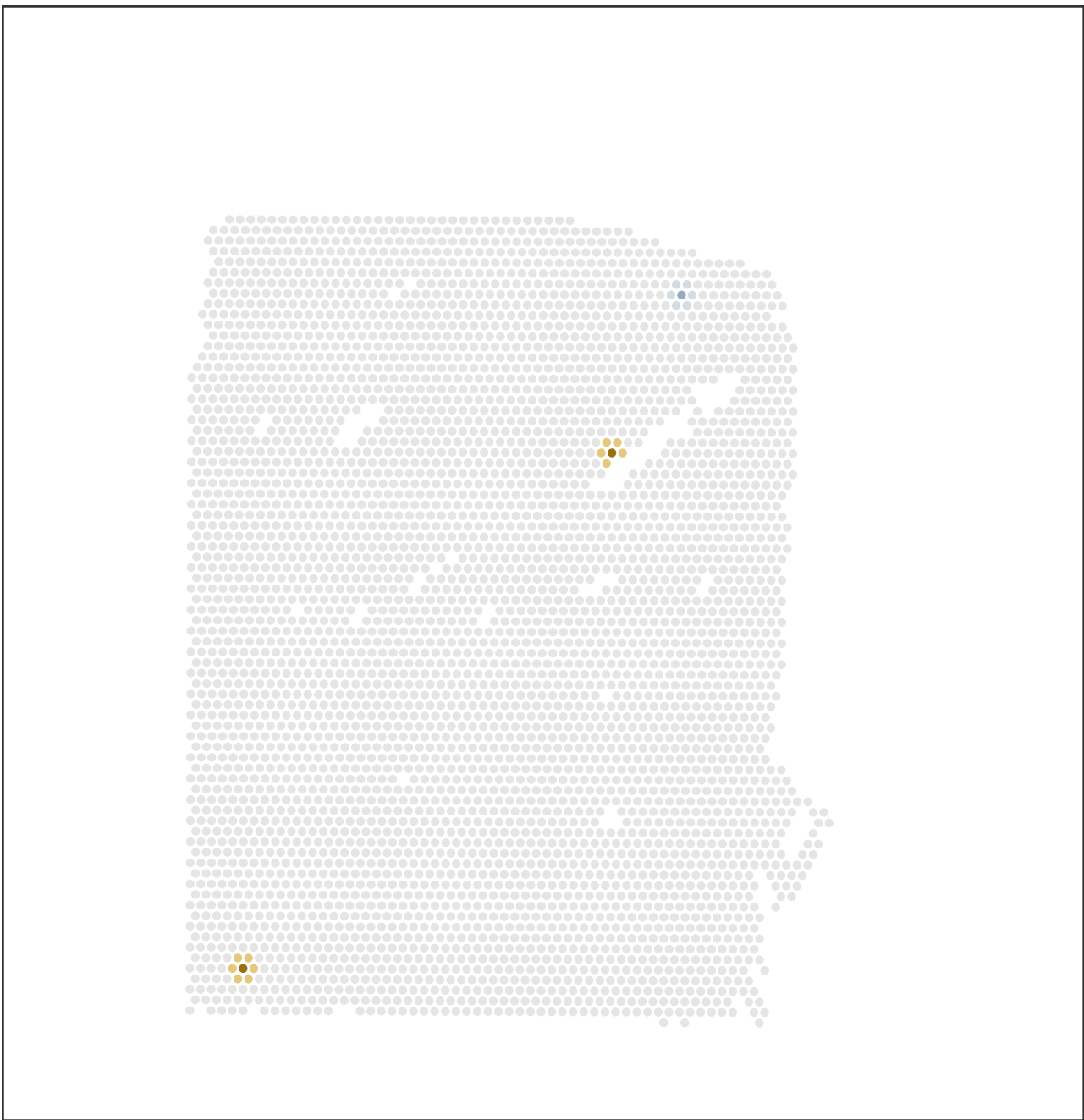
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10A27106_D1_Br3880



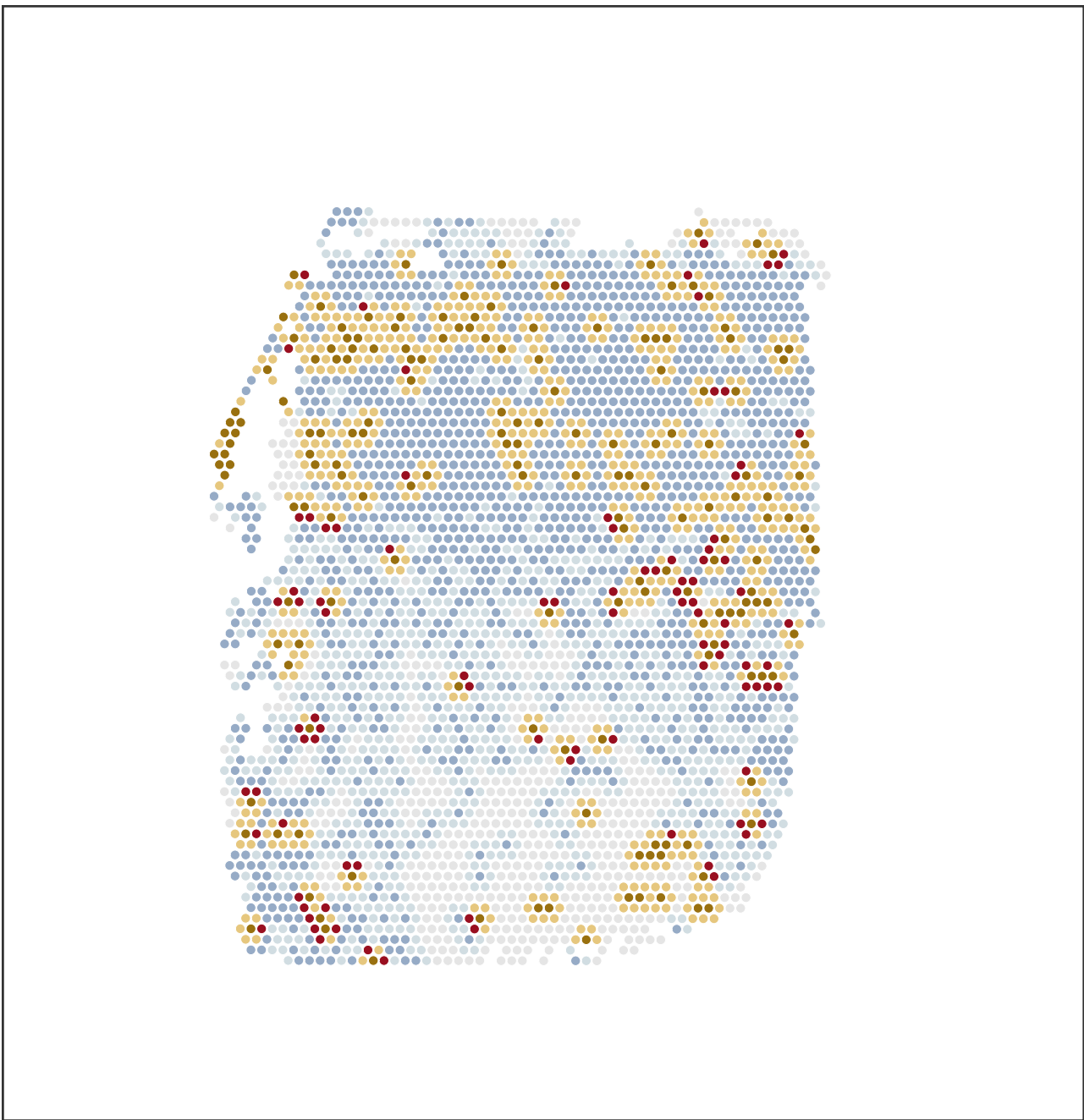
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10T31036_A1_Br3874



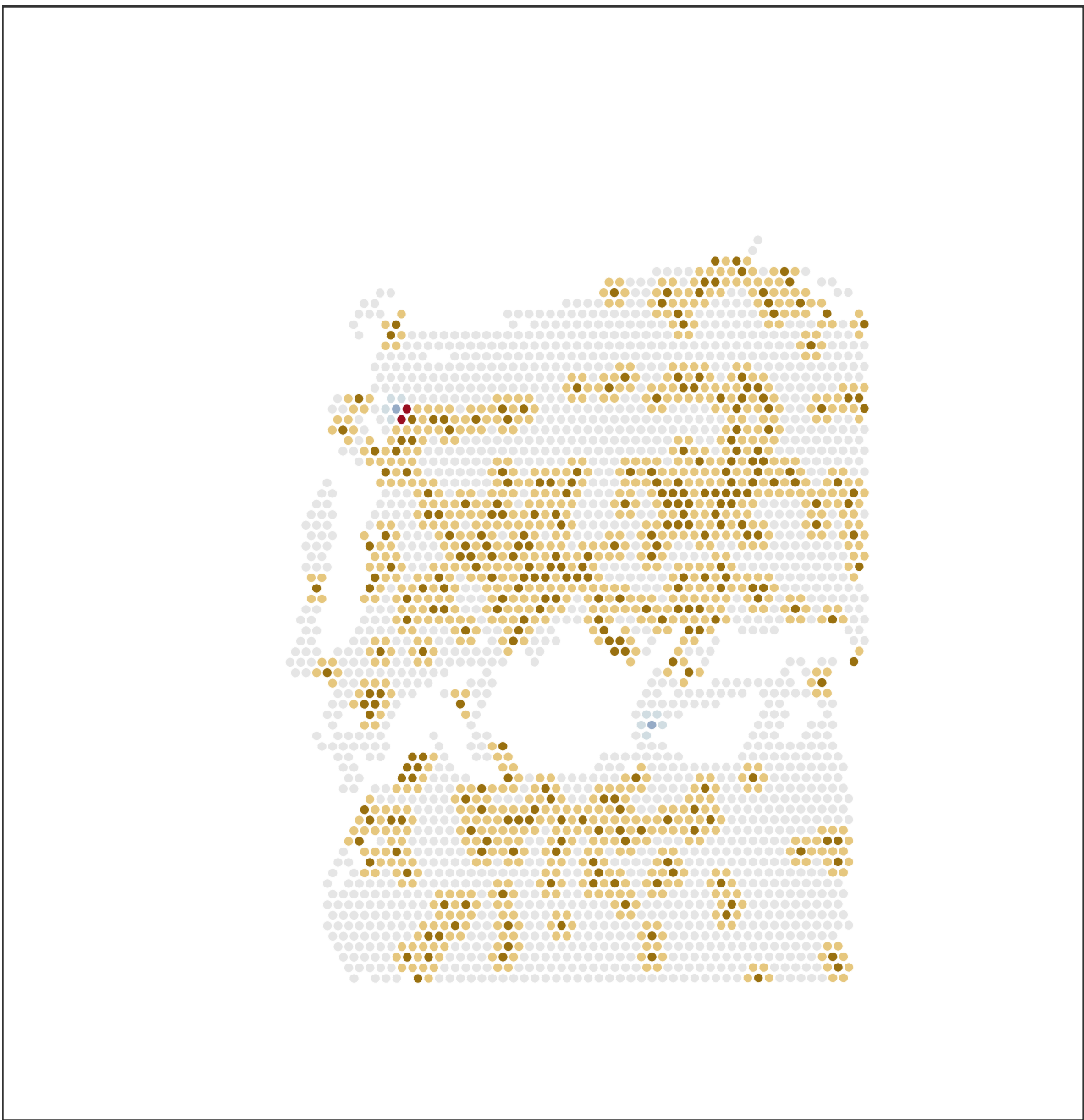
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10T31036_B1_Br3854



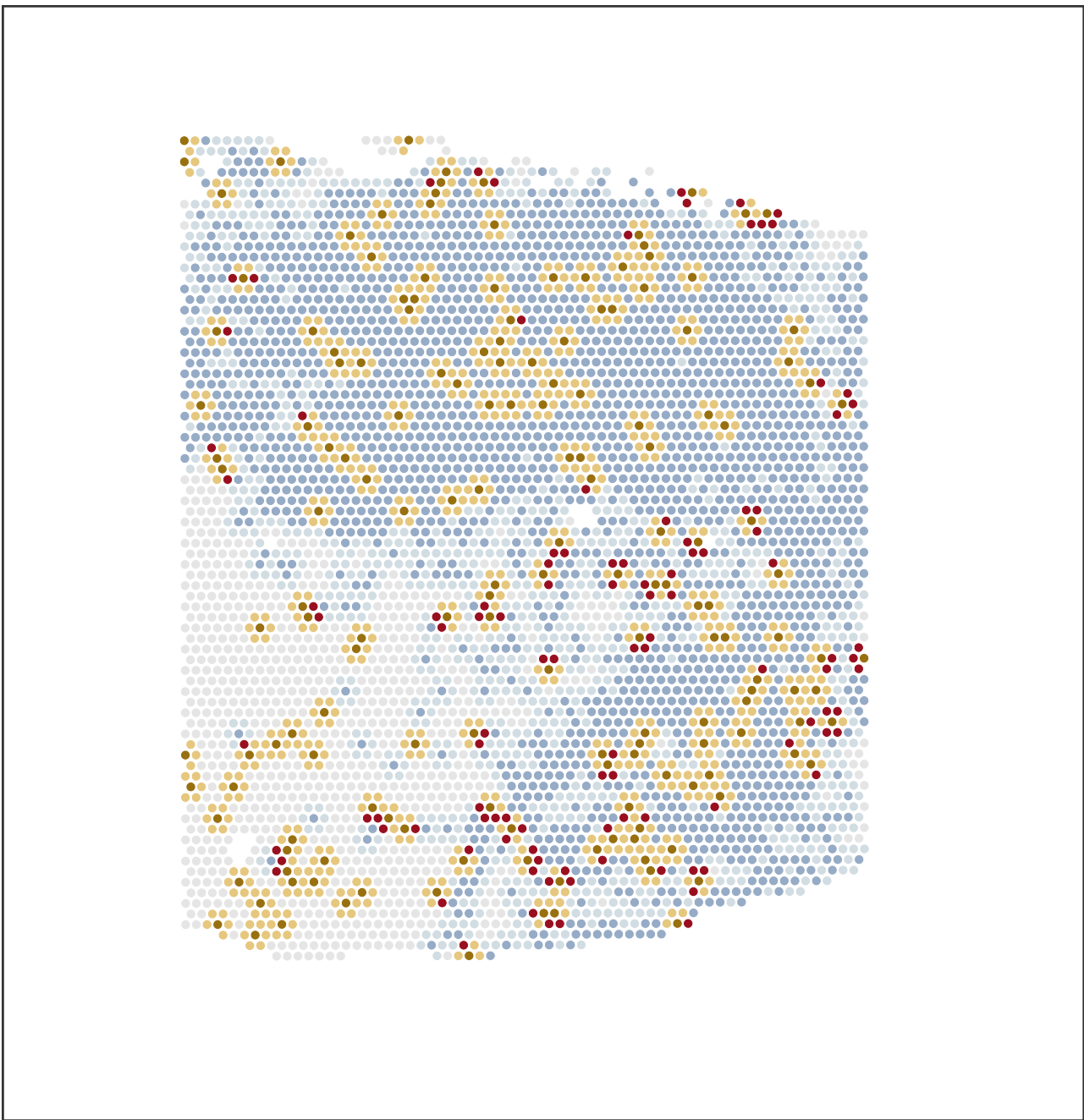
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10T31036_C1_Br3873



- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10T31036_D1_Br3880



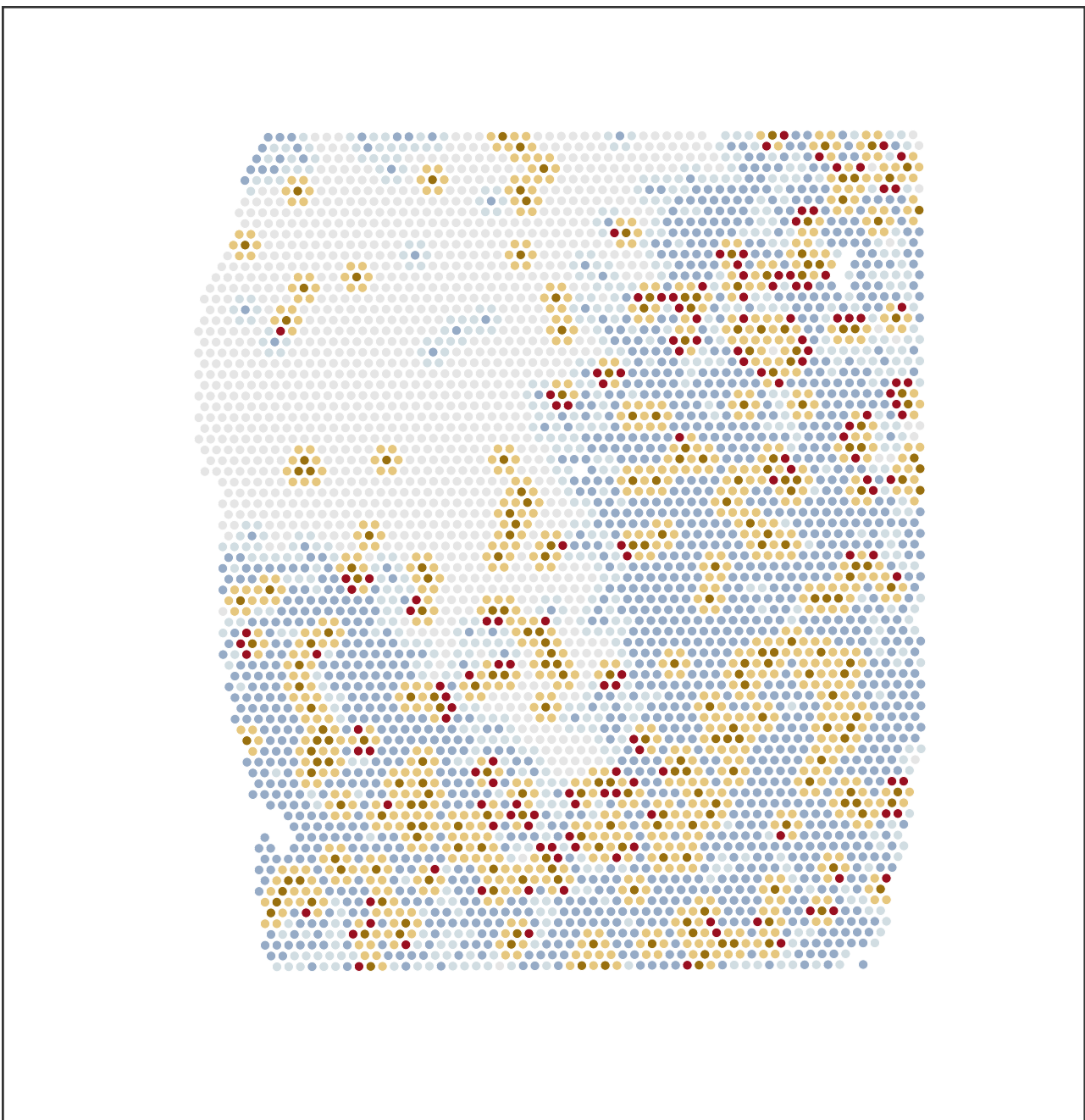
- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10A27004_A1_Br3874



- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both

V10A27004_D1_Br3880



- none
- Ab
- n_Ab
- pTau
- n_pTau
- n_both