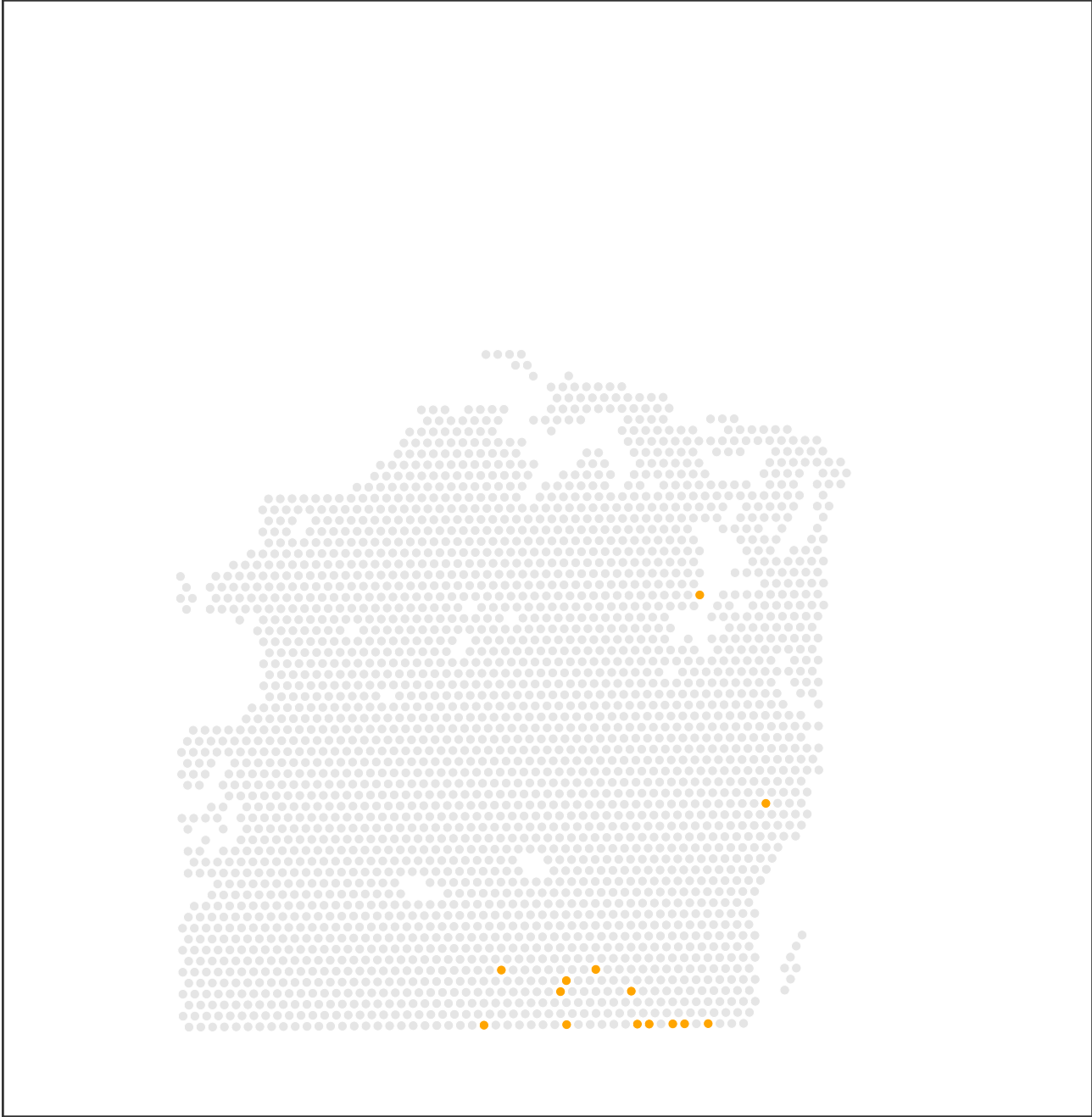


V10A27106\_A1\_Br3874



● FALSE  
● TRUE

V10A27106\_B1\_Br3854



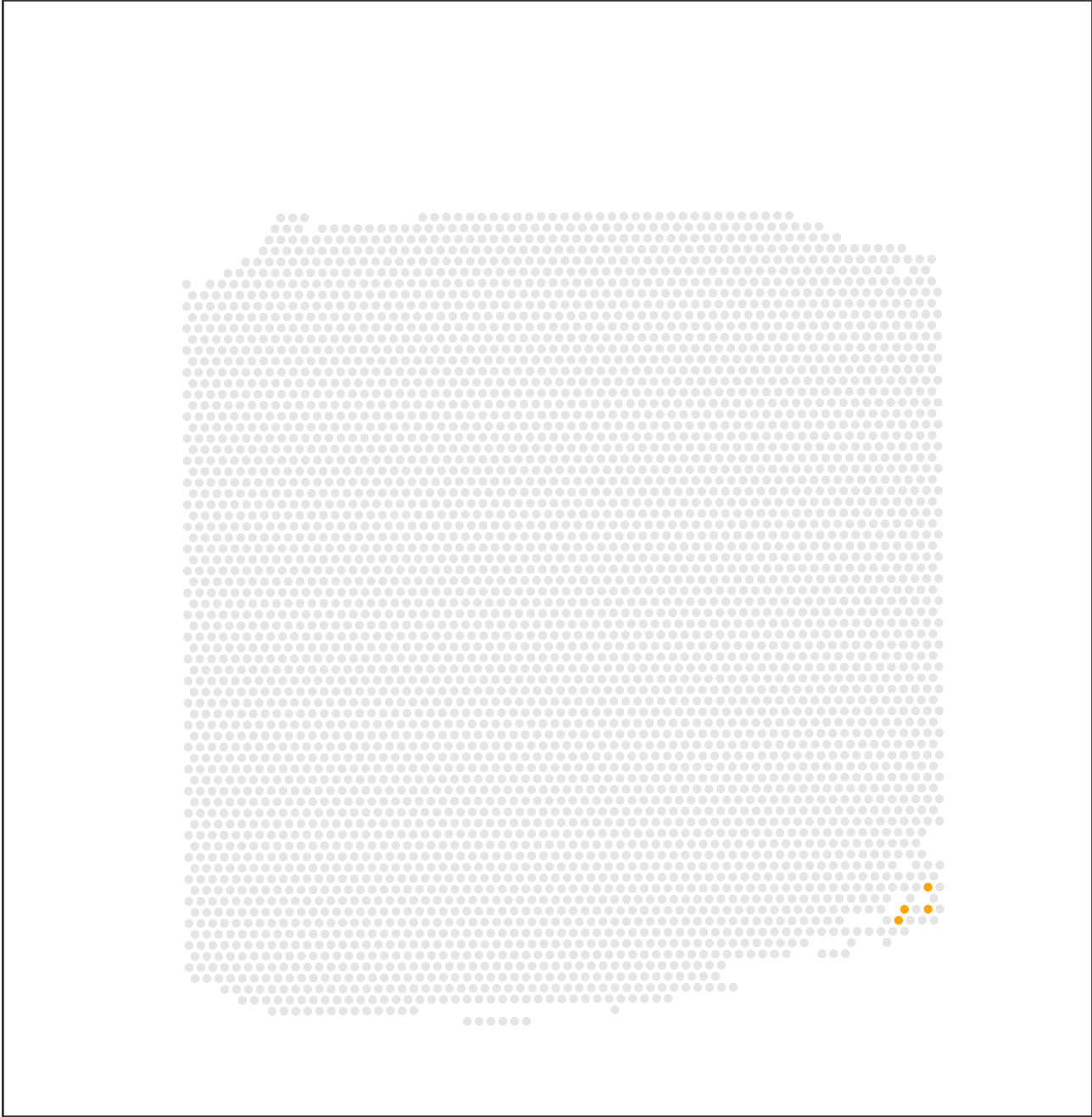
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● TRUE

V10A27106\_C1\_Br3873



● FALSE  
● TRUE

V10A27106\_D1\_Br3880



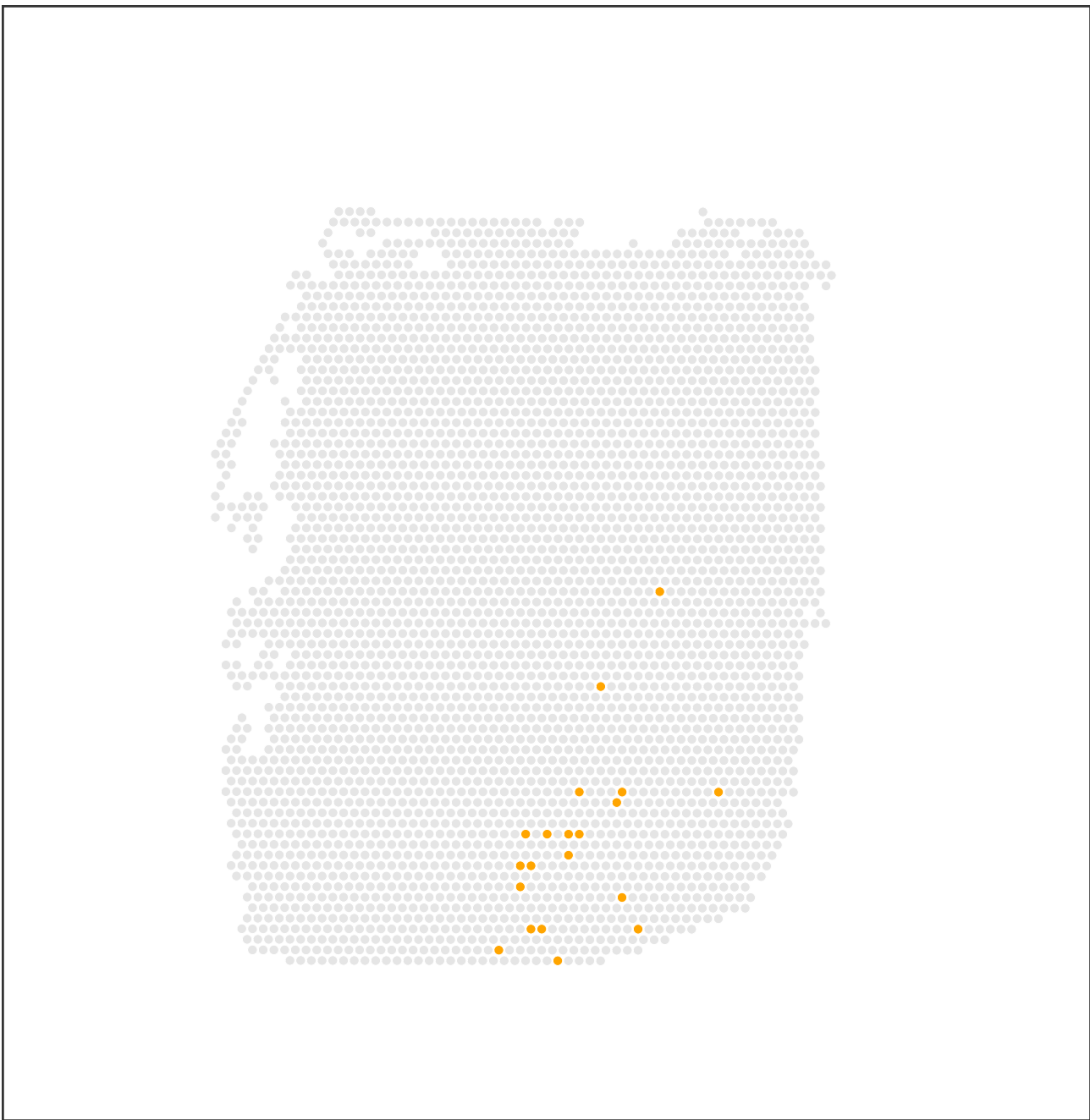
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● TRUE

V10T31036\_A1\_Br3874



● FALSE  
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V10T31036\_B1\_Br3854



● FALSE  
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V10T31036\_C1\_Br3873



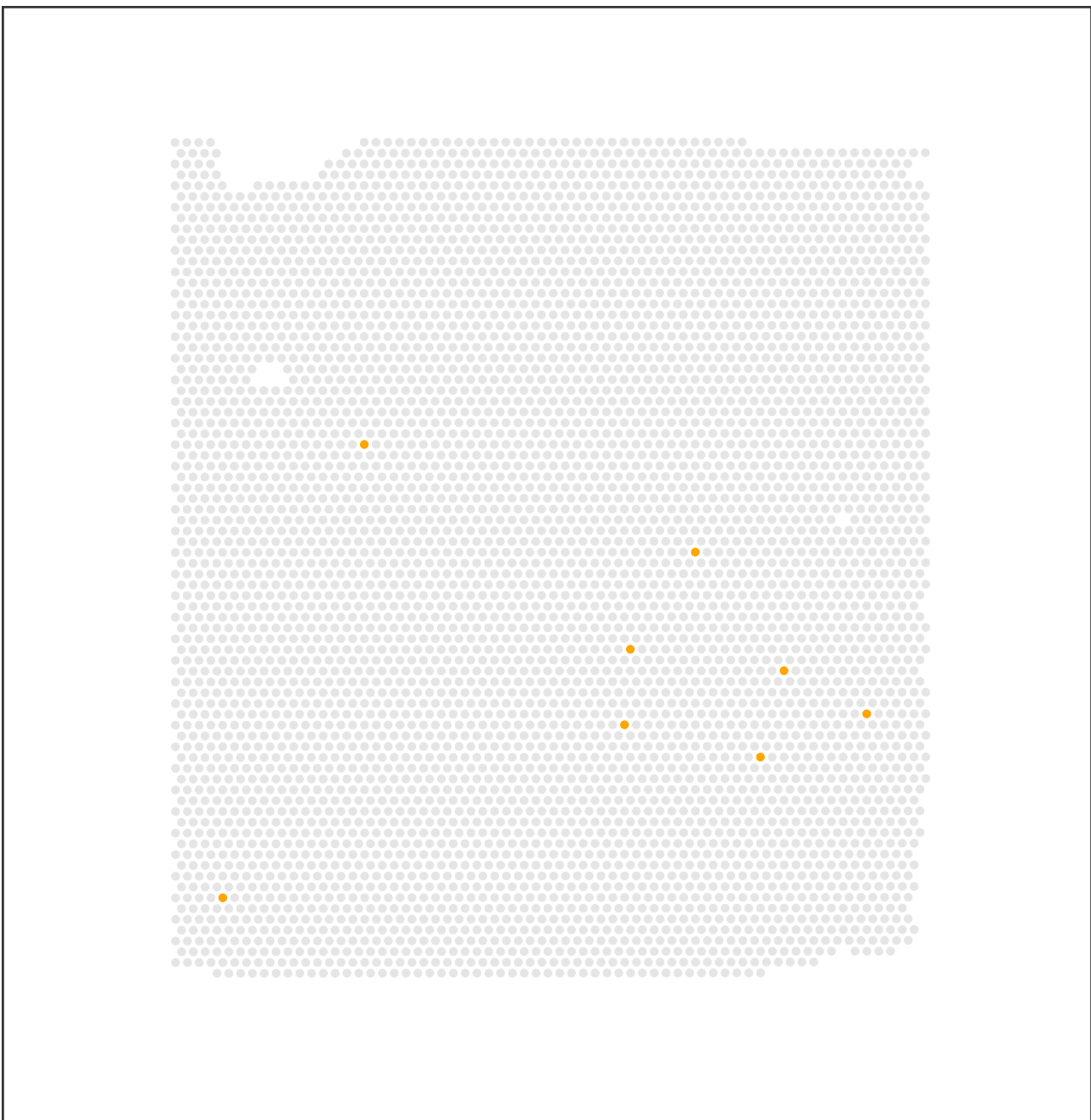
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V10T31036\_D1\_Br3880



● FALSE  
● TRUE

V10A27004\_A1\_Br3874



● FALSE  
● TRUE

V10A27004\_D1\_Br3880



● FALSE  
● TRUE