

<p>Tabela Verdade AND</p> <table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>X</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> </tr> </tbody> </table>	A	B	X	0	0	0	0	1	0	1	0	0	1	1	1		<p>simbolo</p>
A	B	X															
0	0	0															
0	1	0															
1	0	0															
1	1	1															
<p>Tabela Verdade</p>	<p>Circuito Elétrico</p>	<p>Símbolo</p>															

Circuito Somador Completo de 1 Bit

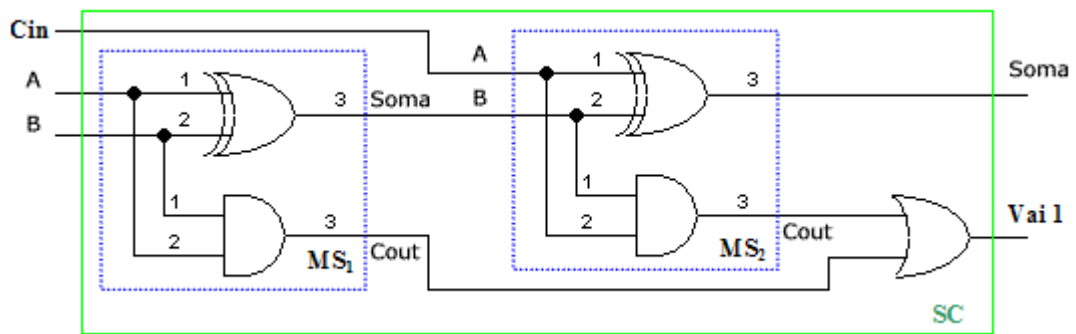
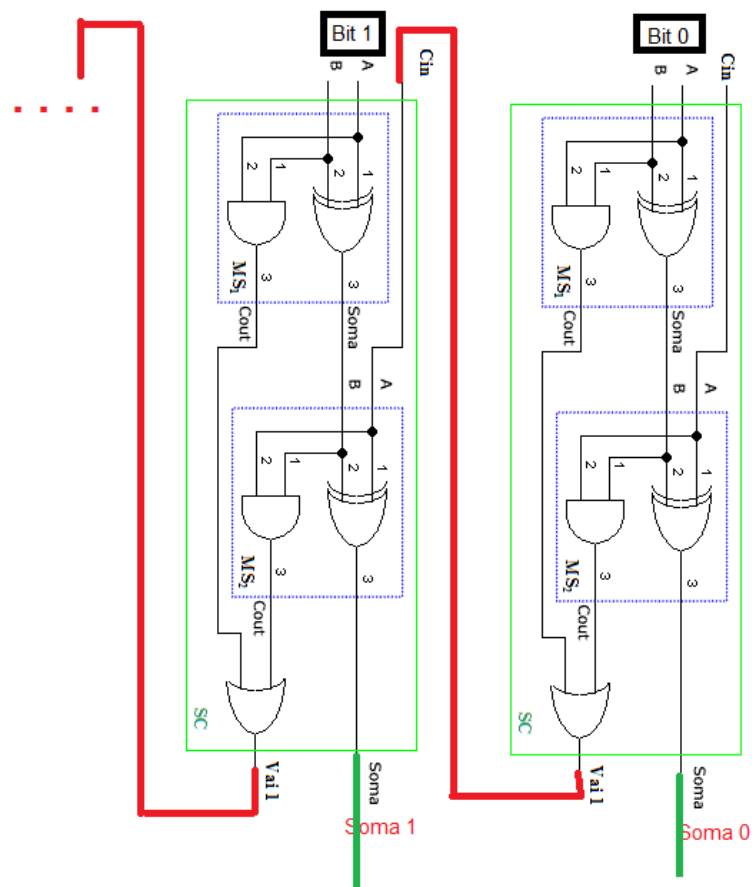


Tabela Verdade – Somador Completo

Cin	A	B	Soma MS ₁	Cout MS ₁	Cout MS ₂	Soma	Vai 1
0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	0
0	1	0	1	0	0	1	0
0	1	1	0	1	0	0	1
1	0	0	0	0	0	1	0
1	0	1	1	0	1	0	1
1	1	0	1	0	1	0	1
1	1	1	0	1	0	1	1



Somador com 2 bits completos ..