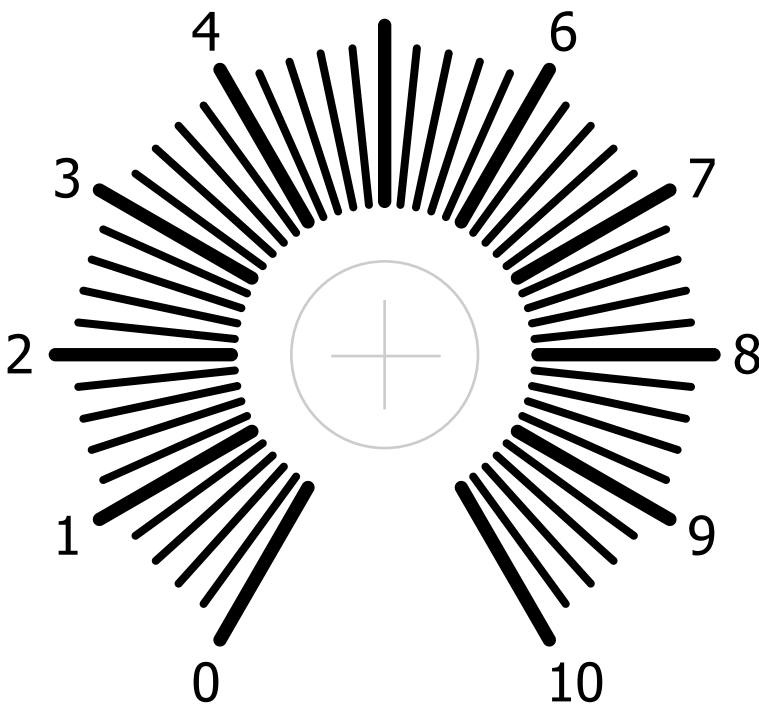
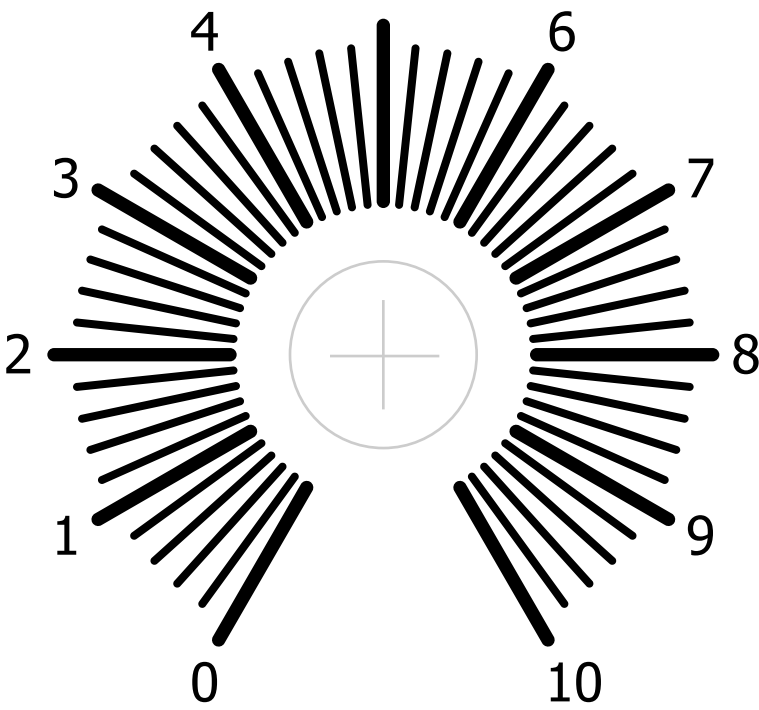




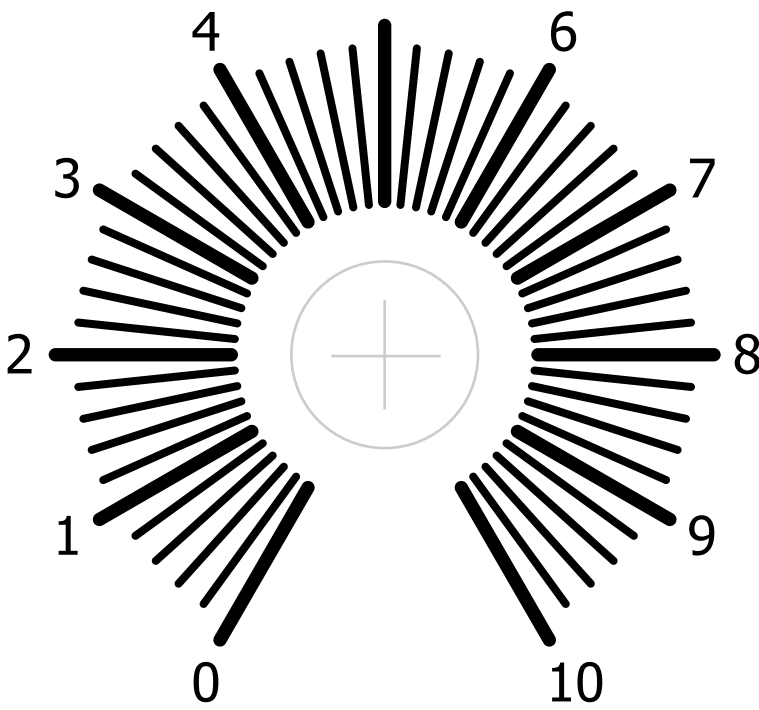
Dial 1



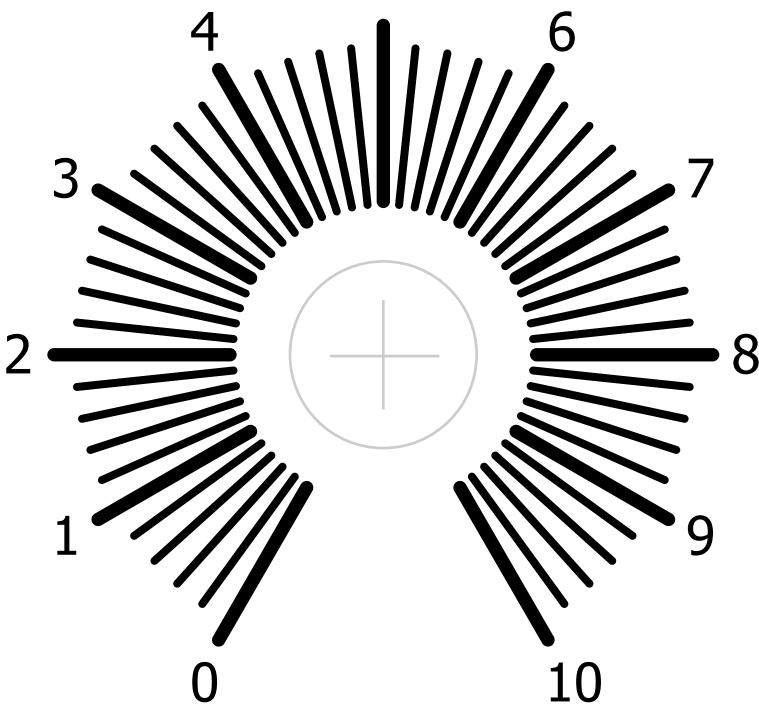
Dial 2



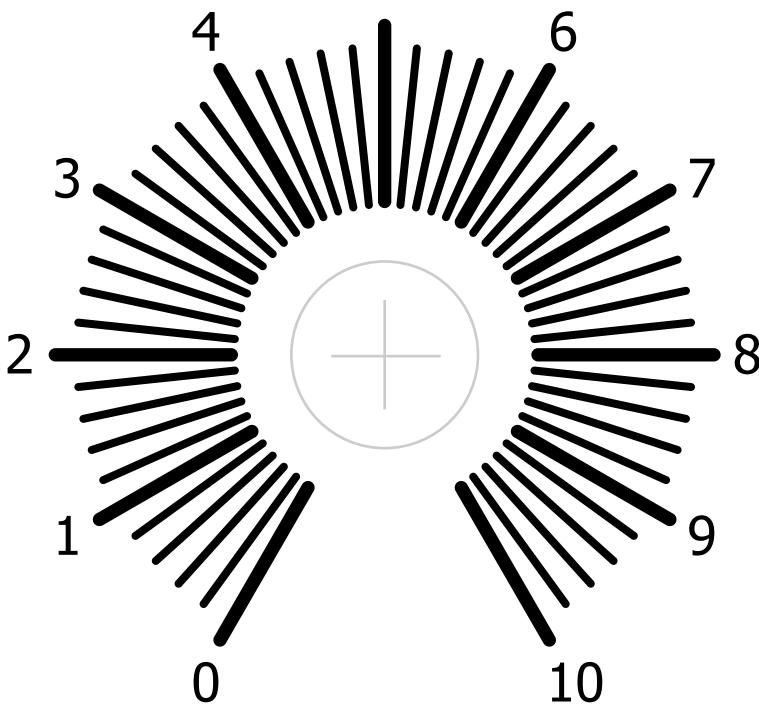
Dial 3



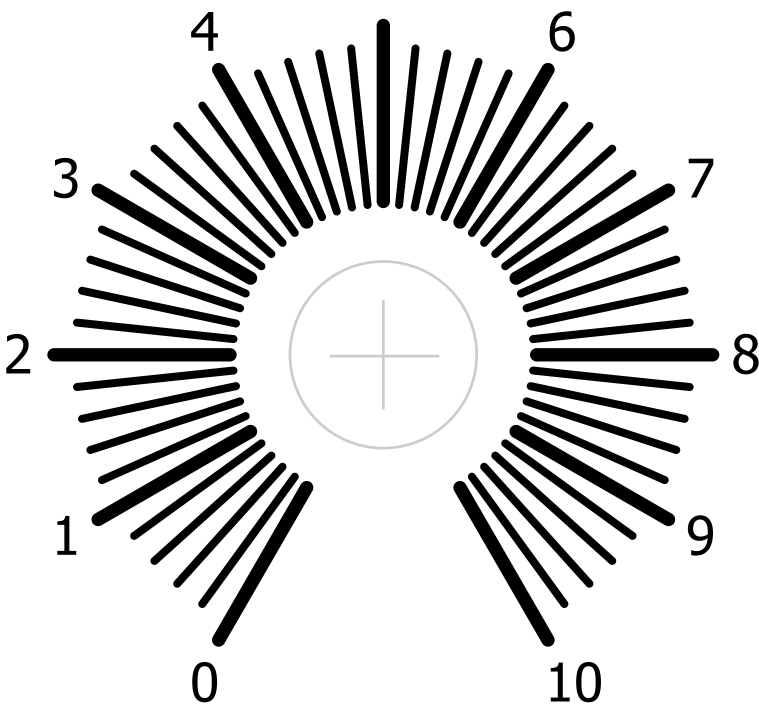
Dial 4



Dial 5



Dial 6



Jack 1



Jack 2



Jack 3



Jack 4



Jack 5



Jack 6



Jack 7



Jack 8



Jack 9



Jack 10



Jack 11



Jack 12

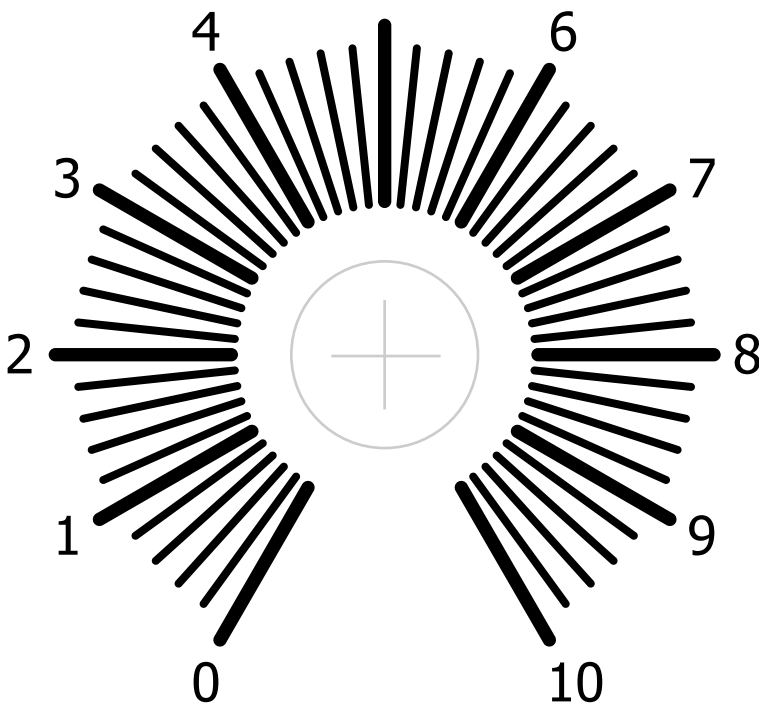




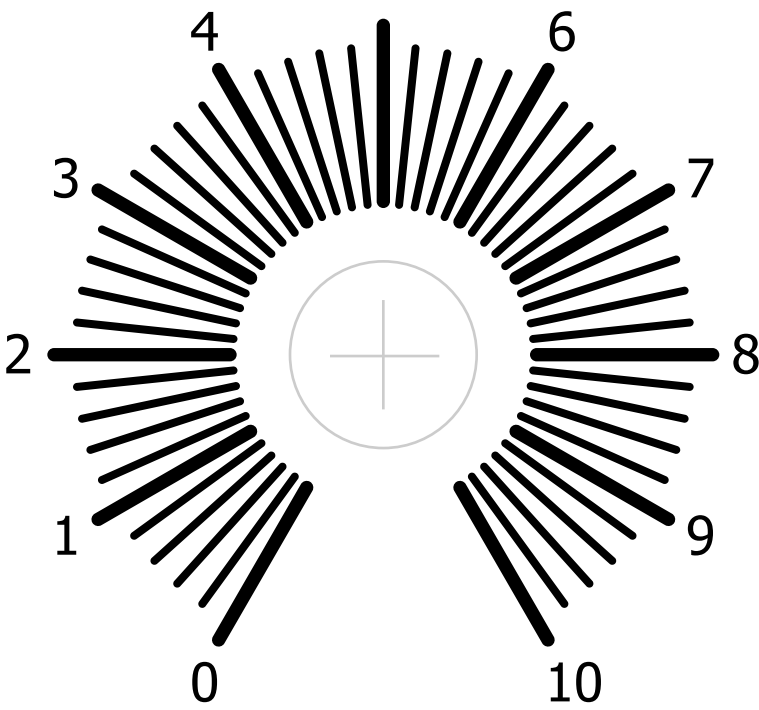
VCO



Course



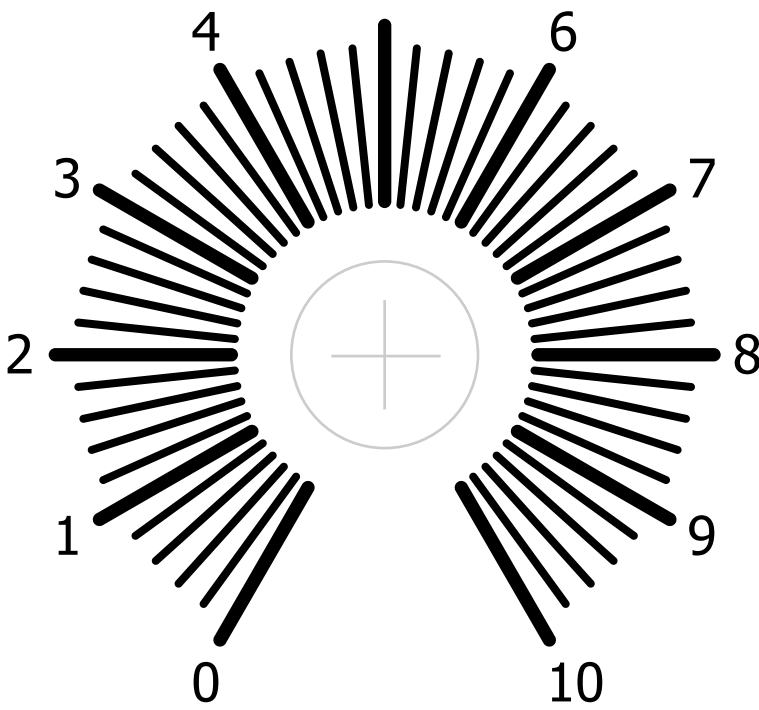
Fine



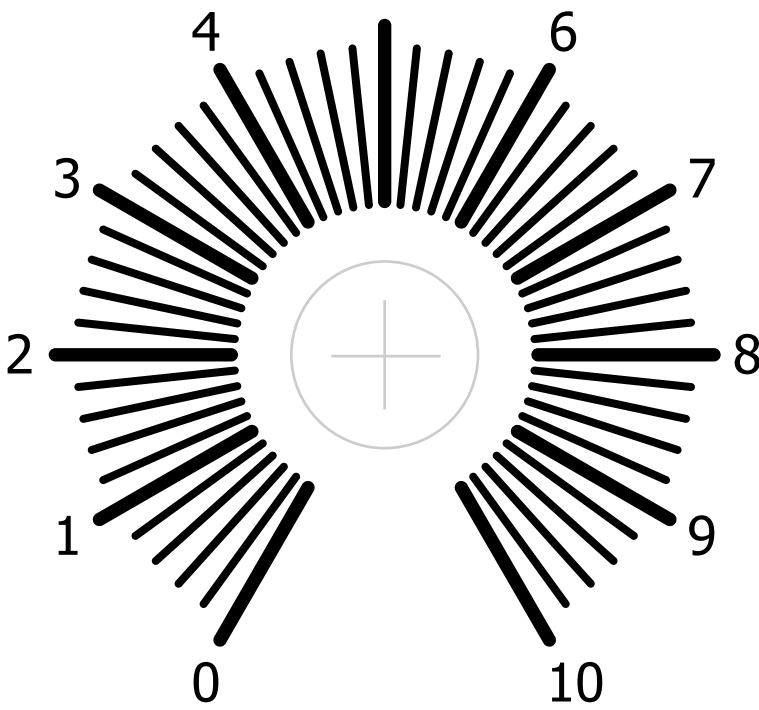
Frequency
Adjust



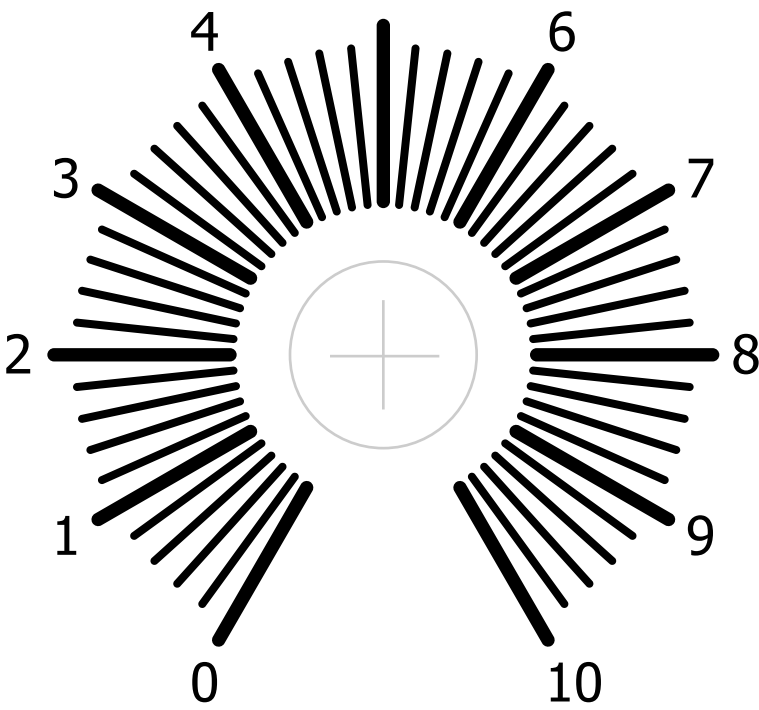
Freq Mod 1 Depth



Freq Mod 2 Depth



PWM Width %



Freq Mod 1 In



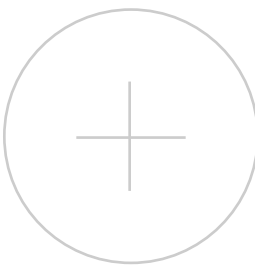
PWM CV In



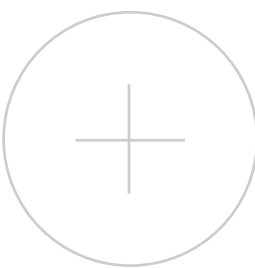
Freq Mod 1 In



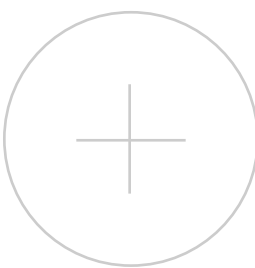
Sync In



CV In



Sine Out



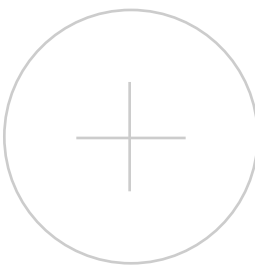
Triangle Out



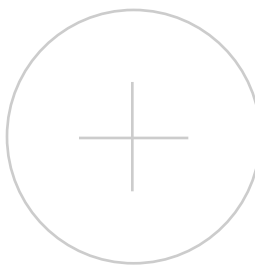
CV Linear In



Ramp Out

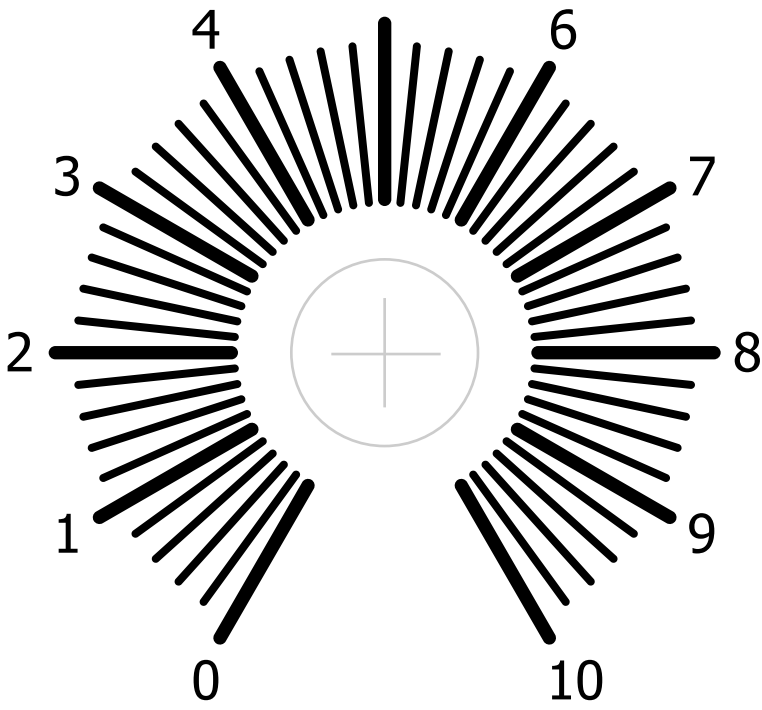


Square Out

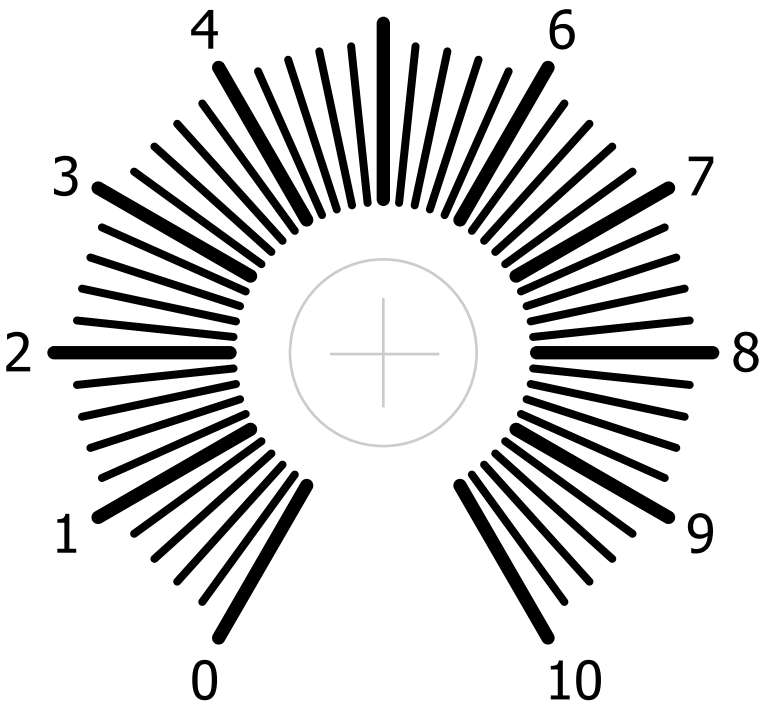


VCF 12

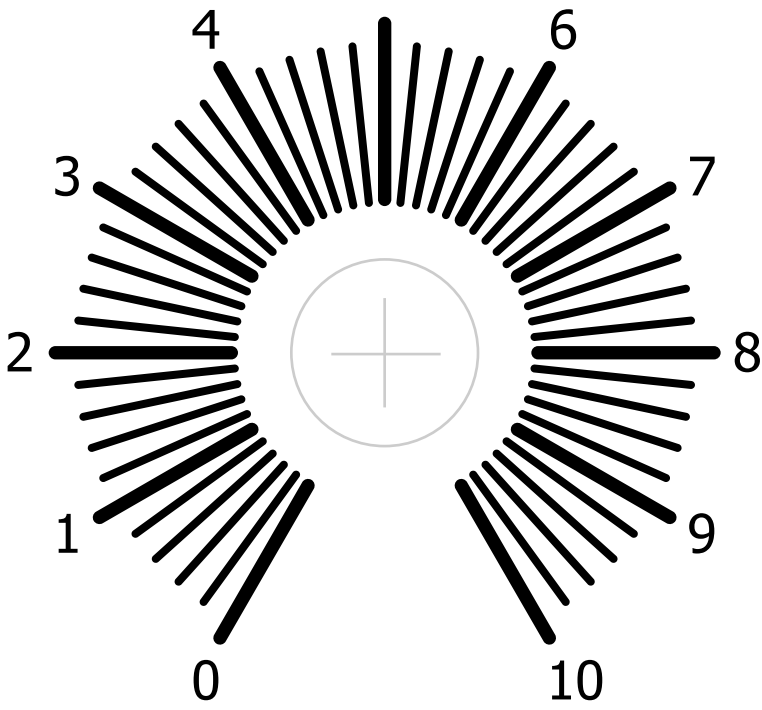
Signal 1



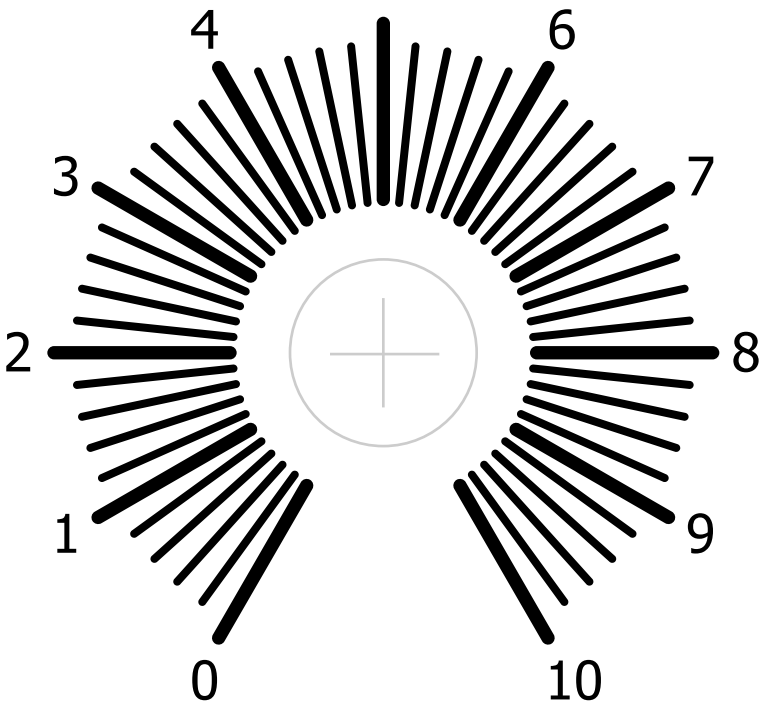
Resonance



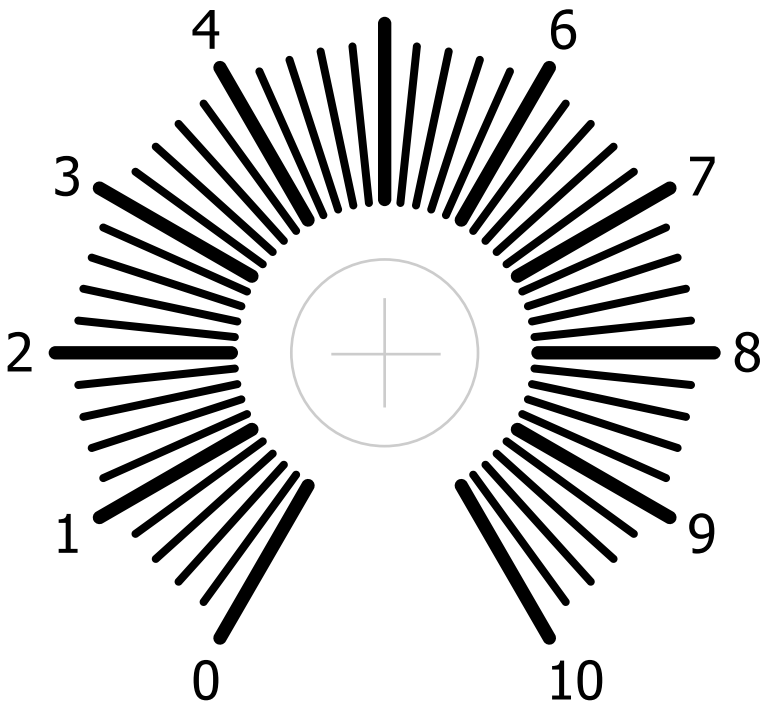
Signal 2



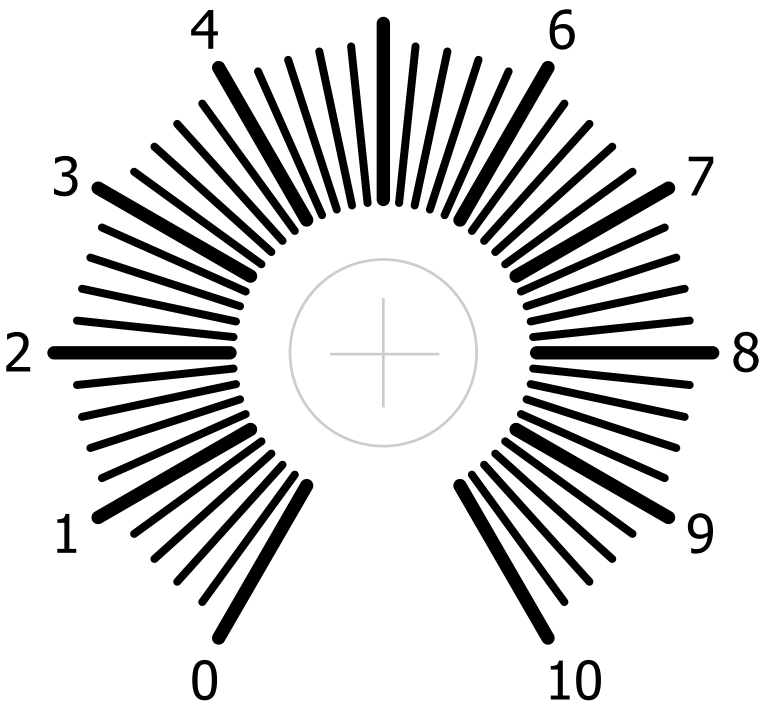
Cut-Off Frequency



Signal 3



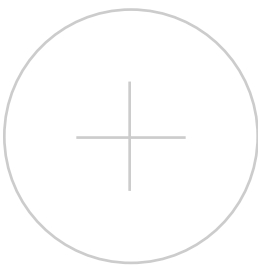
Freq Mod Depth



Signal 1 In



Cut-Off CV In



High-Pass Out



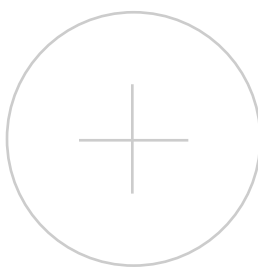
Signal 2 In



Freq Mod In



Band-Pass Out



Signal 3 In



Resonance CV In

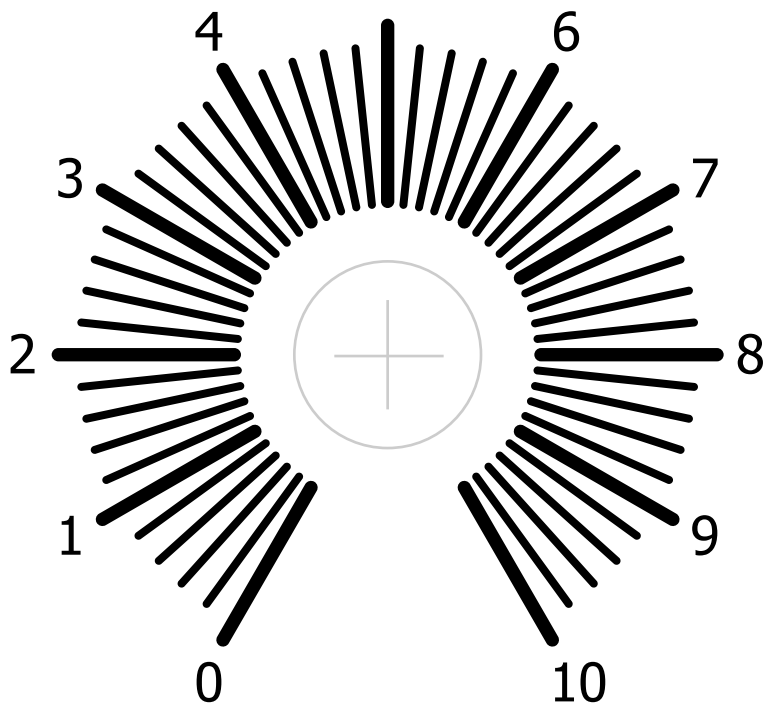


Low-Pass Out

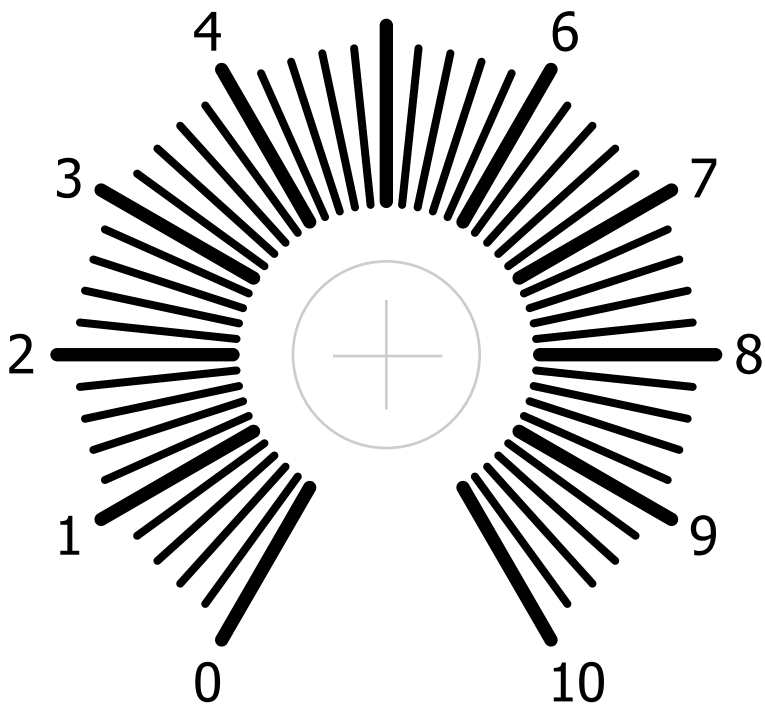


VCF 24

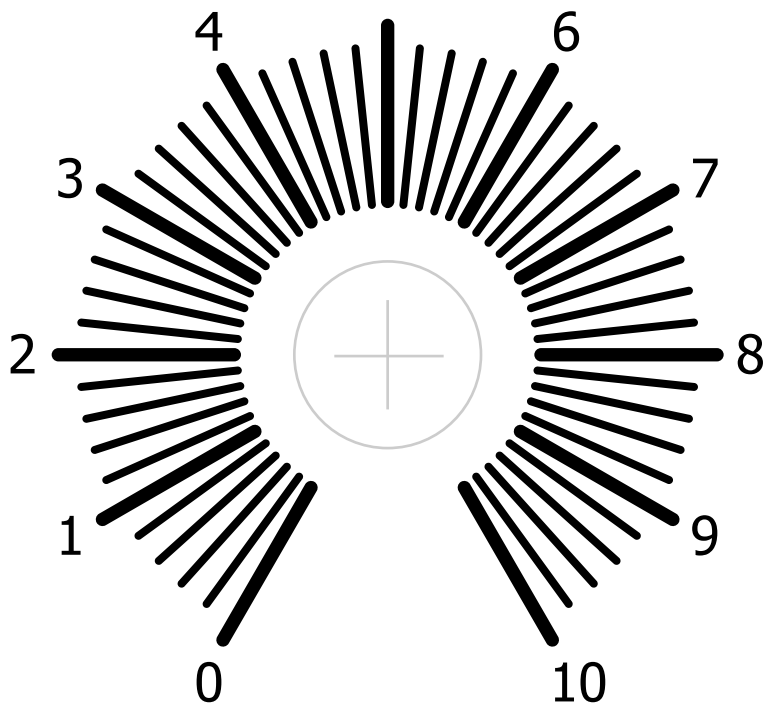
Signal 1



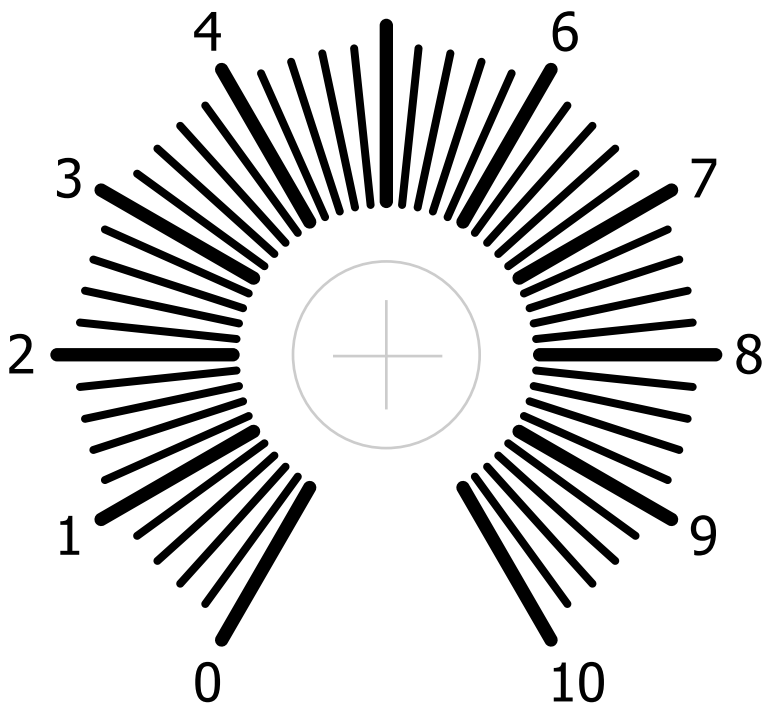
Resonance



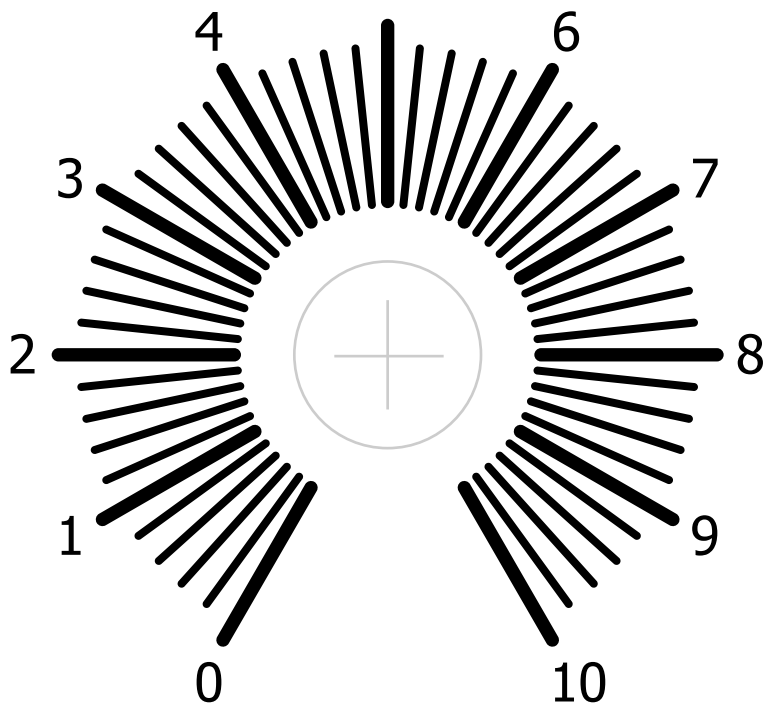
Signal 2



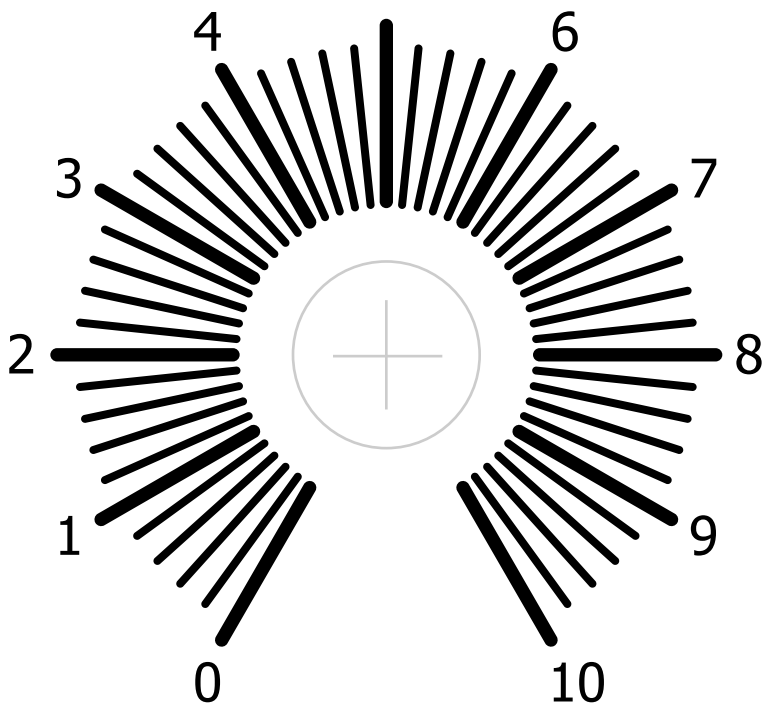
Cut-Off Frequency



Signal 3



Freq Mod Depth



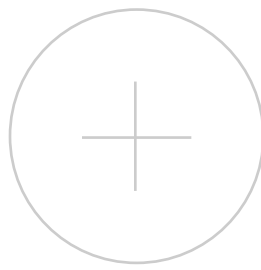
Signal 1 In



Cut-Off CV In



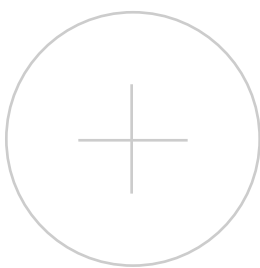
Cut-Off CV In



Signal 2 In



Freq Mod In



Signal 3 In



Resonance CV In



Low-Pass Out

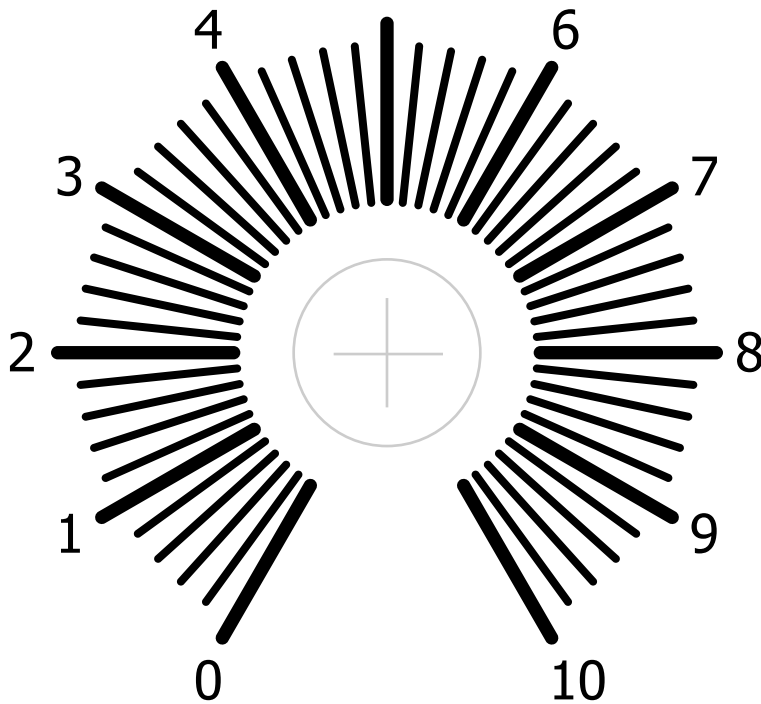




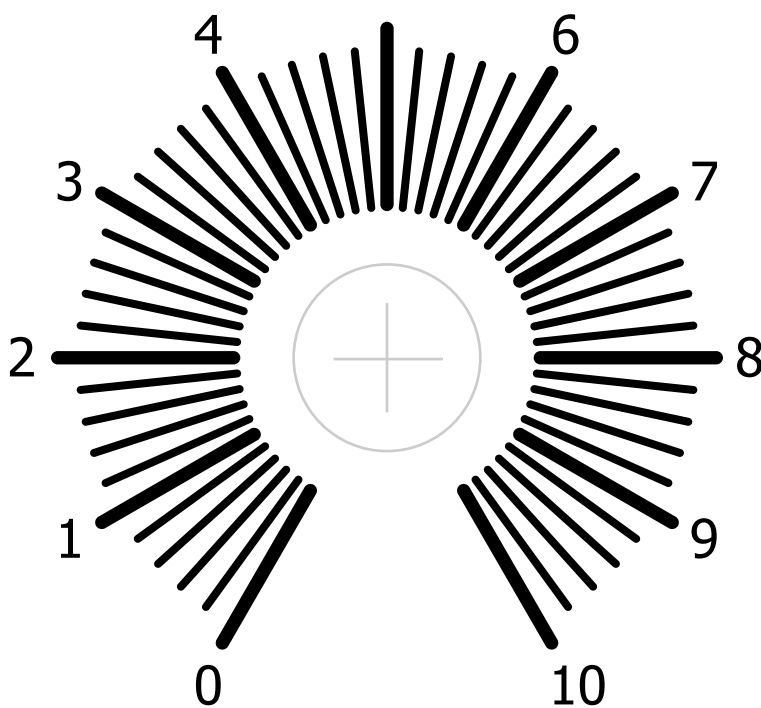
1U HEAD



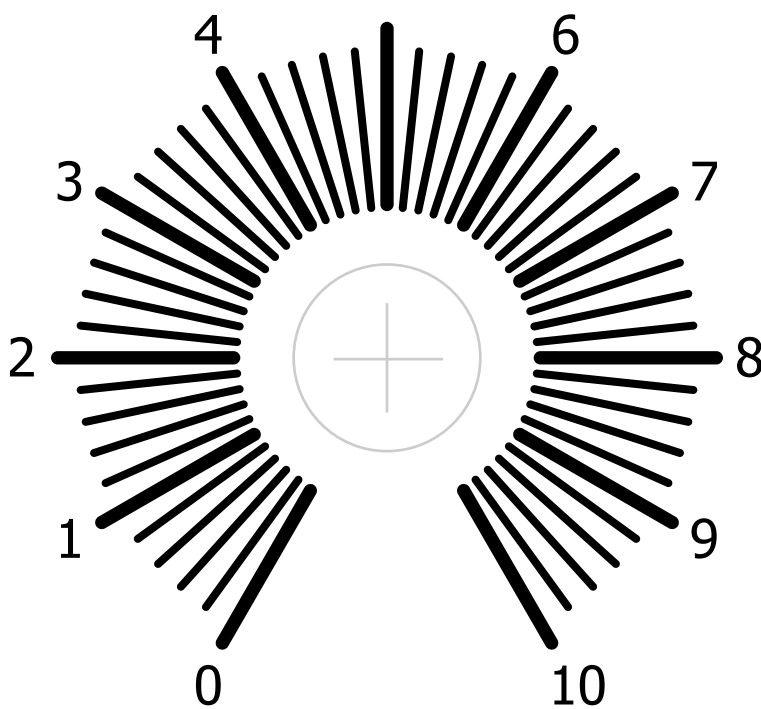
Dial 1



Dial 2



Dial 3



Jack 1



Jack 2



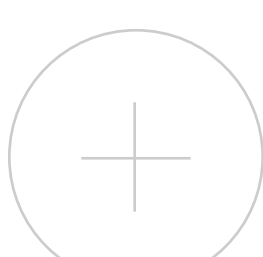
Jack 3



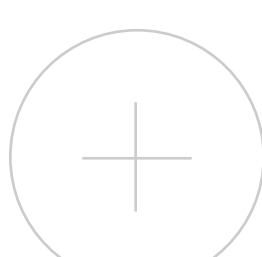
Jack 4



Jack 5



Jack 6



Jack 7



Jack 8



1U FOOTER

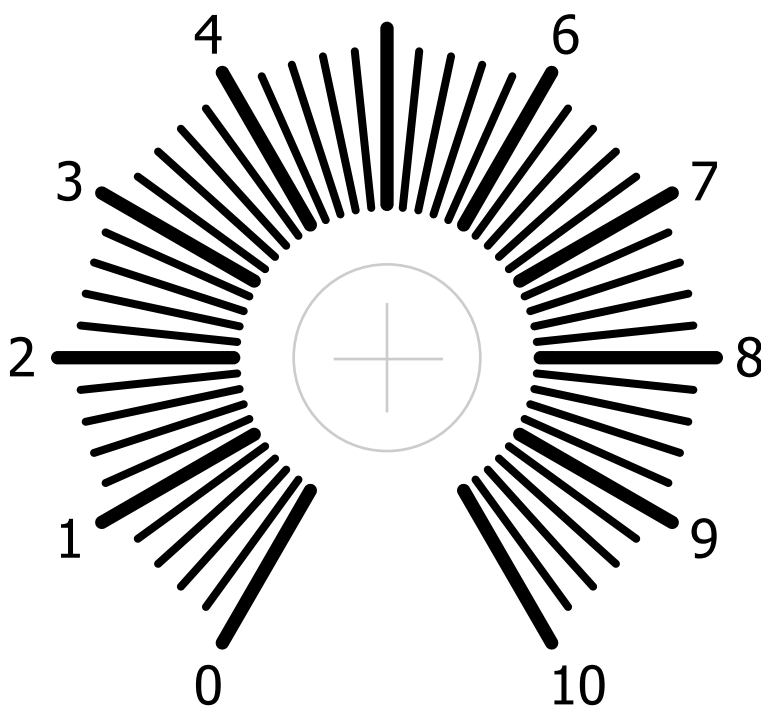




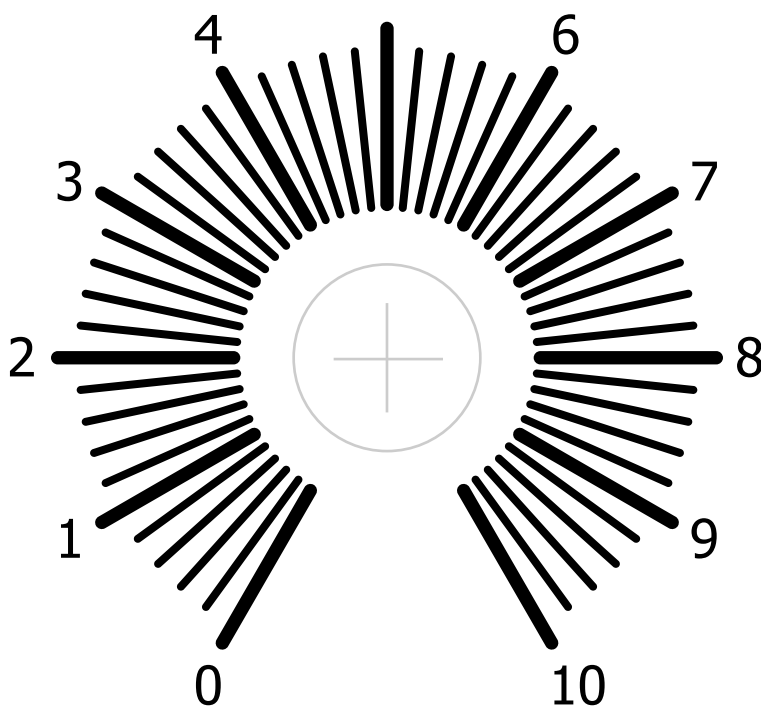
ADSR



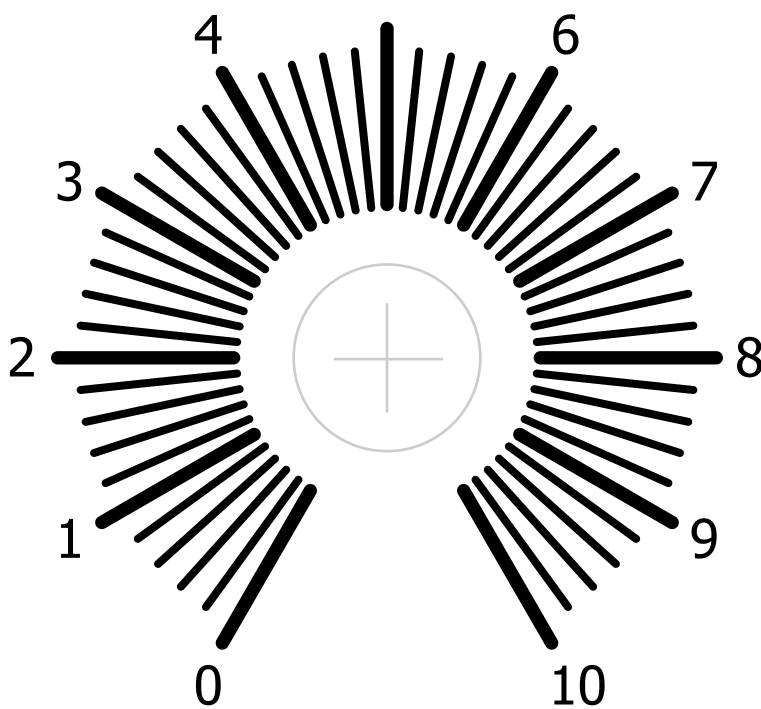
Attack



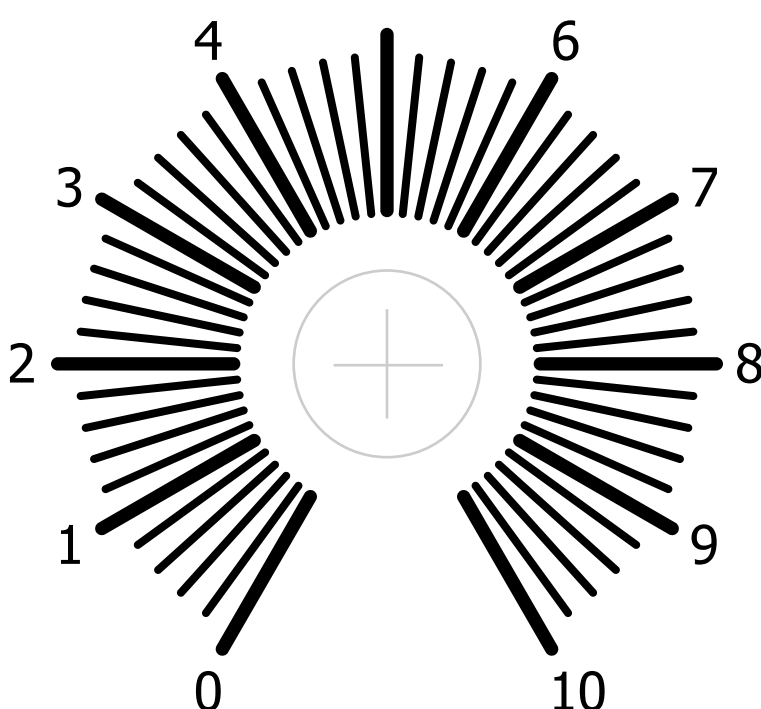
Decay



Sustain



Release



Manual

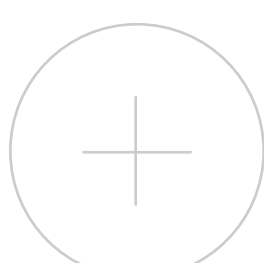
Duration

Short



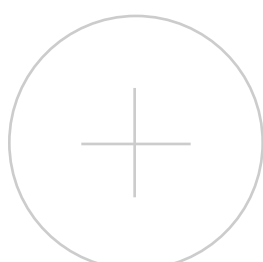
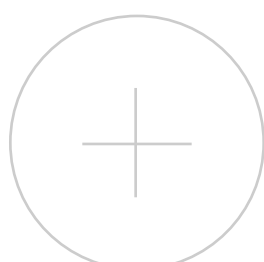
Long

Gate In



Trigger In

Out



MFOS

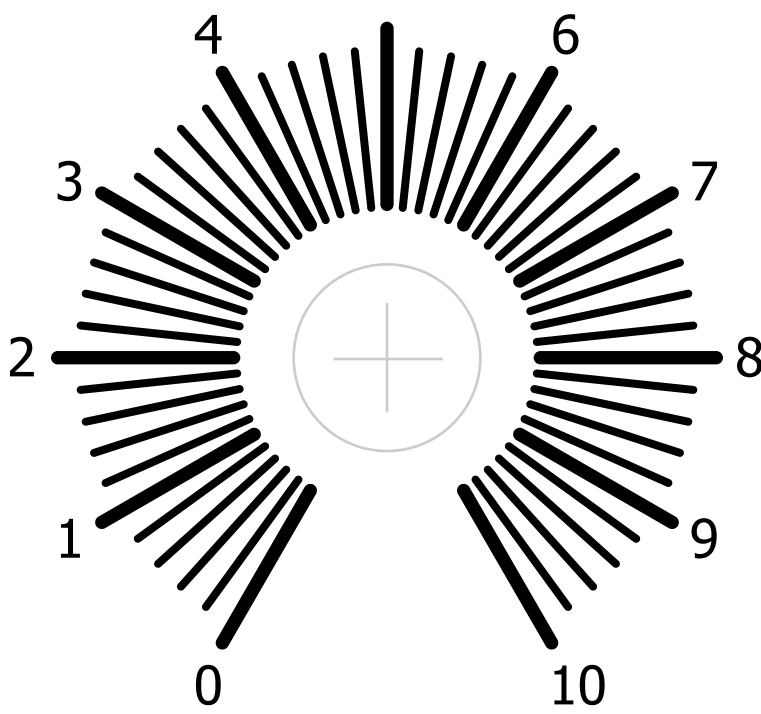




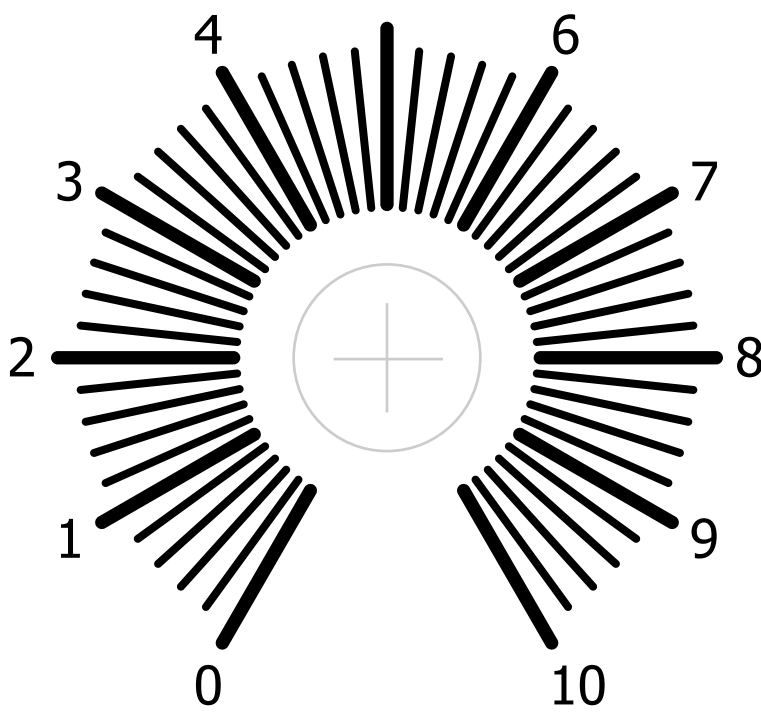
VCA



VCA 1 Gain



VCA 2 Gain



VCA 1

VCA 2



Response

Response

Log

Log



Linear

Linear

Input

Input



Gain CV In

Gain CV In



Gain CV In

Gain CV In



Out

Out



MFOS

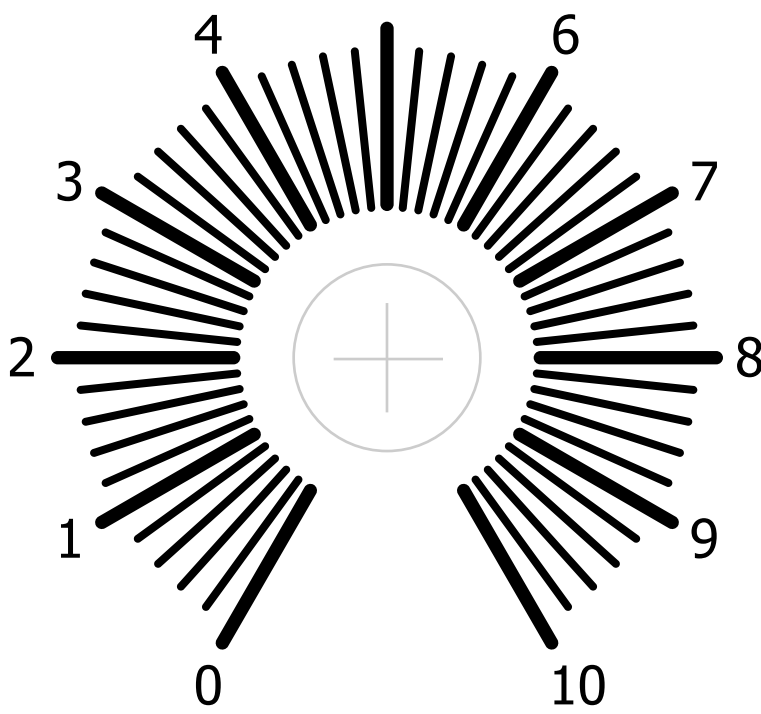




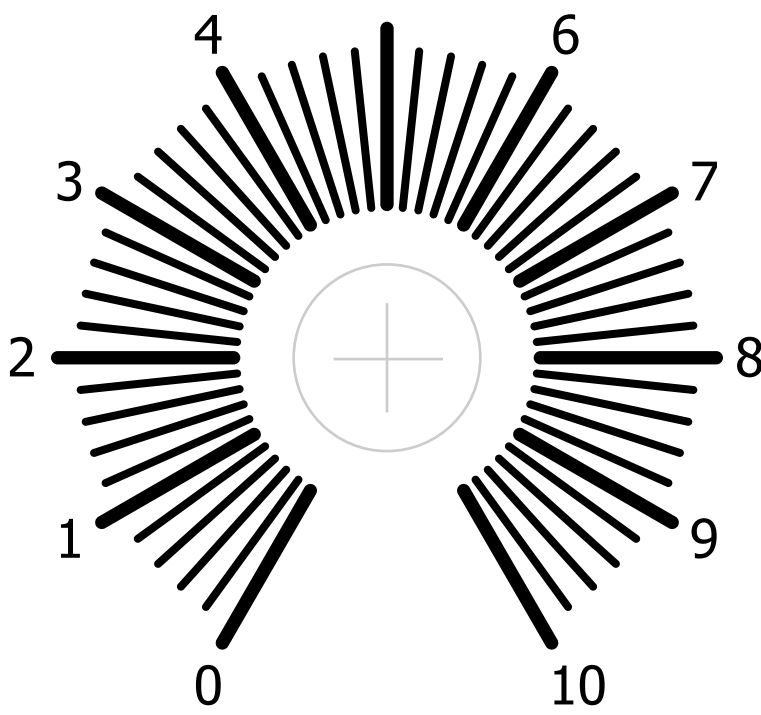
LFO



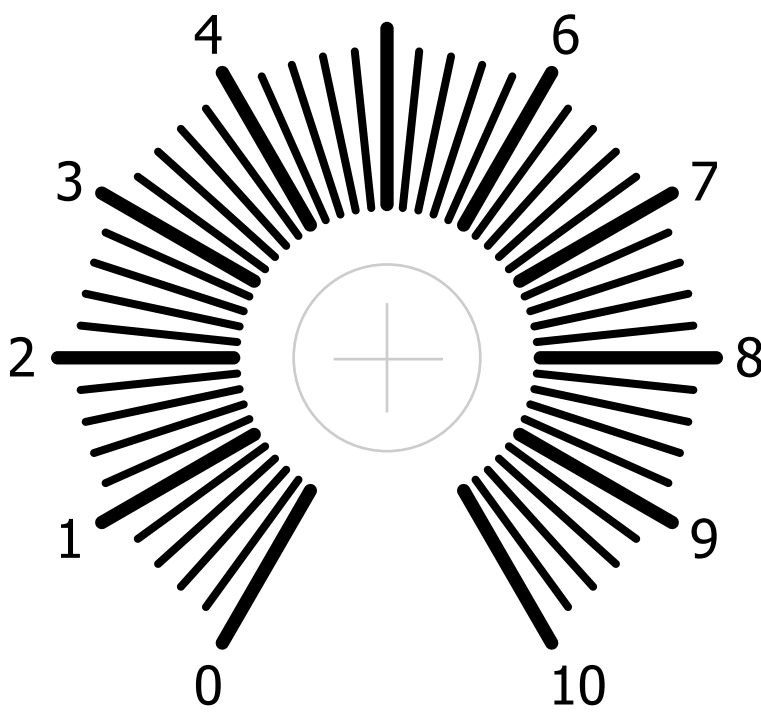
Course



Fine



PWM Width %



Square Out



Sine Out



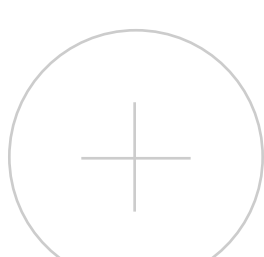
PWM CV In



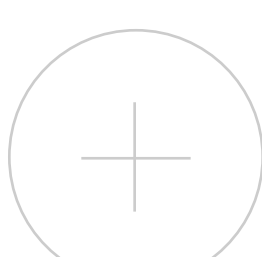
Triangle Out



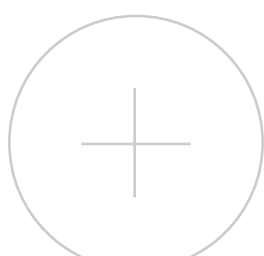
Freq CV In



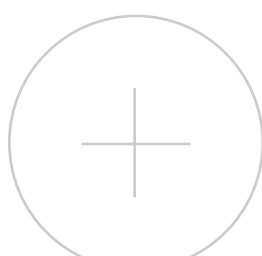
Saw Out



Freq CV In



Ramp Out



MFOS





MULTI



Link 1/2

Link 3/4

Off

Off

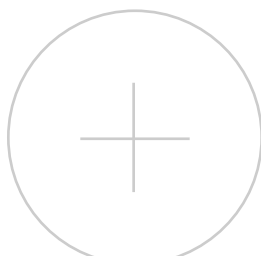
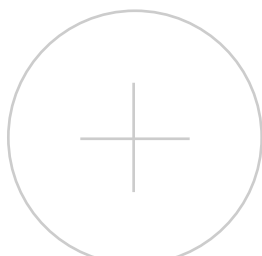


On

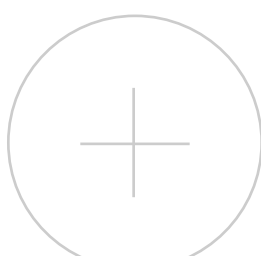
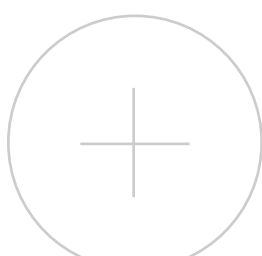
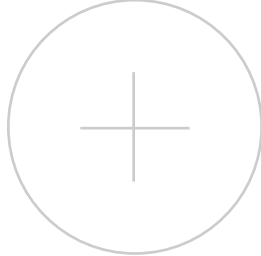
On



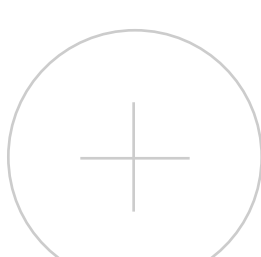
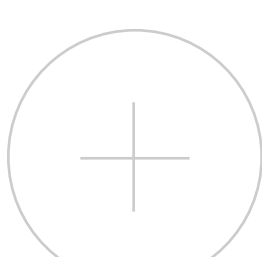
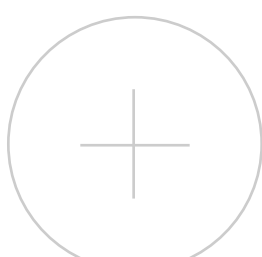
Bank 1



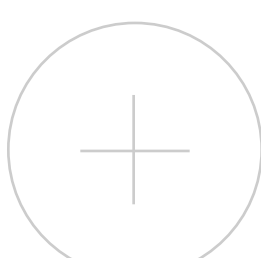
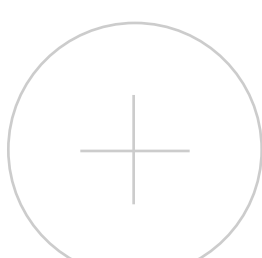
Bank 2



Bank 3



Bank 4

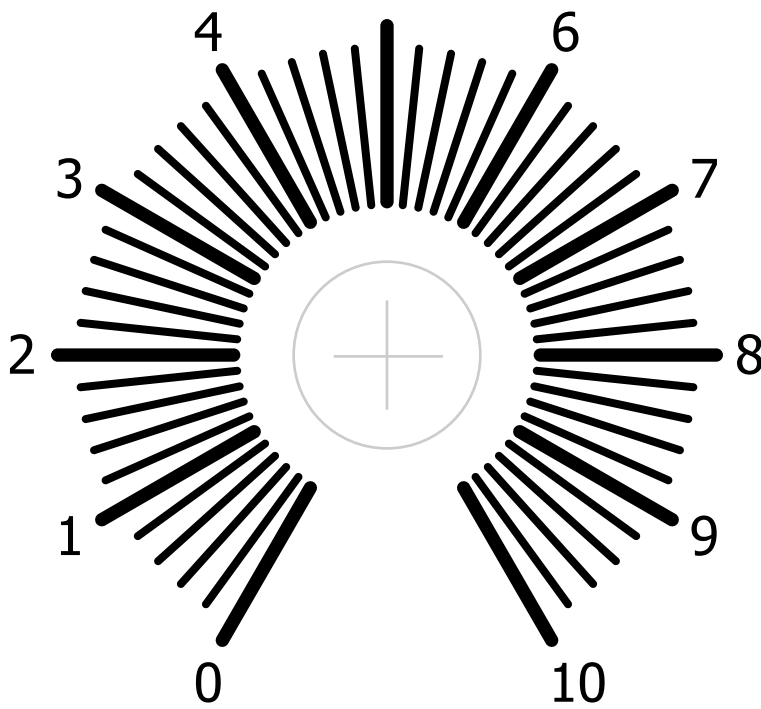




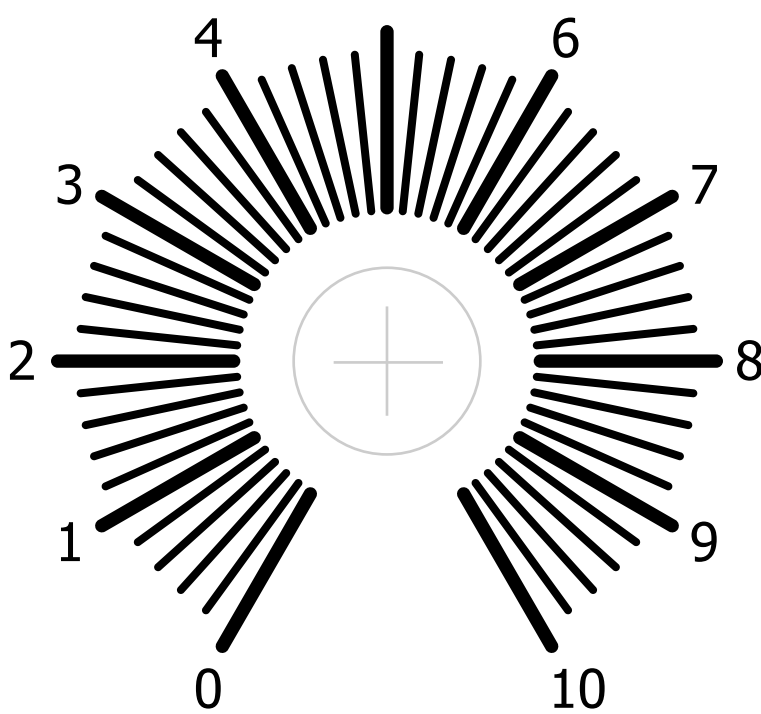
LEVEL



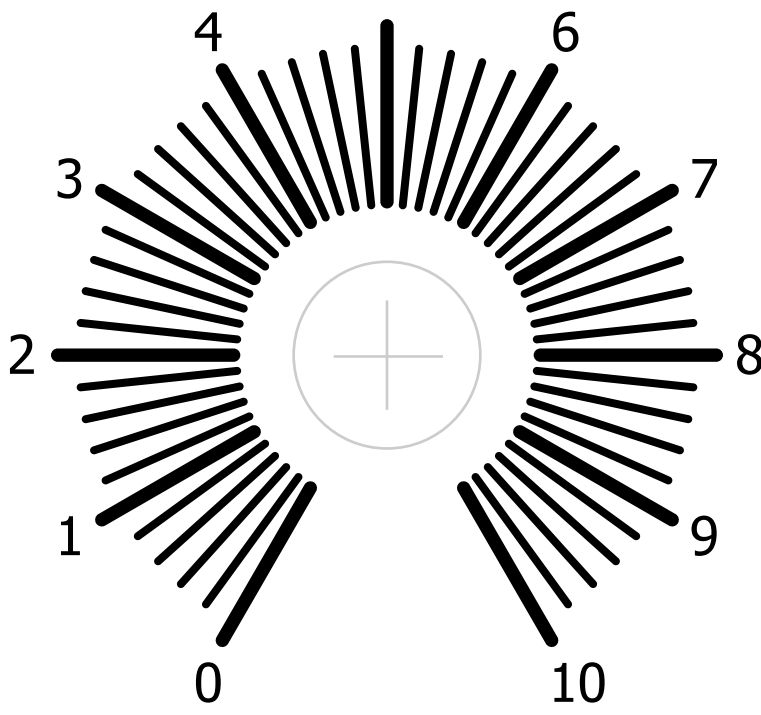
One



Two



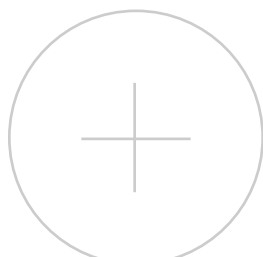
Three



One In



One Out



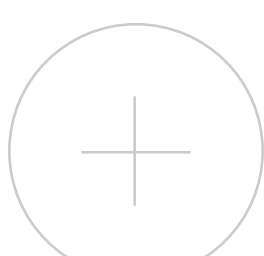
Two In



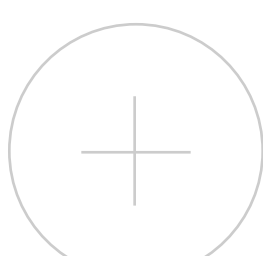
Two Out



Three In



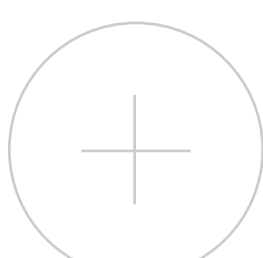
Three Out



Four In



Four Out



LukeLabs

