```
1      /*******************************************************
2      CL2bm1 Test Menus for UART0_Dr1 R.Oliva
3      L&R Ingeniería 2010-2017
4      Project : Test_UART0_Dr1  with AtMega1284P 14.76MHz
5      File:     Tst_UartMenu_main.h
6      Version : 5  (14.76MHz Test system)
7      Date    : 20.11.2017
8      Author  : Rafael Oliva
9      Company : L&R Ingenieria
10     */
11
12     // Definitions required..
13     #define    SEI()           #asm("sei")
14     #define    CLI()           #asm("cli")
15
16     // From 1284P testing 24.7.2010
17     #define OLED       PORTD.5
18     #define BACK_LIGHT  PORTD.6
19
20     // From ADC definitions
21     #define FIRST_ADC_INPUT  0
22     #define LAST_ADC_INPUT   7
23     #define ADC_VREF_TYPE    0x00
24
25     void Initialize_CL2_simple(void);      // 19.11.2017 Simple CL2bm1 initialize with 1284P
26
27     void External_Timer_Init(void);        // 19.12.2017 CKOUT driven Interrupt
```