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CL2bm1 Test Menus for UART1_Dr1 R.Oliva
L&R Ingeniería 2010-2018
Project : Test_UART1_Dr1 with AtMega1284P 14.76MHz
File:    Tst_Uart1Menu_main.h
Version : 1 (14.76MHz Test system)
Date    : 30.01.2018
Author   : Rafael Oliva
Company  : L&R Ingenieria
*/
```

```
// Definitions required..
```

```
#define SEI()      #asm("sei")
#define CLI()      #asm("cli")
```

```
// From 1284P testing 24.7.2010
```

```
#define OLED       PORTD.5
#define BACK_LIGHT PORTD.6
```

```
// From ADC definitions
```

```
#define FIRST_ADC_INPUT 0
#define LAST_ADC_INPUT  7
#define ADC_VREF_TYPE    0x00
```

```
void Initialize_CL2_simple(void);      // 19.11.2017 Simple CL2bm1 initialize with 1284P
```

```
void External_Timer_Init(void);        // 19.12.2017 CKOUT driven Interrupt
```