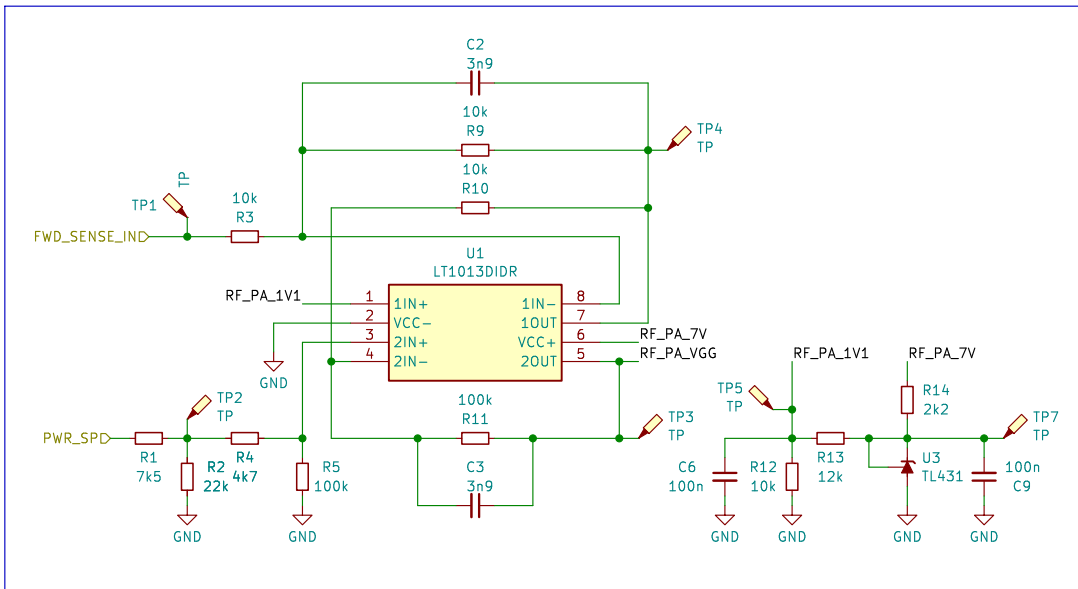
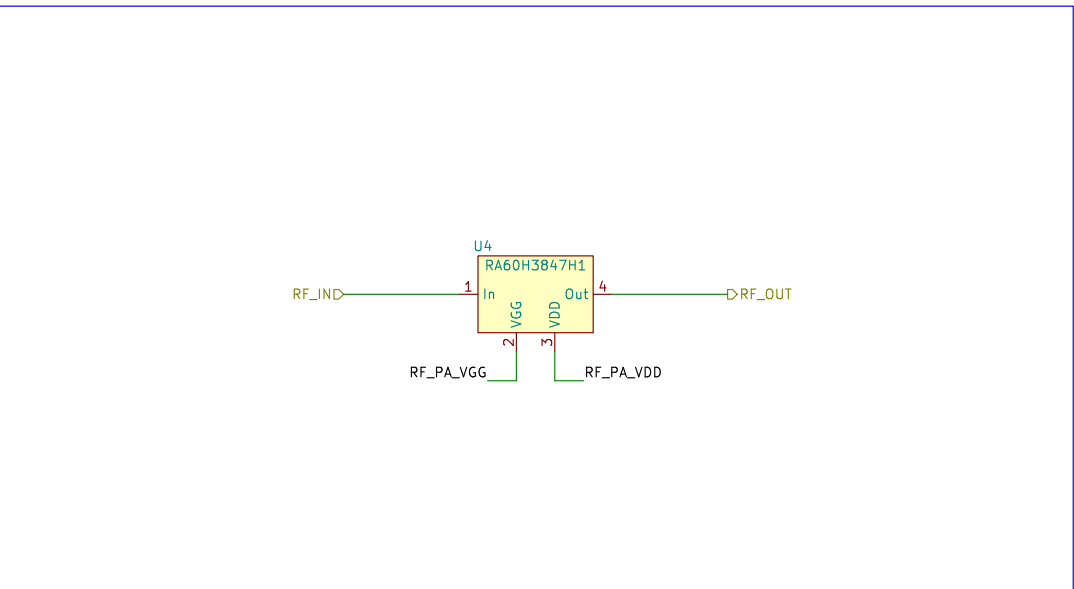


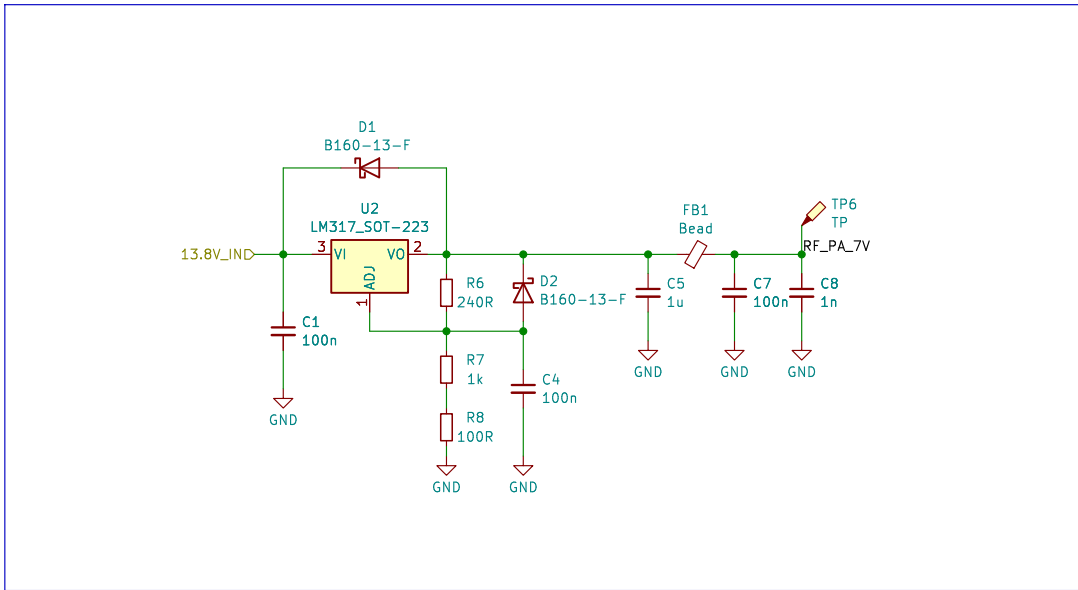
## ALC



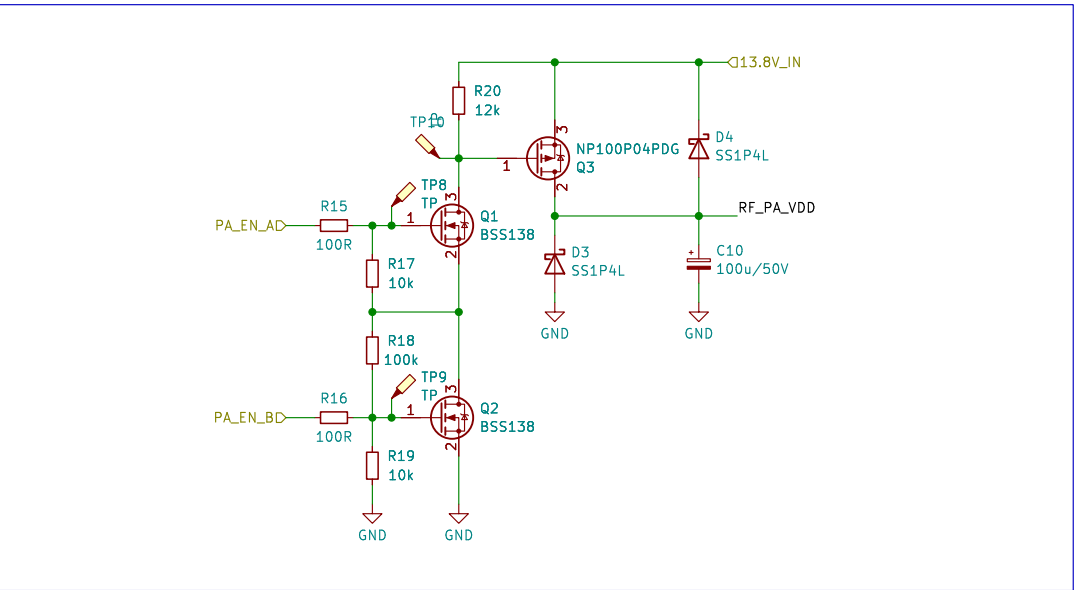
## RF PA



## Voltage reg.



## Supply switching



Wojciech Kaczmarek, SP5WWP

**M17 Project**

Sheet: /RF PA/

File: rf\_pa.kicad\_sch

**Title: M17 Remote Radio Unit – RF board**

Size: A3

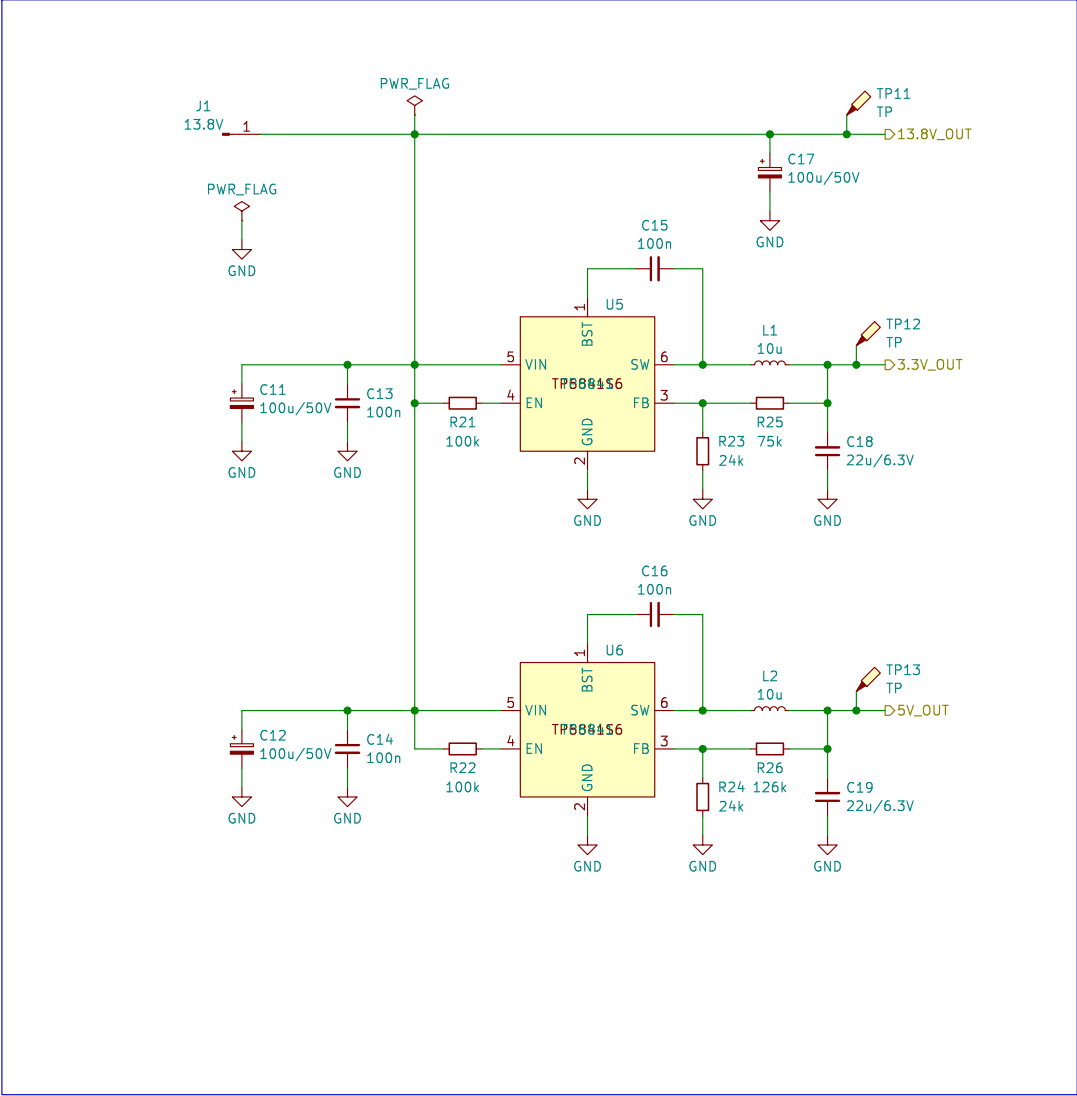
Date: 15-10-2023

Rev: A

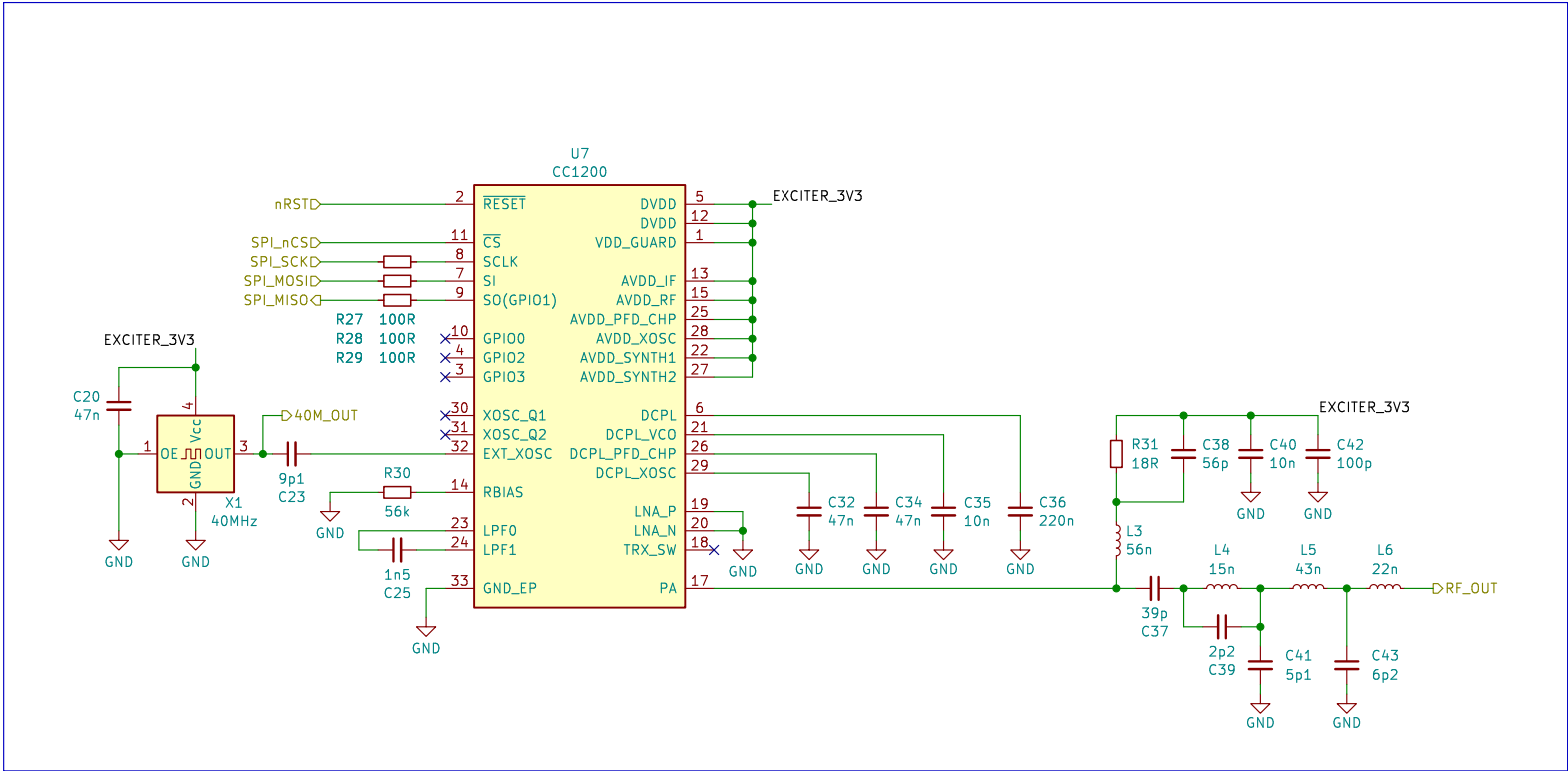
KiCad E.D.A. kicad 7.0.8

Id: 2/11

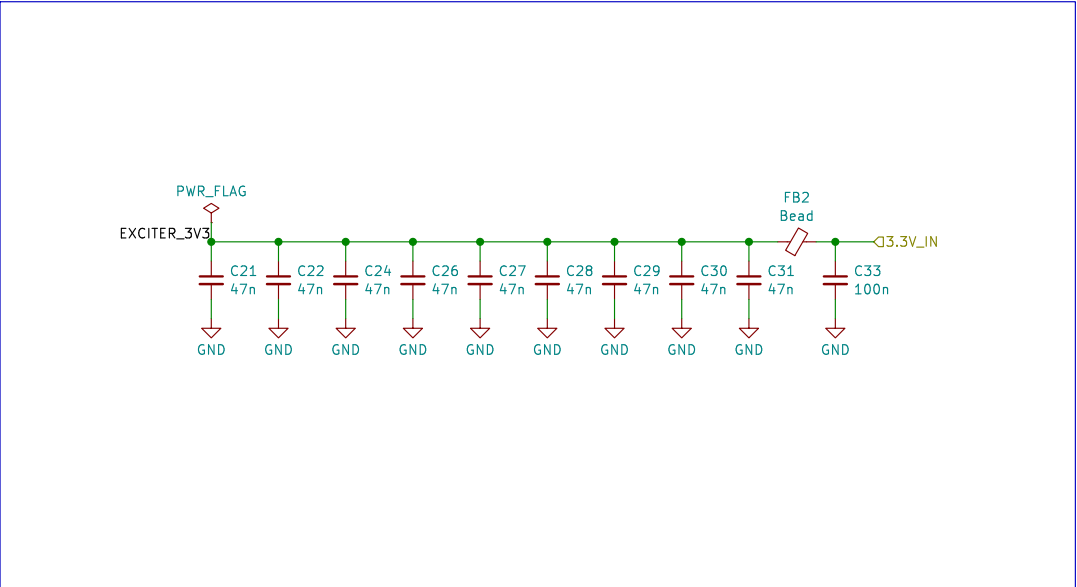
Voltage reg.

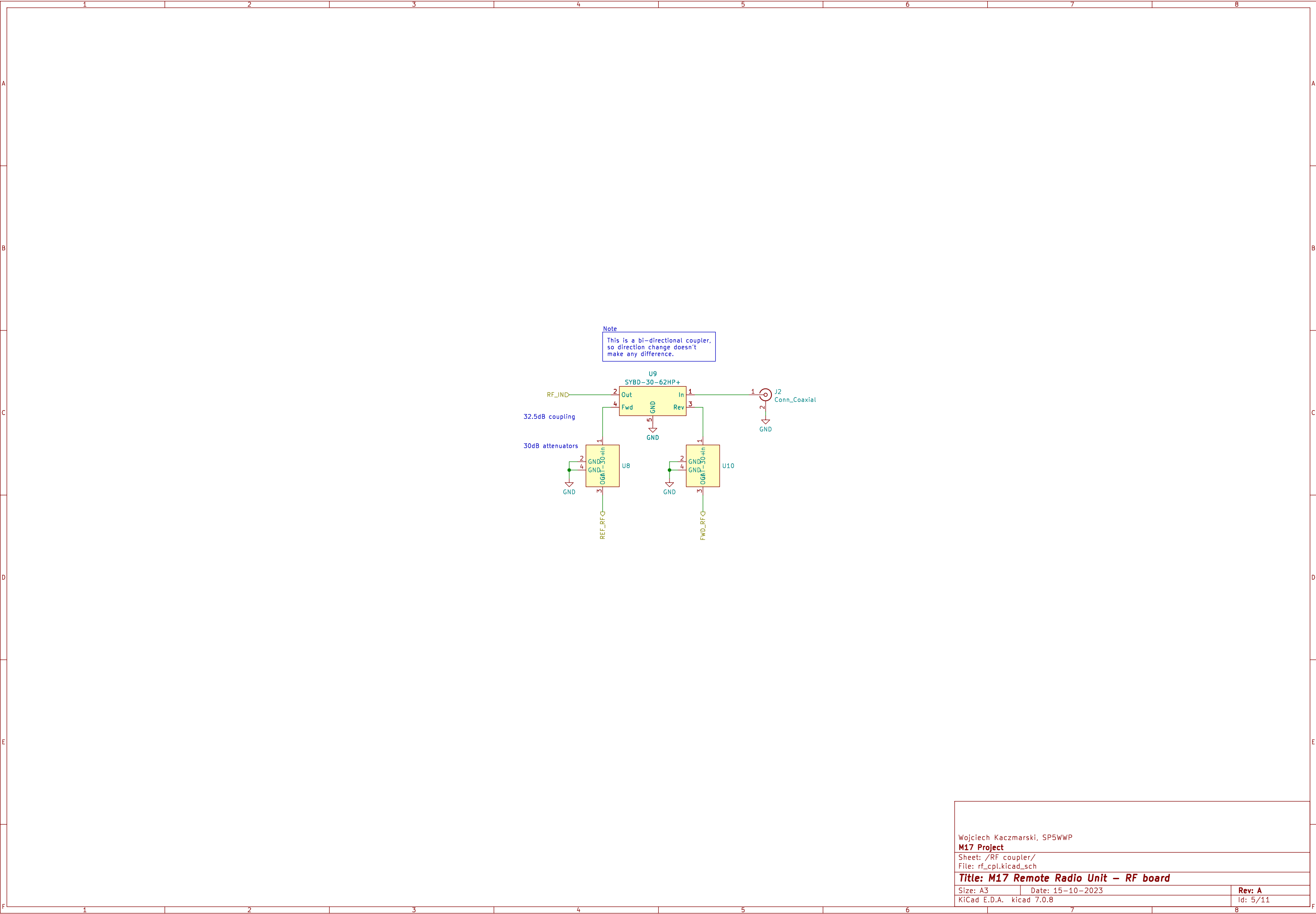


Exciter

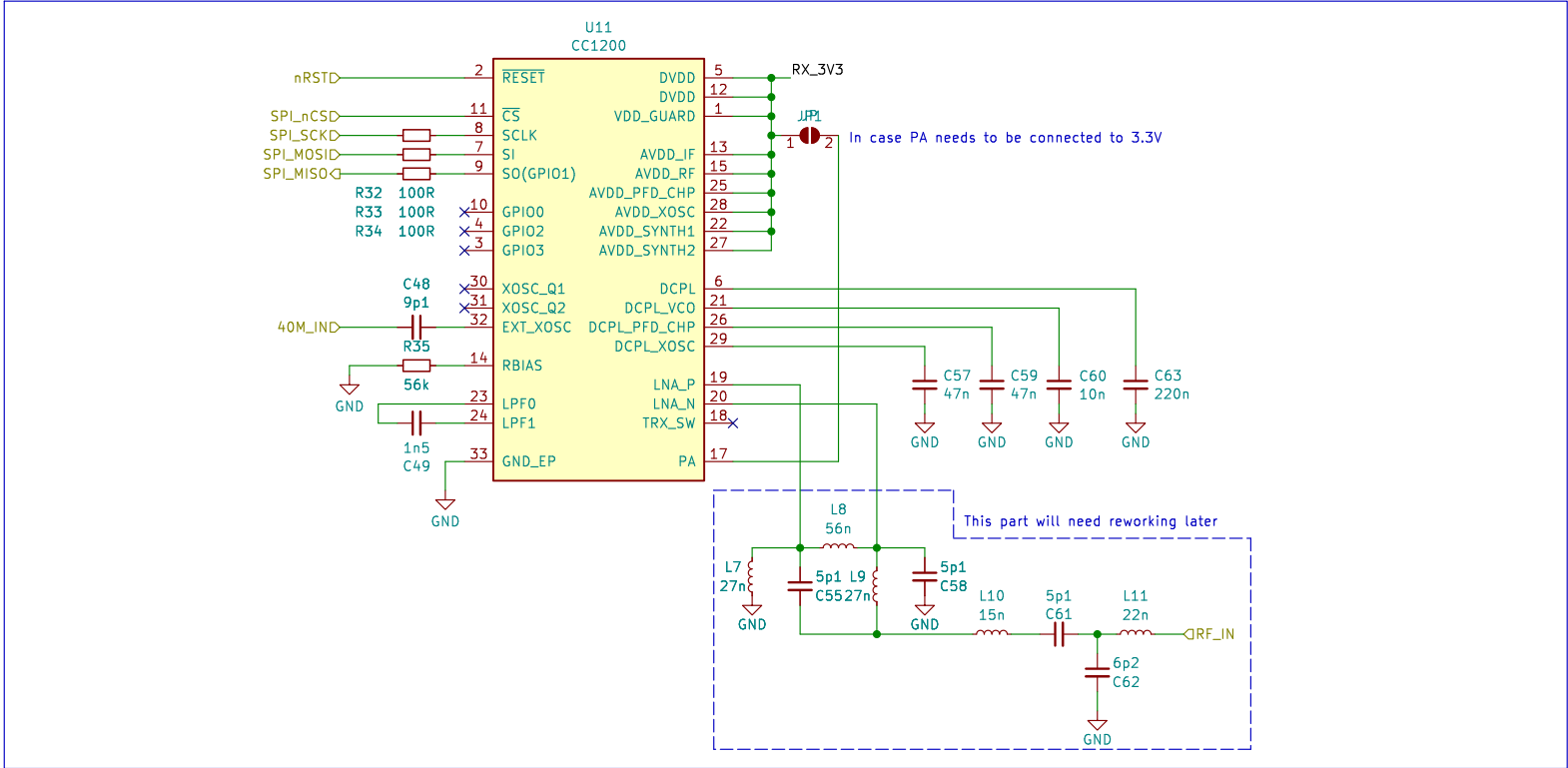


Decoupling

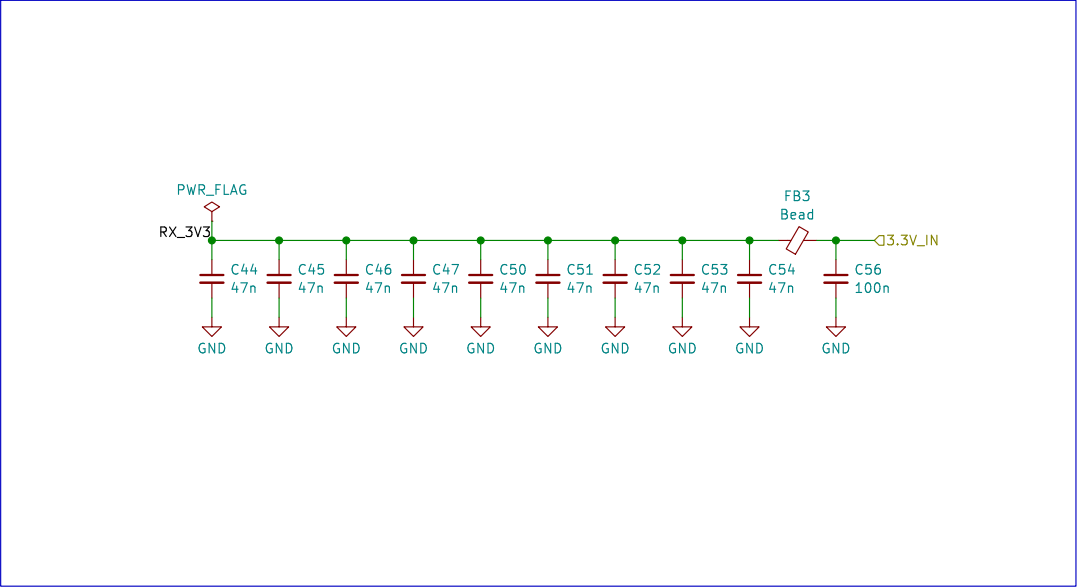




Receiver



Decoupling



Wojciech Kaczmarek, SP5WWP

**M17 Project**

Sheet: /Receiver/

File: rx.kicad\_sch

**Title: M17 Remote Radio Unit – RF board**

Size: A3

Date: 15-10-2023

Rev: A

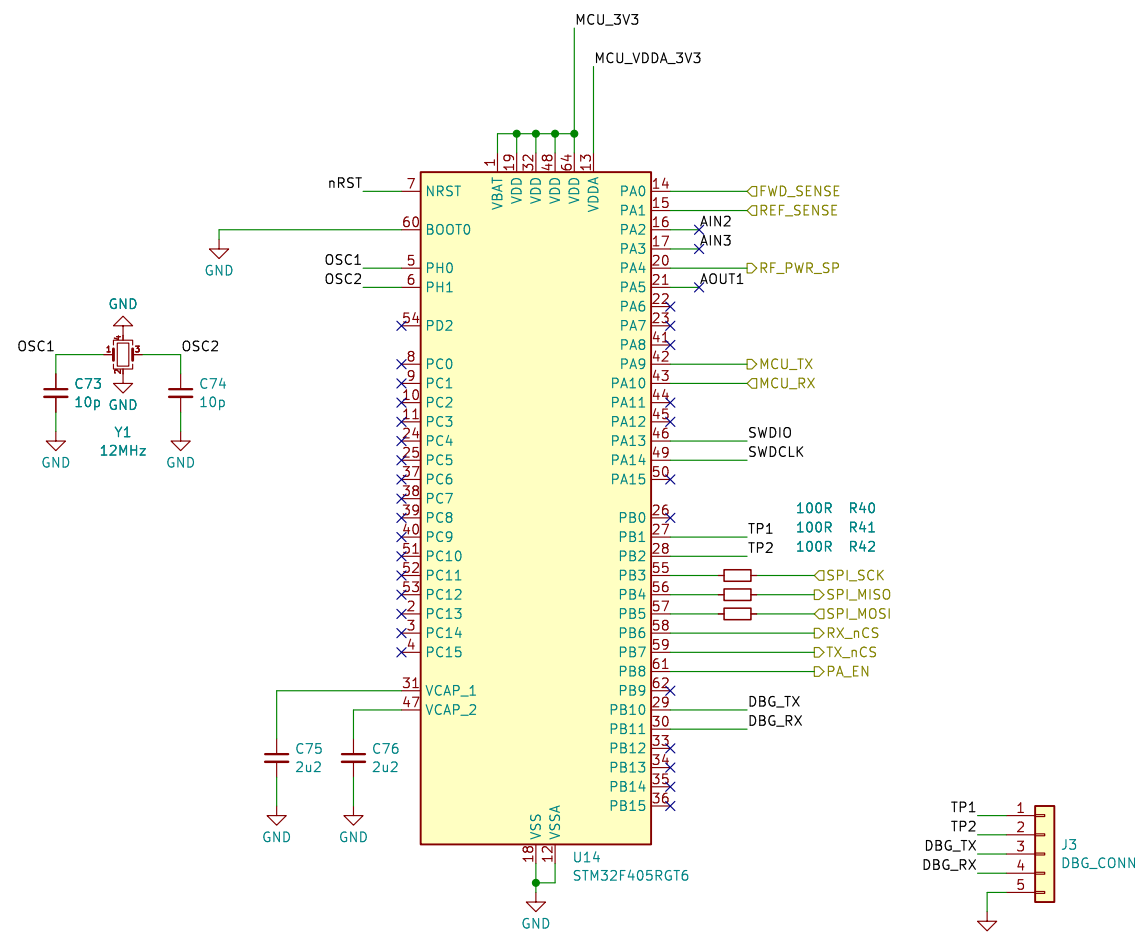
KiCad E.D.A. kicad 7.0.8

Id: 6/11

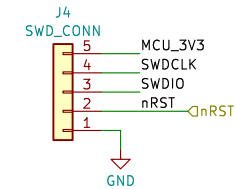
[illegible]

The schematic diagram illustrates a power supply decoupling circuit for a PWR\_FLAG signal. The circuit begins with a 5V\_INDC input, which is connected to a series resistor R64 and a series inductor L64. A parallel combination of a 1uF capacitor (C64) and a 100nF capacitor (C67) is connected to ground (GND). The signal then passes through a series inductor L65 and a parallel combination of a 1nF capacitor (C69) and a 100nF capacitor (C67) to ground. The final output is connected to the PWR\_FLAG pin of the PWR\_FLAG component.

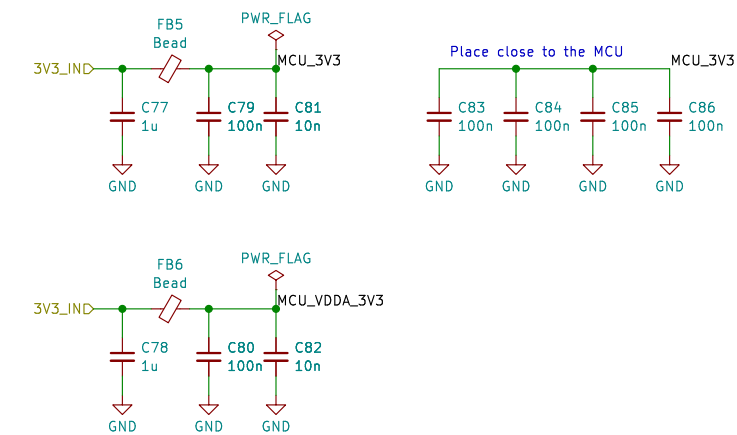
## MCU



## SWD



## Decoupling



Wojciech Kaczmarek, SP5WWP

**M17 Project**

Sheet: /MCU/

File: mcu.kicad\_sch

**Title: M17 Remote Radio Unit – RF board**

Size: A3

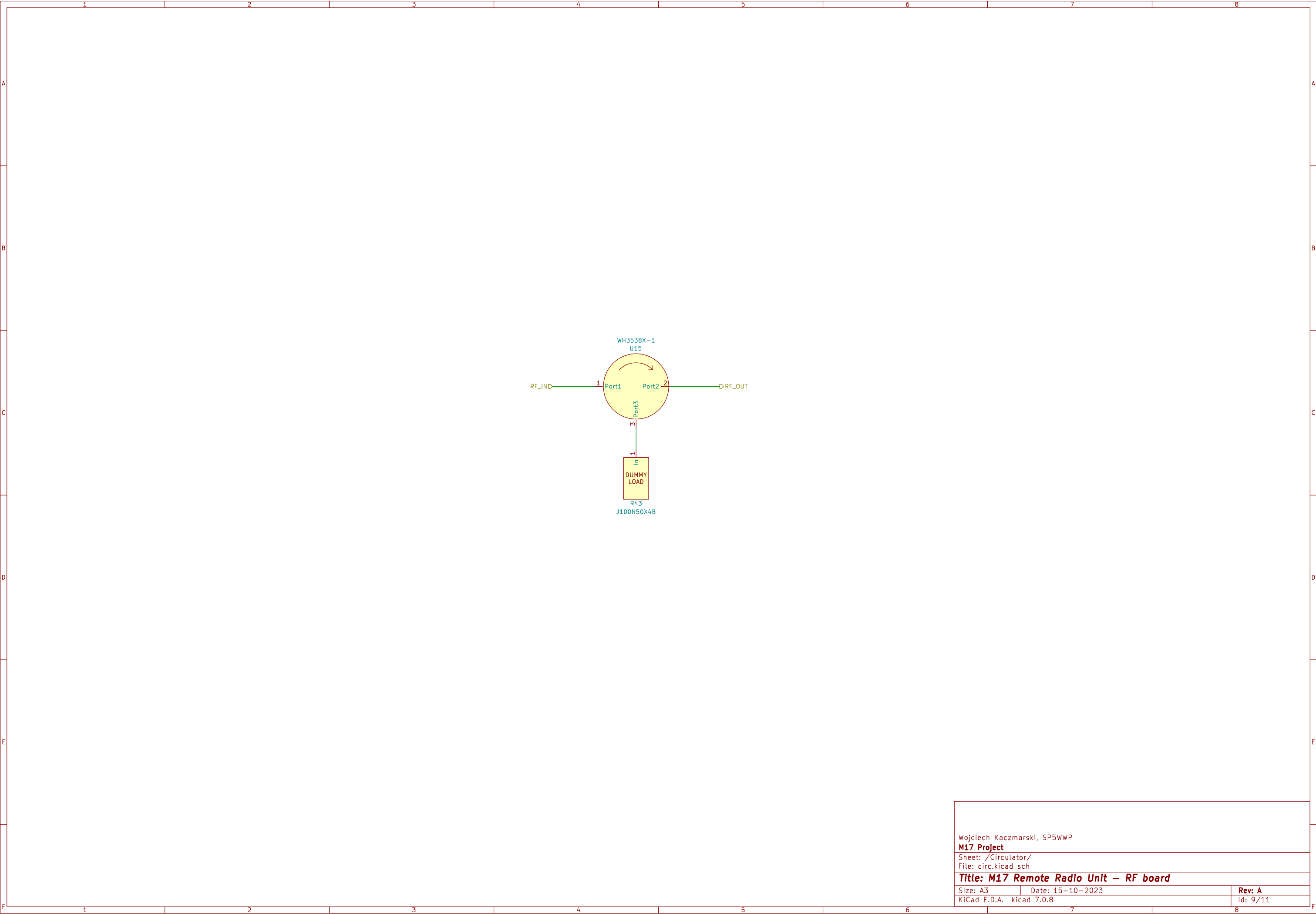
Date: 15-10-2023

Rev: A

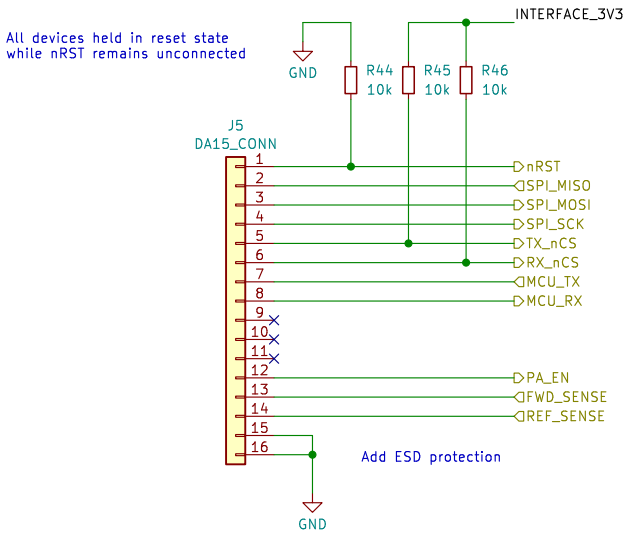
KiCad E.D.A. kicad 7.0.8

Id: 8/11

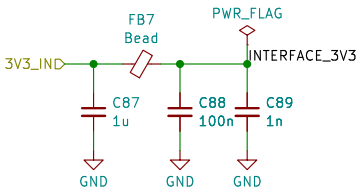




External connector



Decoupling



Wojciech Kaczmarek, SP5WWP

**M17 Project**

Sheet: /Interface/

File: interface.kicad\_sch

**Title: M17 Remote Radio Unit – RF board**

Size: A3

Date: 15-10-2023

Rev: A

KiCad E.D.A. kicad 7.0.8

Id: 10/11

