

MCUXSDKMIMXRT117XBRN

MCUXpresso SDK Release Notes for MIMXRT1170-EVKB

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Release notes

Document information

Information	Content
Keywords	MCUXpresso SDK, Release Notes, MIMXRT1170-EVKB
Abstract	This document describes the MCUXpresso SDK release notes for MIMXRT1170-EVKB.



1 Overview

The MCUXpresso SDK is a comprehensive software enablement package designed to simplify and accelerate application development with Arm Cortex-M-based devices from NXP, including its general purpose, crossover and Bluetooth-enabled MCUs. MCUXpresso SW and Tools for DSC further extends the SDK support to current 32-bit Digital Signal Controllers. The MCUXpresso SDK includes production-grade software with integrated RTOS (optional), integrated enabling software technologies (stacks and middleware), reference software, and more.

In addition to working seamlessly with the MCUXpresso IDE, the MCUXpresso SDK also supports and provides example projects for IAR, KEIL, and GCC with Cmake. Support for the MCUXpresso Config Tools allows easy cloning of existing SDK examples and demos, allowing users to leverage the existing software examples provided by the SDK for their own projects.

Underscoring our commitment to high quality, the MCUXpresso SDK is MISRA compliant and checked with Coverity static analysis tools. For details on MCUXpresso SDK, see [MCUXpresso-SDK: Software Development Kit for MCUXpresso](#).

2 MCUXpresso SDK

As part of the MCUXpresso software and tools, MCUXpresso SDK is the evolution of Kinetis SDK, includes support for LPC, DSC, and i.MX System-on-Chip (SoC). The same drivers, APIs, and middleware are still available with support for Kinetis, LPC, DSC, and i.MX silicon. The MCUXpresso SDK adds support for the MCUXpresso IDE, an Eclipse-based toolchain that works with all MCUXpresso SDKs. Easily import your SDK into the new toolchain to access to all of the available components, examples, and demos for your target silicon. In addition to the MCUXpresso IDE, support for the MCUXpresso Config Tools allows easy cloning of existing SDK examples and demos, allowing users to leverage the existing software examples provided by the SDK for their own projects.

In order to maintain compatibility with legacy Freescale code, the filenames and source code in MCUXpresso SDK containing the legacy Freescale prefix FSL has been left as is. The FSL prefix has been redefined as the NXP Foundation Software Library.

3 Development tools

The MCUXpresso SDK is compiled and tested with these development tools:

- Keil MDK, version is 5.38.a
- MCUXpresso IDE, version is 11.8.0
- GCC Arm Embedded, version is 12.2.Rel1
- IAR Embedded Workbench for Arm, version is 9.40

Note: *There are some issues when using Segger J-Link Version 7.00 to debug CM4/CM7 flash related targets. NXP is working with Segger to get this issue fixed in the future. However, if you want to use the JLink probe to debug flash related targets before the issue is fixed, contact NXP for a temporary patch.*

4 Supported development system

This release supports boards and devices listed in [Table 1](#). The boards and devices in bold were tested in this release.

Table 1. Supported boards and devices

Development boards	MCU devices
MIMXRT1170-EVKB	MIMXRT1176DVMAA , MIMXRT1176CVM8A, MIMXRT1176AVM8A, MIMXRT1171DVMAA , MIMXRT1171AVM8A, MIMXRT1171CVM8A, MIMXRT1173CVM8A , MIMXRT1175DVMAA, MIMXRT1175AVM8A, MIMXRT1175CVM8A , MIMXRT1172DVMAA, MIMXRT1172AVM8A, MIMXRT1172CVM8A

5 MCUXpresso SDK release package

The MCUXpresso SDK release package content is aligned with the silicon subfamily it supports. This includes the boards, devices, documentation, and middleware.

5.1 Device support

The device folder contains the whole software enablement available for the specific System-on-Chip (SoC) subfamily. This folder includes clock-specific implementation, device register header files, device register feature header files, and the system configuration source files. Included with the standard SoC support are folders containing peripheral drivers, toolchain support, and a standard debug console. The device-specific header files provide a direct access to the microcontroller peripheral registers. The device header file provides an overall SoC memory mapped register definition. The folder also includes the feature header file for each peripheral on the microcontroller. The toolchain folder contains the startup code and linker files for each supported toolchain. The startup code efficiently transfers the code execution to the main() function.

5.1.1 Board support

The boards folder provides the board-specific demo applications, driver examples, and middleware examples.

5.1.2 Demo application and other examples

The demo applications demonstrate the usage of the peripheral drivers to achieve a system level solution. Each demo application contains a readme file that describes the operation of the demo and required setup steps. The driver examples demonstrate the capabilities of the peripheral drivers. Each example implements a common use case to help demonstrate the driver functionality.

5.2 CMSIS DSP Library

The MCUXpresso SDK is shipped with the standard CMSIS development pack, including the prebuilt libraries.

5.3 Operating system

5.3.1 FreeRTOS

Real-time operating system for microcontrollers from Amazon.

5.3.2 Azure RTOS

Azure RTOS middleware (FileX, GUIX, LevelX, NetX Duo, USBX) and the ThreadX RTOS are optional components in MCUXpresso SDK. Source code for these items is included in the *rtos/azure-rtos* directory, and examples for the target board are located in *boards/evkbimxrt1070/azure_rtos_examples*.

For more information, see the *MCUXpresso SDK Azure RTOS Release Notes* and *Getting Started with MCUXpresso SDK for Azure RTOS*.

5.4 Middleware

5.4.1 AWS IoT

Amazon Web Service (AWS) IoT Core SDK.

5.4.2 Azure RTOS FileX

A file system based on Azure RTOS.

5.4.3 Azure RTOS GUIX

A GUI library based on Azure RTOS.

5.4.4 Azure RTOS IoT

A software package that connects to the IoT Hub through Azure RTOS.

5.4.5 Azure RTOS LevelX

NOR/NAND Flash wear leveling component

5.4.6 Azure RTOS NetX Duo

A network protocol stack based on Azure RTOS.

5.4.7 Azure RTOS ThreadX

Azure RTOS ThreadX.

5.4.8 Azure RTOS USBX

A USB library based on Azure RTOS.

5.4.9 canopen

MicroCANopen Stack from Embedded Solutions Academy

5.4.10 Crank Storyboard GUI

Crank Storyboard GUI Engine from Crank Software.

5.4.11 Essential Audio Processing Library

Audio processing blocks for enhancing the tonal and spatial perception of sound in audio applications.

5.4.12 eIQ

The package contains several example applications using the eIQ TensorFlow Lite for Microcontrollers library.
eIQ machine learning SDK containing:

- Arm CMSIS-NN library (neural network kernels optimized for Cortex-M cores)
- Inference engines:
 - TensorFlow Lite Micro
 - DeepView RT
- Example code for TensorFlow Lite Micro, Glow, and DeepView RT

5.4.13 Embedded Wizard GUI

The MCUXpresso SDK is pre-integrated with the Embedded Wizard GUI framework. The integration demonstrates state-of-the-art GUI applications by using hardware accelerated graphic operations (where applicable, otherwise pure software). Embedded Wizard GUI from TARA Systems.

5.4.14 emWin

The MCUXpresso SDK is pre-integrated with the SEGGER emWin GUI middleware. The AppWizard provides developers and designers with a flexible tool to create stunning user interface applications, without writing any code.

5.4.15 Fatfs

The FatFs file system is integrated with the MCUXpresso SDK and can be used to access either the SD card or the USB memory stick when the SD card driver or the USB Mass Storage Device class implementation is used.

5.4.16 FreeMASTER

FreeMASTER communication driver for 32-bit platforms.

5.4.17 IEC60730B Safety Library

NXP IEC60730B Safety Library

5.4.18 IoT Sensing Software Development Kit (ISSDK)

The IoT Sensing Software Development Kit (ISSDK) is the embedded software framework enabling the NXP digital and analog sensors for IoT applications. ISSDK combines a set of robust sensor drivers and algorithms along with example applications that allow users to get started with using NXP IoT motion & pressure sensors. ISSDK is being offered as a middleware component in MCUXpresso SDK.

5.4.19 JPEG library

JPEG library

5.4.20 littlefs

LittleFS filesystem stack

5.4.21 LVGL

LVGL Open Source Graphics Library

5.4.22 lwIP

The lwIP TCP/IP stack is pre-integrated with MCUXpresso SDK and runs on top of the MCUXpresso SDK Ethernet driver with Ethernet-capable devices/boards.

For details, see the *lwIP TCP/IP Stack and MCUXpresso SDK Integration User's Guide* (document MCUXSDKLWIPUG).

lwIP is a small independent implementation of the TCP/IP protocol suite.

5.4.23 Maestro Audio Framework for MCU

Maestro Audio Framework library for MCU

5.4.24 Motor Control Software (ACIM, BLDC, PMSM)

Motor control examples.

5.4.25 mbedTLS

mbedtls SSL/TLS library

5.4.26 MCU Boot

MCU Bootloader source code.

5.4.27 MMCAU

MMCAU

5.4.28 multicore

Multicore Software Development Kit

5.4.29 NAND Flash Management Stack

NAND Flash Management Stack.

5.4.30 NXP Wi-Fi

The MCUXpresso SDK provides driver for NXP Wi-Fi external modules. The Wi-Fi driver is integrated with LWIP TCP/IP stack and demonstrated with several network applications (iperf and AWS IoT).

For more information, see *Getting Started with NXP based Wireless Modules and i.MX RT Platform Running on RTOS* (document: UM11441).

5.4.31 Openh264

H.264 Codec Library

5.4.32 openvg

Openvg library

5.4.33 sdmmc stack

The SDMMC software is integrated with MCUXpresso SDK to support SD/MMC/SDIO standard specification. This also includes a host adapter layer for bare-metal/RTOS applications.

5.4.34 Simple Open EtherCAT Master

Simple Open EtherCAT Master (SOEM) is an open source EtherCAT master stack that is used to write custom EtherCAT Master applications. For more information on how to use SOEM, see the Getting Started with MCUXpresso SDK for SOEM document.

5.4.35 USB Host, Device, OTG Stack

See the MCUXpresso SDK USB Stack User's Guide (document MCUXSDKUSBSUG) for more information.

5.4.36 USB Type-C Power Delivery Authentication

USB Type-C Power Delivery Authentication

5.4.37 VG-Lite GPU Library

vglite lib.

5.4.38 Voice intelligent technology library

Voice Intelligent Technology (VIT) Library.

5.4.39 Wireless EdgeFast Bluetooth PAL

For more information, see the MCUXpresso SDK EdgeFast Bluetooth Protocol Abstraction Layer User's Guide.

6 Release contents

[Table 2](#) provides an overview of the MCUXpresso SDK release package contents and locations.

Table 2. MCUXpresso SDK release package contents and locations

Deliverable	Location
Boards	INSTALL_DIR/boards
Demo Applications	INSTALL_DIR/boards/<board_name>/demo_apps
Driver Examples	INSTALL_DIR/boards/<board_name>/driver_examples
eIQ examples	INSTALL_DIR/boards/<board_name>/eIQ_examples
Board Project Template for MCUXpresso IDE NPW	INSTALL_DIR/boards/<board_name>/project_template
Driver, SoC header files, extension header files and feature header files, utilities	INSTALL_DIR/devices/<device_name>
CMSIS drivers	INSTALL_DIR/devices/<device_name>/cmsis_drivers
Peripheral drivers	INSTALL_DIR/devices/<device_name>/drivers
Toolchain linker files and startup code	INSTALL_DIR/devices/<device_name>/<toolchain_name>
Utilities such as debug console	INSTALL_DIR/devices/<device_name>/utilities

Table 2. MCUXpresso SDK release package contents and locations...continued

Deliverable	Location
Device Project Template for MCUXpresso IDE NPW	INSTALL_DIR/devices/<device_name>/project_template
CMSIS Arm Cortex-M header files, DSP library source	INSTALL_DIR/CMSIS
Components and board device drivers	INSTALL_DIR/components
RTOS	INSTALL_DIR/rtos
Release Notes, Getting Started Document and other documents	INSTALL_DIR/docs
Tools such as shared cmake files	INSTALL_DIR/tools
Middleware	INSTALL_DIR/middleware

7 MISRA compliance

All MCUXpresso SDK drivers comply to MISRA 2012 rules with exceptions in [Table 3](#).

Table 3. MISRA exception rules

Exception rules	Description
Directive 4.4	Sections of code should not be commented out.
Directive 4.5	Identifiers in the same name space with overlapping visibility should be typographically unambiguous.
Directive 4.6	Typedefs that indicate size and signedness should be used in place of the basic numerical types.
Directive 4.8	If a pointer to a structure or union is never dereferenced within a translation unit, then the implementation of the object should be hidden.
Directive 4.9	A function should be used in preference to a function-like macro where they are interchangeable.
Directive 4.13	Functions which are designed to provide operations on a resource should be called in an appropriate sequence.
Rule 1.2	Language extensions should not be used.
Rule 2.3	A project should not contain unused type declarations.
Rule 2.4	A project should not contain unused tag declarations.
Rule 2.5	A project should not contain unused macro declarations.
Rule 2.6	A function should not contain unused label declarations.
Rule 2.7	There should be no unused parameters in functions.
Rule 4.2	Trigraphs should not be used.
Rule 5.1	External identifiers shall be distinct.
Rule 5.4	Macro identifiers shall be distinct.
Rule 5.9	Identifiers that define objects or functions with internal linkage should be unique.
Rule 8.7	Functions and objects should not be defined with external linkage if they are referenced in only one translation unit.

Table 3. MISRA exception rules...continued

Exception rules	Description
Rule 8.9	An object should be defined at block scope if its identifier only appears in a single function.
Rule 8.11	When an array with external linkage is declared, its size should be explicitly specified.
Rule 8.13	A pointer should point to a const-qualified type whenever possible.
Rule 10.5	The value of an expression should not be cast to an inappropriate essential type.
Rule 11.4	A conversion should not be performed between a pointer to object and an integer type.
Rule 11.5	A conversion should not be performed from pointer to void into pointer to object.
Rule 12.1	The precedence of operators within expressions should be made explicit.
Rule 12.3	The comma operator should not be used.
Rule 12.4	Evaluation of constant expressions should not lead to unsigned integer wrap-around.
Rule 13.3	A full expression containing an increment (++) or decrement (–) operator should have no other potential side effects other than that caused by the increment or decrement operator.
Rule 15.4	There should be no more than one break or go to statement used to terminate any iteration statement.
Rule 17.5	The function argument corresponding to a parameter declared to have an array type shall have an appropriate number of elements.
Rule 17.8	A function parameter should not be modified.
Rule 19.2	The union keyword should not be used.
Rule 20.1	#include directives should only be preceded by preprocessor directives or comments.
Rule 20.10	The # and ## preprocessor operators should not be used.
Rule 21.1	#define and #undef shall not be used on a reserved identifier or reserved macro name.
Rule 21.2	A reserved identifier or macro name shall not be declared.
Rule 21.12	The exception handling features of <fenv.h> should not be used.

8 Known Issues

8.1 Maximum file path length in Windows 7 operating system

The Windows 7 operating system imposes a 260-character maximum length for file paths. When installing the MCUXpresso SDK, place it in a directory close to the root to prevent file paths from exceeding the maximum character length specified by the Windows operating system. The recommended location is the C:\<folder>.

8.2 New Project Wizard compile failure

The following components request the user to manually select other components that they depend upon in order to compile.

These components depend on several other components and the New Project Wizard (NPW) is not able to decide which one is needed by the user.

Note: *xxx means core variants, such as, cm0plus, cm33, cm4, cm33_nodsp.*

Components:issdk_mag3110, issdk_host, systick, gpio_kinetis, gpio_lpc, issdk_mpl3115, sensor_fusion_agm01, sensor_fusion_agm01_lpc, issdk_mma845x, issdk_mma8491q, issdk_mma865x, issdk_mma9553, and CMSIS_RTOS2. CMSIS_RTOS2, and components which include cache driver, such as enet_qos.

Also for low-level adapter components, currently the different types of the same adapter cannot be selected at the same time.

For example, if there are two types of timer adapters, gpt_adapter and pit_adapter, only one can be selected as timer adapter

in one project at a time. Duplicate implementation of the function results in an error.

Note: *Most of middleware components have complex dependencies and are not fully supported in new project wizard. Adding a middleware component may result in compile failure.*

8.3 CMSIS-PACK svd issue

CMSIS-PACK DFP installation takes a while. When installing cmsis-pack DFP, Keil MDK processes the MCU SVD file. The large size of SVD file takes considerable time to finish this conversion. During the installation, the progress appears stalled. However, it finishes after approximately 20 minutes.

8.4 CMSIS PACK new project compile failure

The generated configuration cannot be applied globally. The components, serial_manager_usb_cdc_virtual and serial_manager_usb_cdc_virtual_xxx (xxx means core variants like cm0plus, cm33, cm4, and cm33_nodsp) are unsupported for new project wizard of CMSIS pack and will lead to compile failure if selected while creating new project(s).

8.5 MCUXpresso IDE limitation

- **Cannot debug cm4 sdram related demos with CMSIS-DAP**
MCUXpresso IDE does not support initialization of sdram when debugging.

8.6 IAR debug limitation

CM4 flash target demos cannot be debugged on IAR with JLINK.

8.7 Extra option required when using CMSIS-DAP to debug

When using CMSIS-DAP to debug CM4 sdram related target in IAR, such as `flexspi_nor_sdram` and `sdram_txt`, an extra option must be specified in the debugger settings. For details, see [Section 8.6](#).

8.8 Cannot add SDK components into FreeRTOS projects

It is not possible to add any SDK components into FreeRTOS project using the MCUXpresso IDE New Project wizard.

8.9 Log output may be mixed in shell/hfp example

When multiple tasks print the log, the serial port terminal output has the probability to appear mixed.

8.10 LE encryption failure causes connection to fail

There can be a corner case when LE link encryption can fail. This occurs when device under test (DUT); RT Bluetooth controller here, while waiting for the response to `LL_SLAVE_FEATURES_REQ`, instead receives the `LL_ENC_REQ` response from a remote device.

This causes deadlock scenario where DUT and remote devices are stuck waiting for response from peer.

8.11 Connection disconnects with 7.5 ms connection interval

When wireless example works as a peripheral, the central role with 7.5 ms connection interval connects to the wireless example. However, when using the 6th/7th/8th central to connect the wireless example, all the previous connections with the previous centrals are disconnected.

When wireless example works as central and the connection interval is 7.5 ms, the 4th peripheral is not scanned.

8.12 a2dp sink demo: Noise may occur when phone plays music with other operations

Noise may occur when phone plays music with other operations.

For example:

- Switches the WIFI network of the phone when playing music
- Switches music when playing the online music

8.13 Wireless EdgeFast_Bluetooth stack is not supported by any kits

Wireless EdgeFast_Bluetooth stack can be downloaded by MIMXRT1170-EVKB-AGM01 and MIMXRT1170-EVKB-OM13790HOST in <https://mcuxpresso.nxp.com/en/welcome>. However, it is not supported in these kits.

8.14 Insufficient heap in ota_demo examples

The ota_demo examples, especially ota_demo_wifi, may run out of heap in the out-of-the-box configuration.

Therefore, increasing the heap size to 200 KB in FreeRTOSConfig.h is recommended.

To increase the heap size, change line 62:

```
#define configTOTAL_HEAP_SIZE ( ( size_t ) ( 200 * 1024 ) )
```

8.15 Edgefast_Bluetooth stack New Project Wizard compile failure

To prevent compile failure, when using Edgefast_Bluetooth New Project Wizard (NPW), some actions are done manually.

1. The EDGEFAST Bluetooth New Project Wizard (NPW) does not support the "no board" option. Therefore, a board must be selected when creating project.
2. The c/c++ library type must be set to "NewlibNano (none)".
3. There is a new region "LITTLEFS_FLASH_region" which should be added in Memory configuration setting GUI.

The region "LITTLEFS_FLASH_region" is split from region "BOARD_FLASH".

The size of the region "LITTLEFS_FLASH_region" should meet the following conditions:

- Condition 1, The size should not be less than 16 kB.
 - Condition 2, The size should be a multiple of the erase page. The multiple should not be less than 4.
4. Memory configuration: The memory region sequence should follow the order below.
 - evkbmimxrt1170_cm7

Table 4. Memory region sequence for evkbmimxrt1170_cm7

Memory region	Location	Region Size
BOARD_FLASH	0x30000000	0xDC0000
LITTLEFS_FLASH_region	0x30DC0000	0x200000
SRAM_OC1	0x20240000	0x80000
SRAM_DTC_cm7	0x20000000	0x40000
SRAM_ITC_cm7	0x00000000	0x40000
SRAM_OC2	0x202C0000	0x40000
NCACHE_REGION	0x20300000	0x40000
SRAM_OC_ECC1	0x20340000	0x10000
SRAM_OC_ECC2	0x20350000	0x10000
BOARD_SDRAM	0x80000000	0x4000000

- evkbmimxrt1170_cm4

Table 5. Memory region sequence for evkbmimxrt1170_cm4

Memory region	Location	Region Size
BOARD_FLASH	0x30000000	0x580000
LITTLEFS_FLASH_region	0x30580000	0x200000
SRAM_OC1	0x20240000	0x80000
SRAM_DTC_cm4	0x20000000	0x40000
SRAM_ITC_cm4	0x00000000	0x40000
SRAM_OC2	0x202C0000	0x40000
NCACHE_REGION	0x20300000	0x40000
SRAM_OC_ECC1	0x20340000	0x10000
SRAM_OC_ECC2	0x20350000	0x10000
BOARD_SDRAM	0x80000000	0x4000000

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