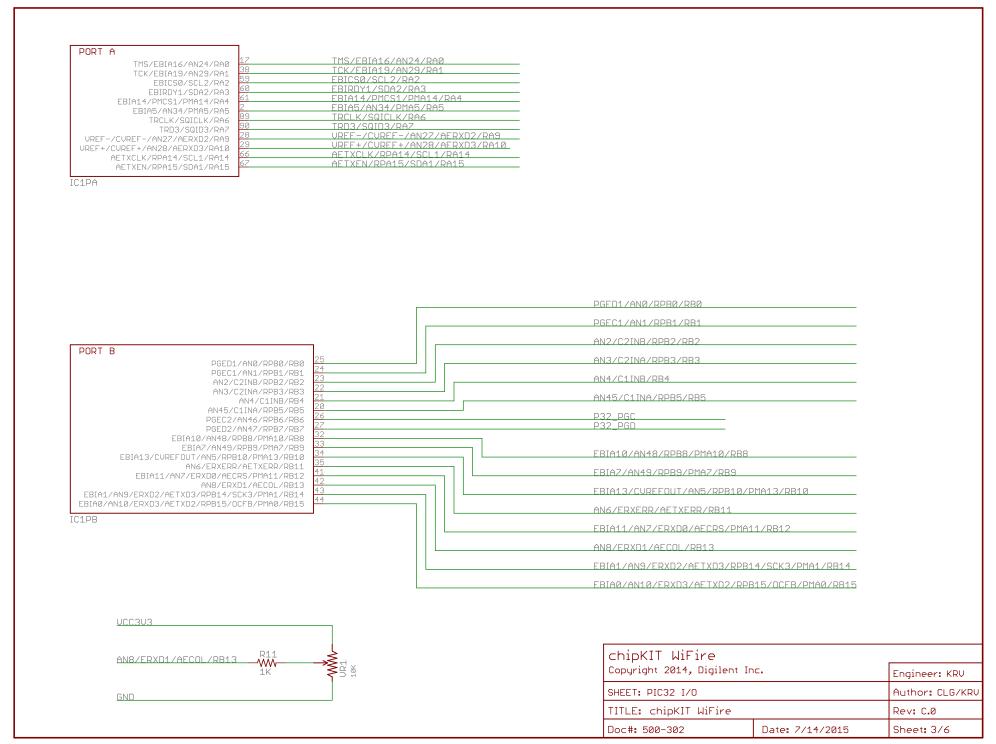
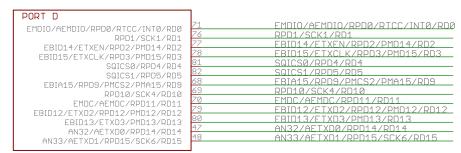


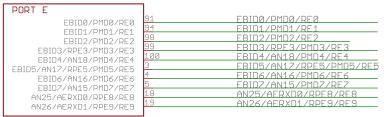
EBIRDY3/RPF2/SDA3/RF2	0	ا ہما
EBID0/PMD0/RE0	26	18 5 T
EBIRDY2/RPF8/SCL3/RF8	1	N 3
FBID1/PMD1/RF1	27	
AN25/AERXDØ/RPE8/RE8	2	
EBID2/PMD2/RE2	28	1201
EMDIO/AEMDIO/RPD0/RTCC/INT0/RD0	3	18°
EBID3/RPE3/PMD3/RE3	29	18 E
EBIRDY1/SDA2/RA3	4	ا ۋۇ
EBID4/AN18/PMD4/RE4	30	10 10 l
RPD1/SCK1/RD1	5	0 11
EBID5/AN17/RPE5/PMD5/RE5	31	1 2 1
EBID14/ETXEN/RPD2/PMD14/RD2	6	1 3 1
EBID6/AN16/PMD6/RE6	32	0 14
AN26/AERXD1/RPE9/RE9	7	15 l
EBIDZ/AN15/PMDZ/REZ	33	0 16
		0 10
		J10

chipKIT WiFire			
Copyright 2014, Digilent Inc.		Engineer: KRV	
SHEET: chipKit I/O connecto	ors	Author: CLG/KRV	
TITLE: chipKIT WiFire		Rev: C.0	
Doc#: 500-302	Date: 7/14/2015	Sheet: 2/6	



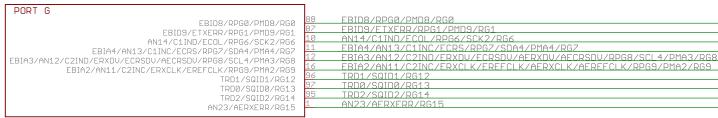


IC1PD

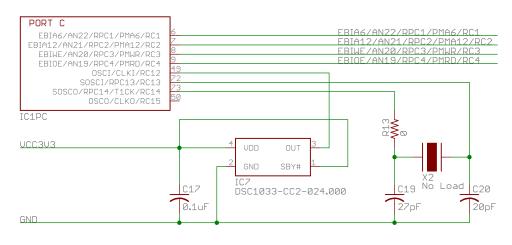


PORT F

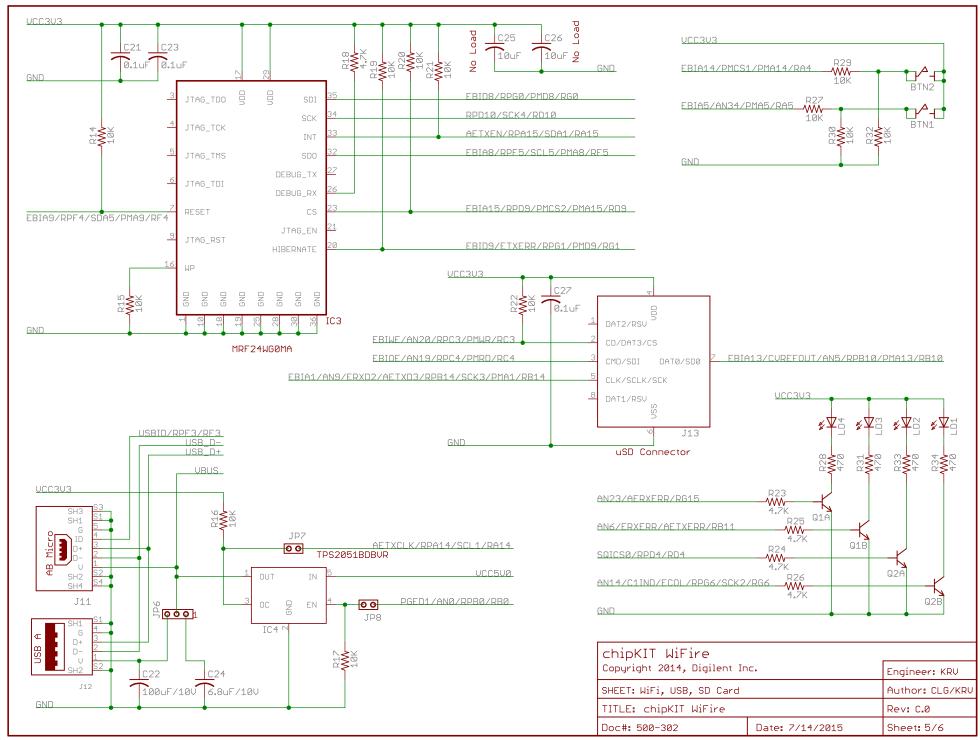
EBID11/ETXD1/RPF0/PMD11/RF0
EBID10/ETXD0/RPF1/PMD10/RF1
EBIRDY3/RPF2/SDA3/RF2
USBID/RPF3/RF3
EBIA9/RPF4/SDA5/PMA9/RF4
EBIA8/RPF5/SCL5/PMA8/RF5
EBIRDY2/RPF8/SCL3/RF8
TD0/EBIA12/AN31/RPF12/RF12
TD1/EBIA18/AN30/RPF13/SCK5/RF13



IC1PG



chipKIT WiFire				
Copyright 2014, Digilent In	C.	Engineer: KRV		
SHEET: PIC32 I/O	HEET: PIC32 I/O			
TITLE: chipKIT WiFire		Rev: C.0		
Doc#: 500-302	Date: 7/14/2015	Sheet: 4/6		



7/14/2015 3:47:38 PM f=0.97 C:\PCB Eagle\chipKIT WiFire\chipKIT WiFire.sch (Sheet: 5/6)

