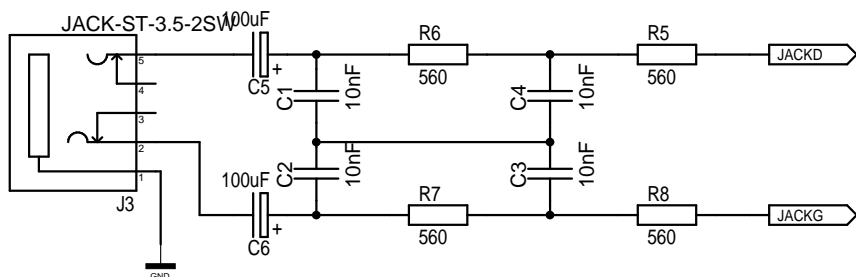
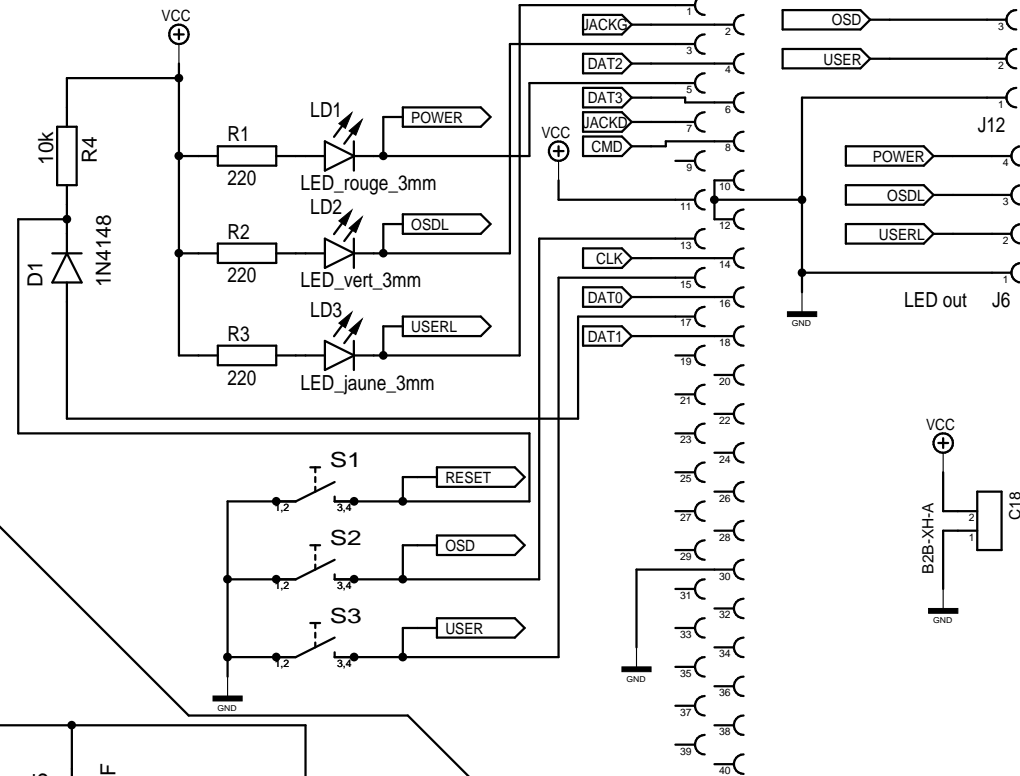


Sortie Audio jack

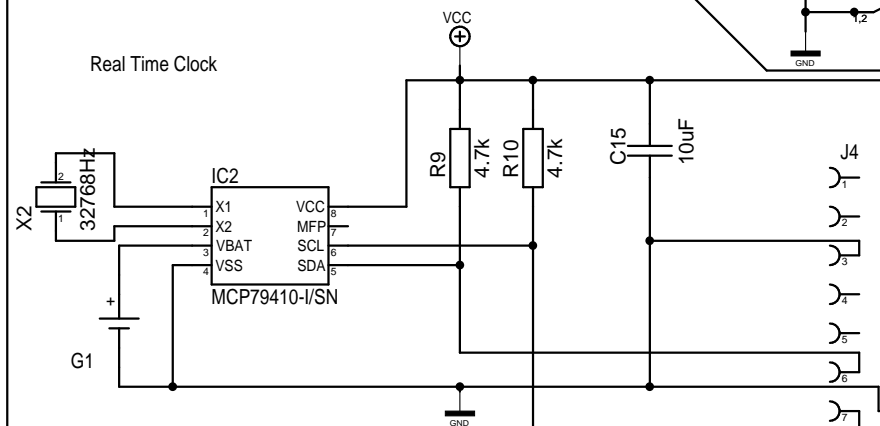


Connections IO



MiSTer FPGA

Real Time Clock



Autres

LOGO EPTM 10mm

M3x10M3x10M3x10

Projet MiSTer

PCB All-In-One

Ecole professionnelle technique et des métiers

Des : MelvTiss

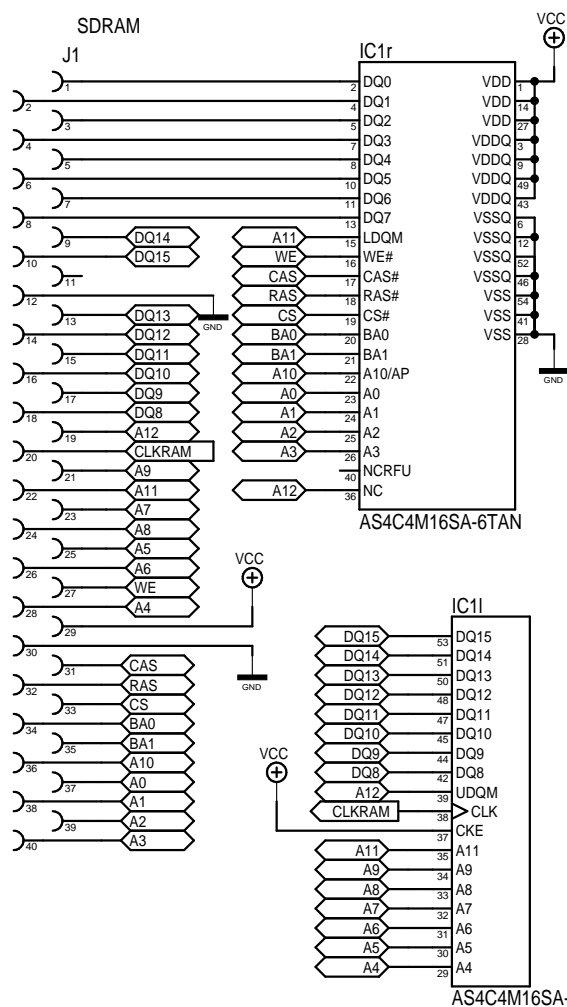
Rev : V2.0

Fichier : PCB ALLNONE V2.T3001

Date : 09.01.2020

Feuille : 1

SDRAM



Lecteur Micro-SD

