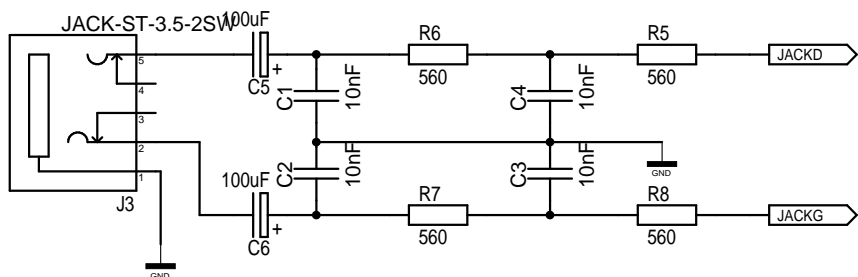
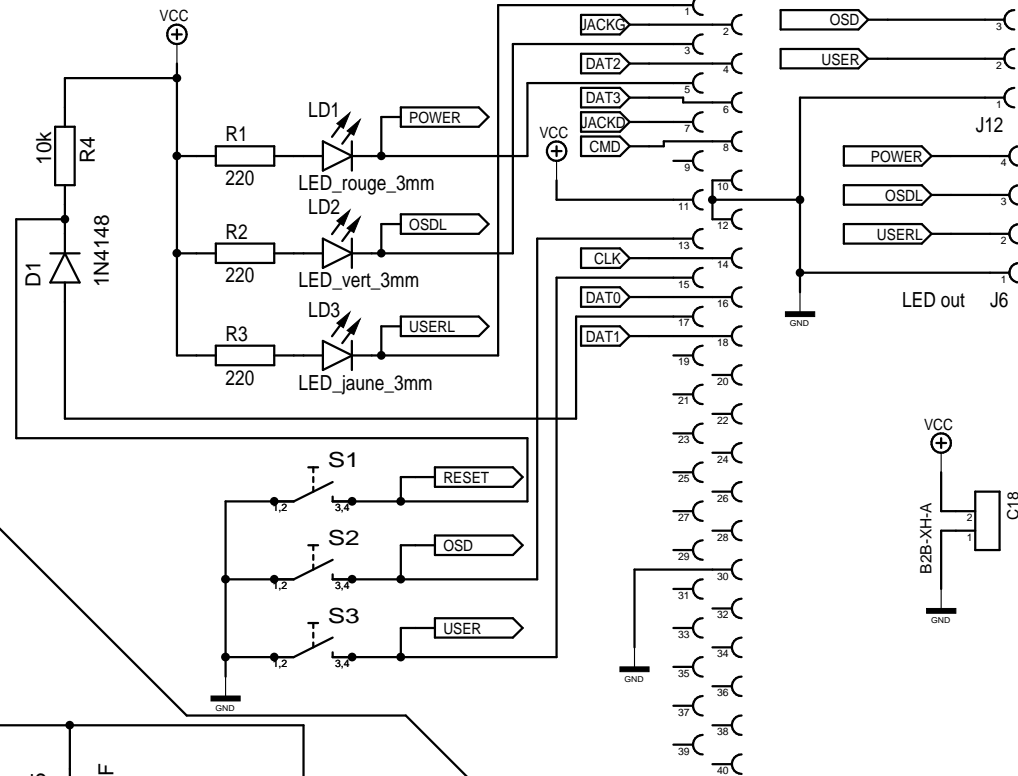


Sortie Audio jack

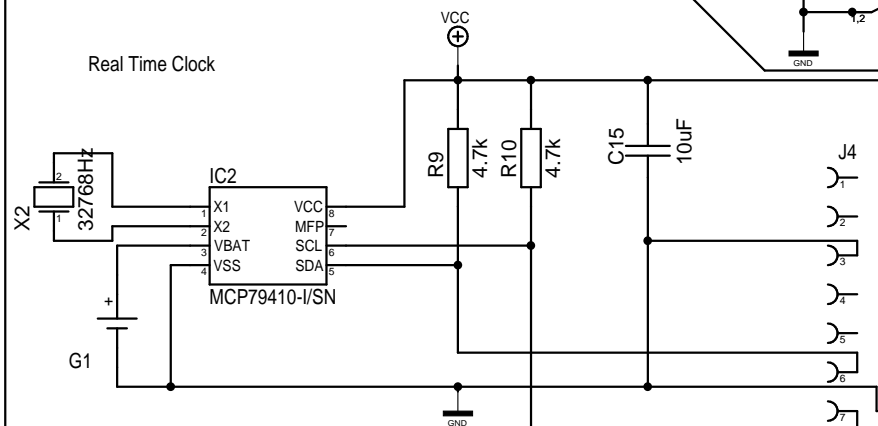


Connections IO



MiSTer FPGA

Real Time Clock



Autres

LOGO EPTM 10mm

M3x10M3x10M3x10

Projet MiSTer

PCB All-In-One

Ecole professionnelle technique et des métiers

Des : MelvTiss

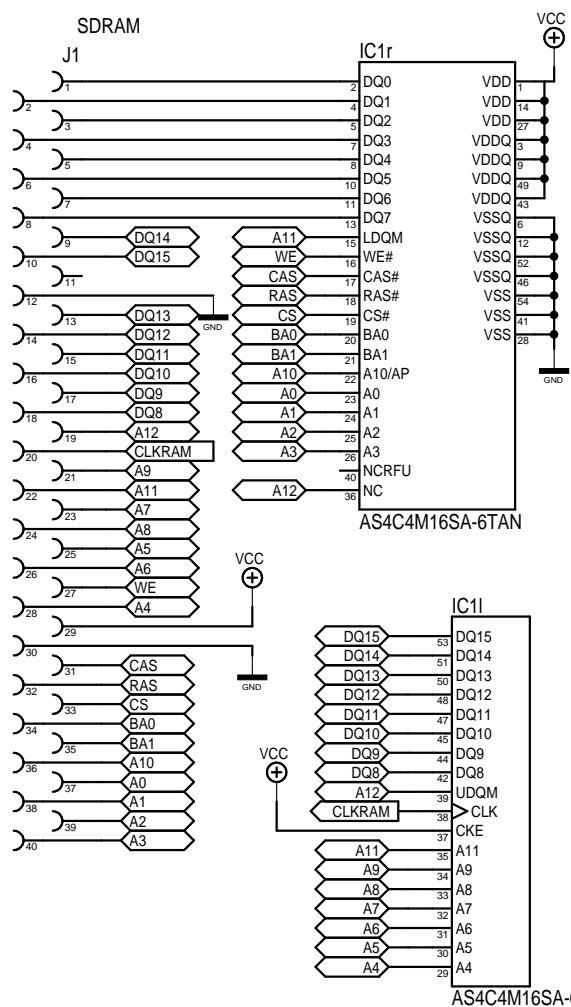
Rev : v3

Fichier : PCB ALLINONE V3.T3001

Date : 14.02.2020

Feuille : 1

SDRAM



Lecteur Micro-SD

