

PYRITE+

MEC1723/MEC1727 Modular EC Card

Mates with Intel RVP MTL Platforms

BLOCK DIAGRAM

Description	Sheet
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MEC1723 or MEC1727	2
MECC Connector, eSPI	3
Keyboard Scan, Trace FIFO, JTAG, SPI Flash	4
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TBD	13
TBD	14

Revision History


Rev 1.0:

Initial Release

Rev 1.1:

Fixed Title Blocks incorrect part #

Change Gigadevice 1.8V part #

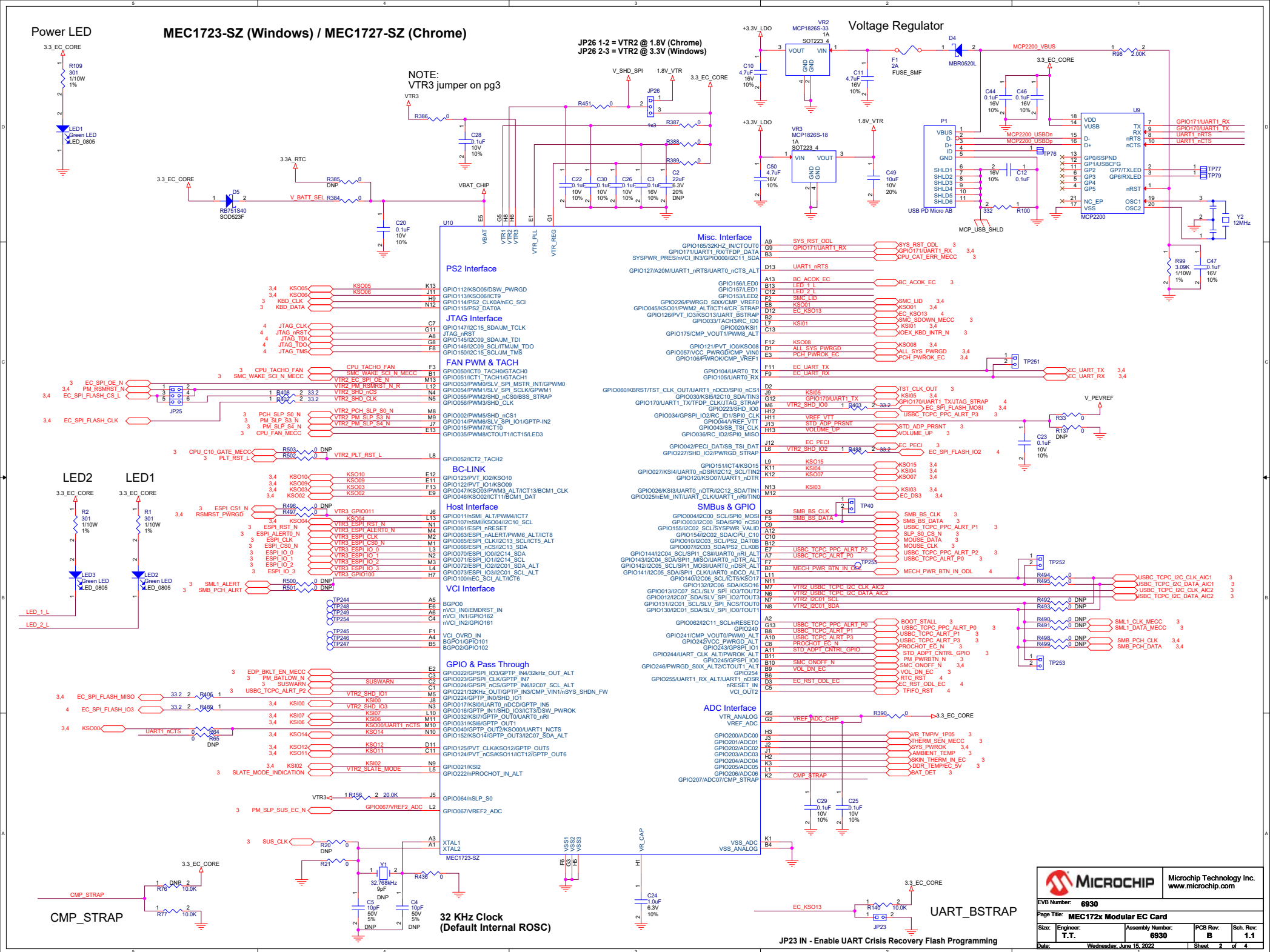
 MICROCHIP		Microchip Technology Inc. www.microchip.com	
EVB Number: 6930			
Page Title: MEC172x Modular EC Card			
Size:	Engineer: T.T.	Assembly Number: 6930	PCB Rev: B Sch. Rev: 1.1
Date:	Wednesday, June 22, 2022		Sheet 1 of 4

Power LED

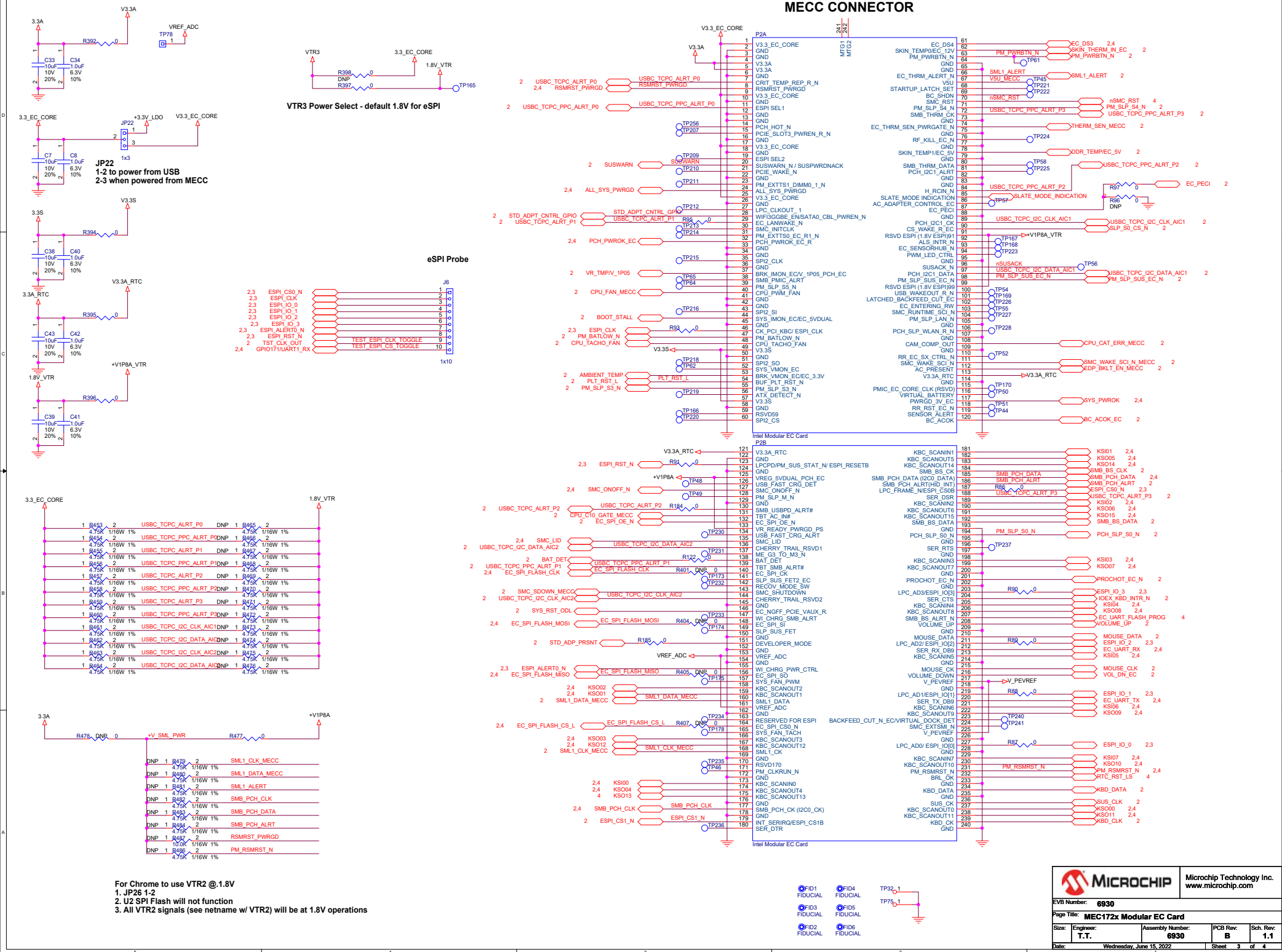
MEC1723-SZ (Windows) / MEC1727-SZ (Chrome)


JP26 1-2 = VTR2 @ 1.8V (Chrome)
JP26 2-3 = VTR2 @ 3.3V (Windows)NOTE:
VTR3 jumper on pg3

Voltage Regulator



MECC CONNECTOR



 MICROCHIP		Microchip Technology Inc. www.microchip.com	
EVB Number: 6930			
Page Title: MEC172x Modular EC Card			
Size:	Engineer:	Assembly Number:	PCB Rev:
	T.T.	6930	B
			Sch. Rev:
			1.1
Date: Wednesday, June 15, 2022		Sheet 3 of 4	

EC UART Flash Programming (MEC1727)

3 EC_UART_FLASH_PROG

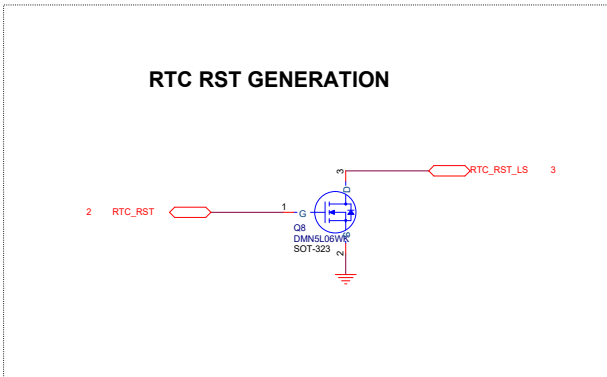
3.4 KSO13

2 EC_KSO13

1 2

D15
R8781S40
SOD523F

1 2k00K



POWER BUTTON

The schematic diagram illustrates the POWER BUTTON circuit. It features a 3.3V core supply connected to a 100K pull-up resistor (R101). A 1.0uF capacitor (C13) is connected to ground. The button, labeled S2 (SPST MOM SW_MOM_PB), is connected to the 3.3V supply and a network of resistors (R103, R104, R151, R155) that interface with the KBD_PWR_BTN_ODL signal. The button is also connected to the MECH_PWR_BTN_IN_ODL signal (pin 2) and the SMC_ONOFF_N signal (pin 2.3).

RESET CIRCUIT

3.3_EC_CORE

R504
10.0K
1/16W
1%

2 EC_RST_ODD_EC

R105 0

3 nSMC_RST

R106 0

R107 100

2

SPST_MOM
SW_MOM_PB

C14
1.0uF
16V
10%

2

Software Developers Debug Interface

2.4 GPIO17(UART1_TX)UTAG_STRAP 1 J4 2 2.3 EC_UART_TX 2.3

2.3 GPIO17(UART1_RX)TFIFO_RST 3 3 4 EC_UART_RX 2.3

2 5 6

2k3

Ground

[illegible]

J9 - Chromebook Keyboard (Default)
J3/J7 - Microchip Keyboard (Reserved Test Only)

[illegible]