

# Everglades EVB

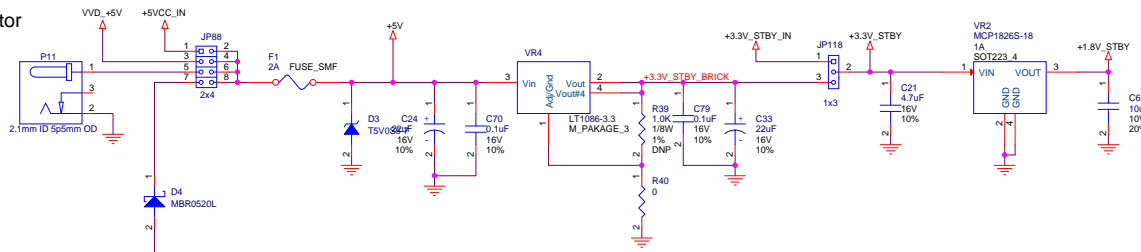
## BLOCK DIAGRAM

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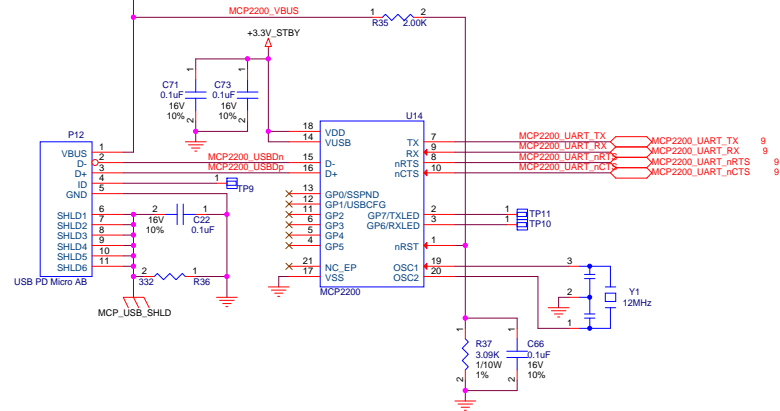
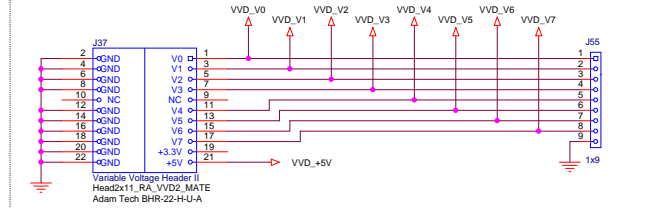
### Revision History

- Rev 1.0:**  
Initial Release
- Rev 1.1:**  
Remove non-existent LPC related signals

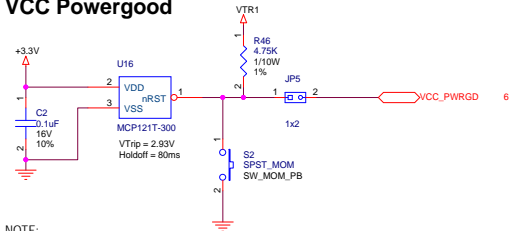
## Voltage Regulator



## Variable Voltage Header II

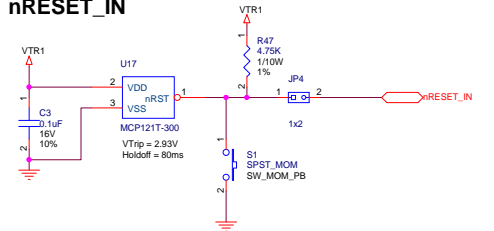


## VCC Powergood



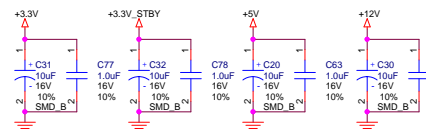
NOTE:  
+3.3V powered with pull-up to VTR1

## nRESET\_IN

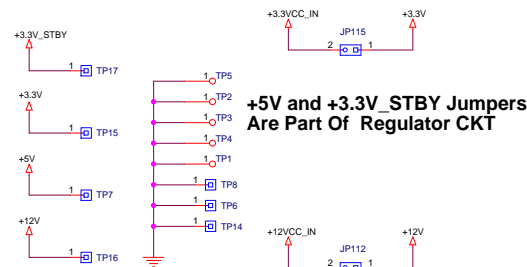


NOTE:  
+3.3V\_STBY powered with pull-up to VTR1

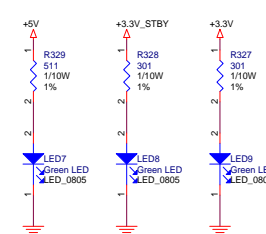
## General Board Capacitors



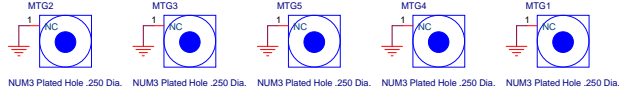
## Test Points




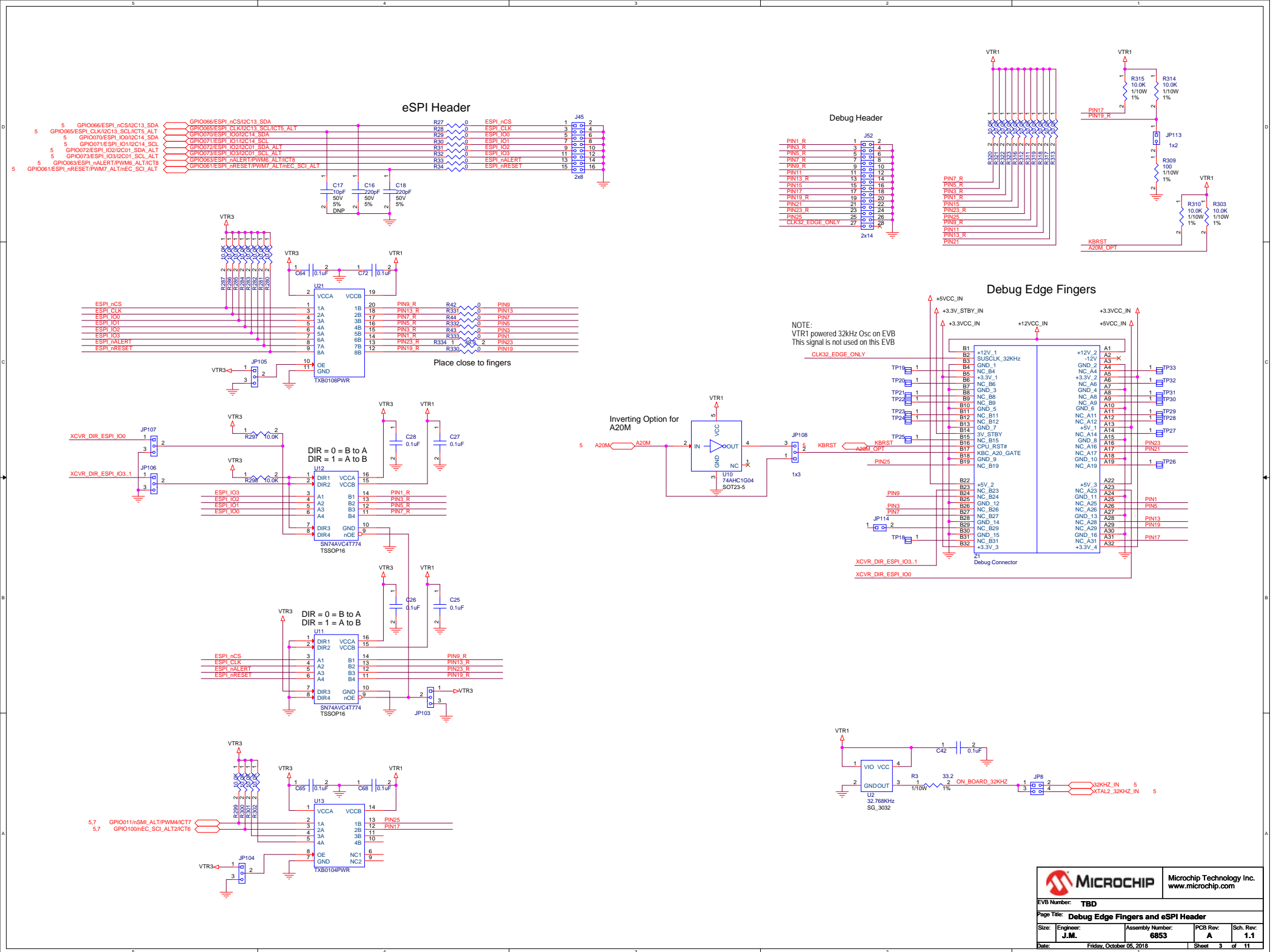
## Power LEDs

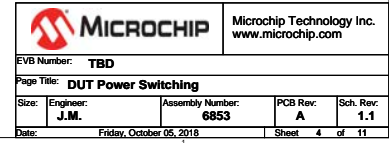


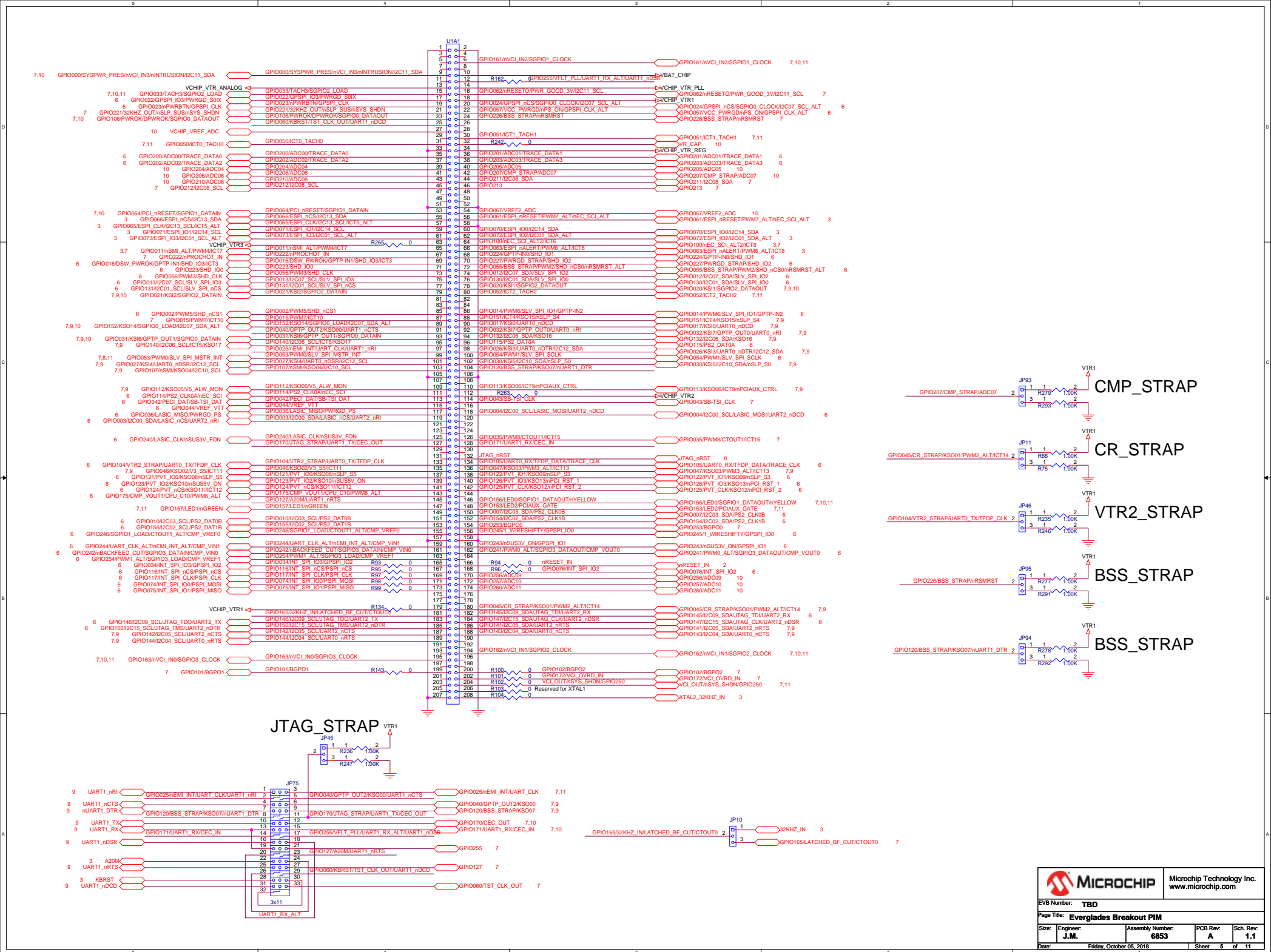
## Mounting Holes

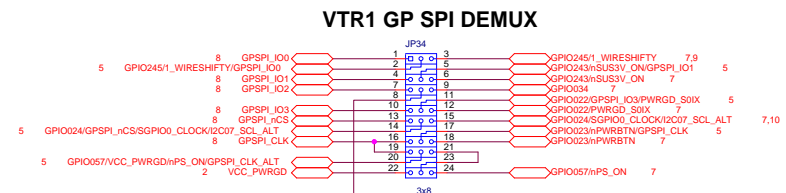
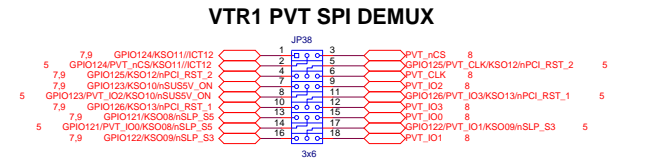
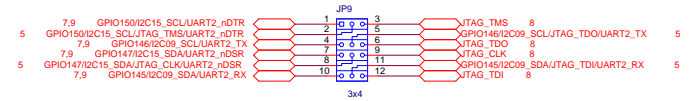
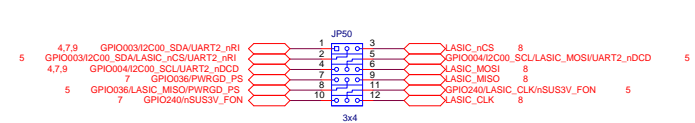


 <b>MICROCHIP</b>		Microchip Technology Inc. www.microchip.com	
EVB Number: <b>TBD</b>			
Page Title: <b>EVB Power and Resets</b>			
Size: <b>J.M.</b>	Engineer:	Assembly Number: <b>6853</b>	PCB Rev: <b>A</b>
			Sch. Rev: <b>1.1</b>
Date: <b>Friday, October 05, 2018</b>		Sheet <b>2</b> of <b>11</b>	

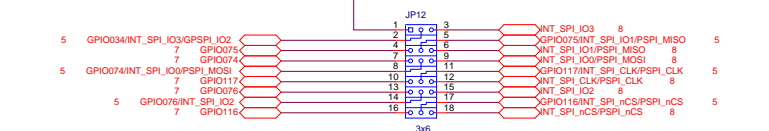




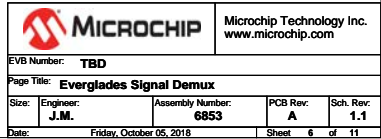
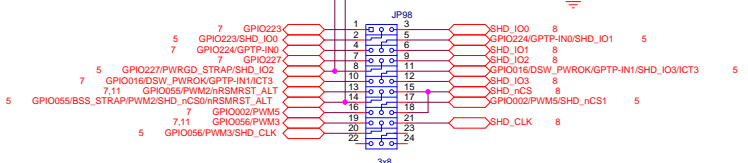
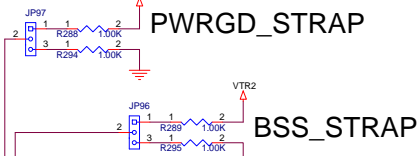


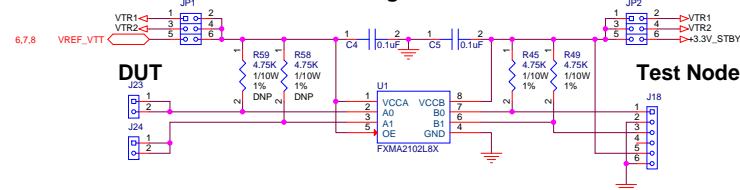
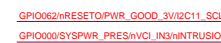
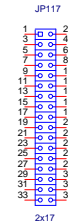
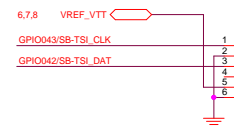


## VTR1 INT SPI DEMUX



PWRGD STRAP



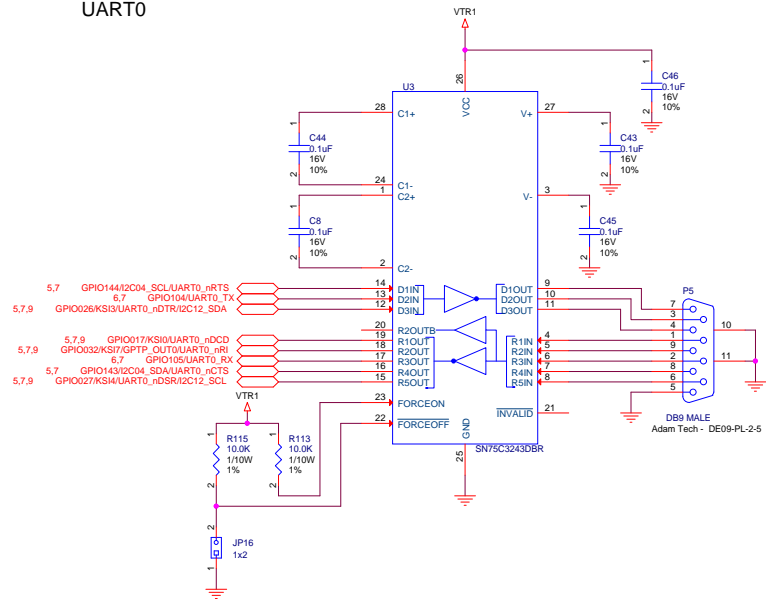


EVB Number: <b>TBD</b>			
Page Title: <b>GPIO, SGPIO and I2C</b>			
Size:	Engineer: <b>J.M.</b>	Assembly Number: <b>6853</b>	PCB Rev: <b>A</b>
			Sch. Rev: <b>1.1</b>
Date:	Friday, October 05, 2018		Sheet <b>7</b> of <b>11</b>

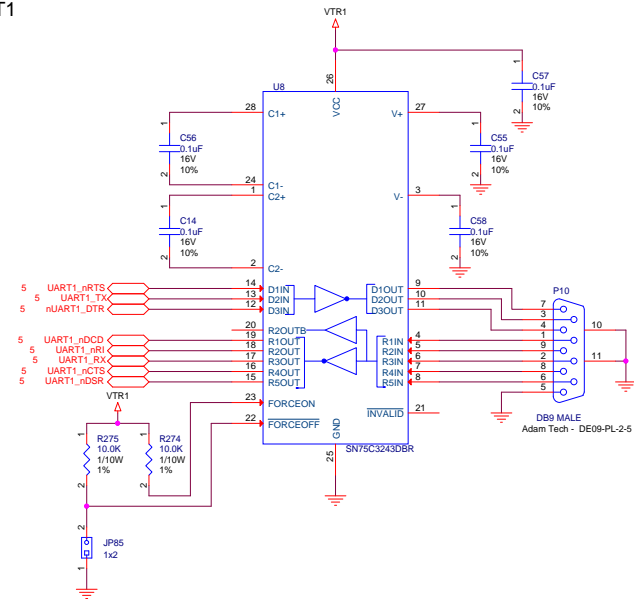




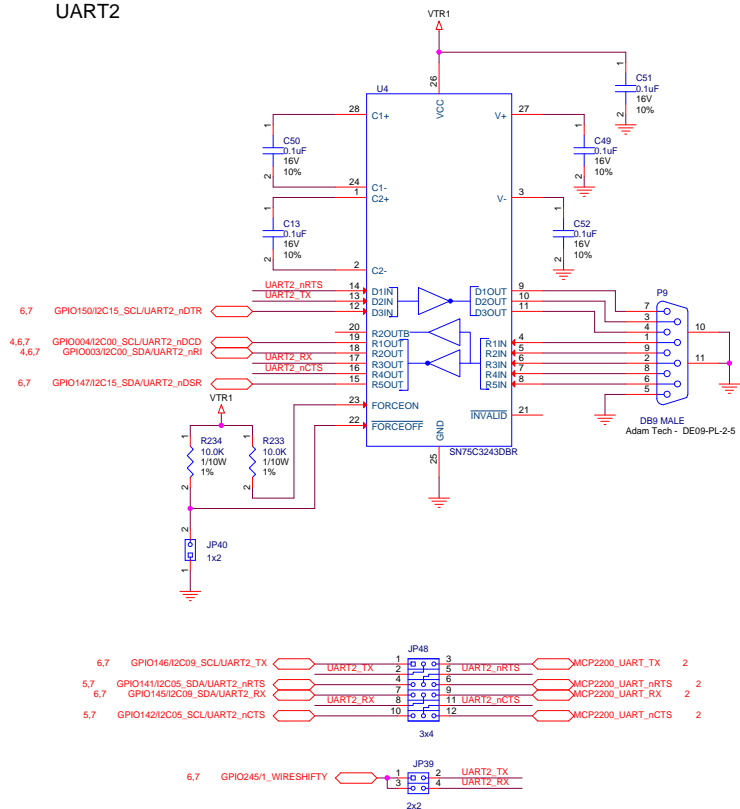
## UART0



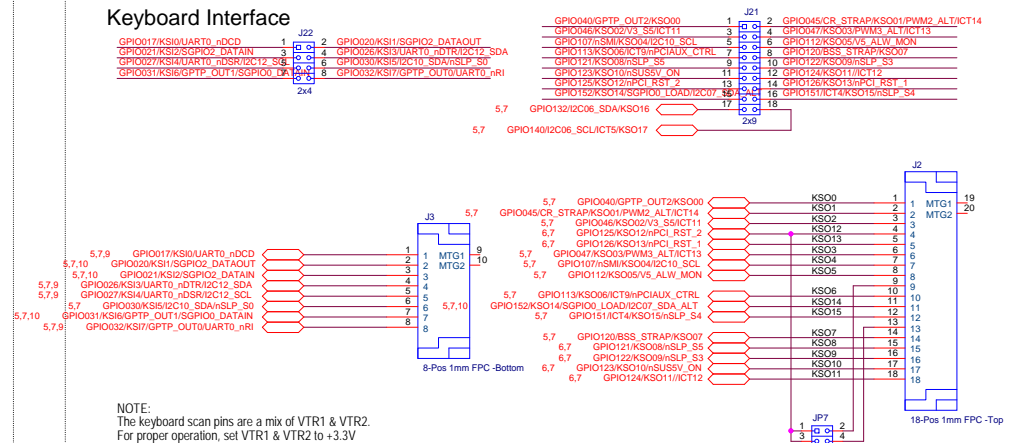
## UART1



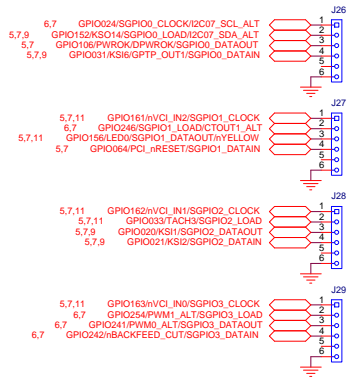
## UART2



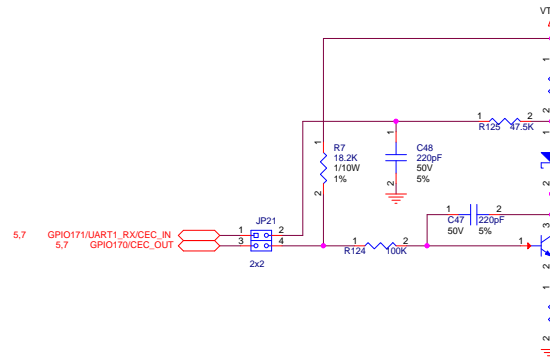
## Keyboard Interface



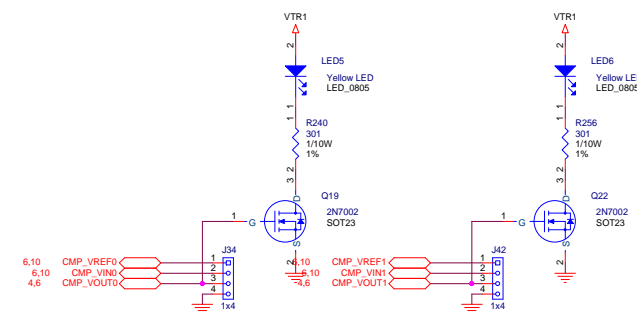
## SGPIO Bus Interface



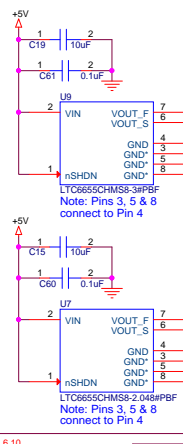
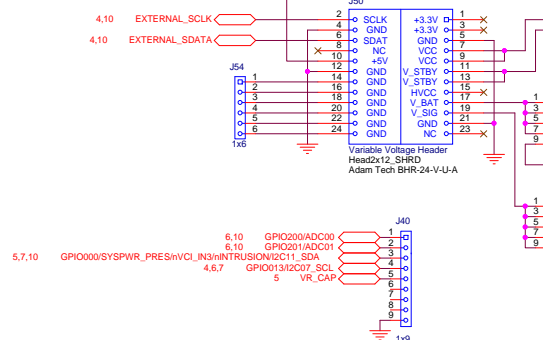
## HDMI-CEC Circuit



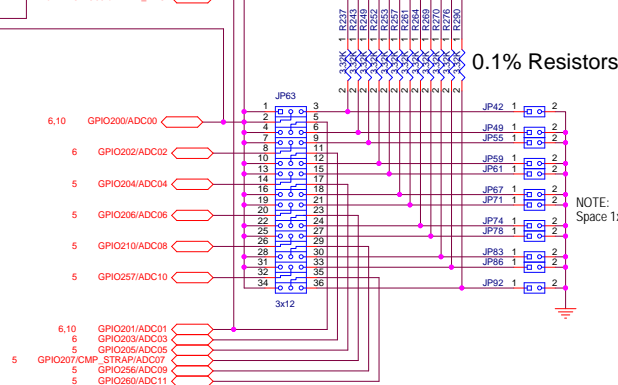
## Comparator Access Headers



## Variable Voltage Header



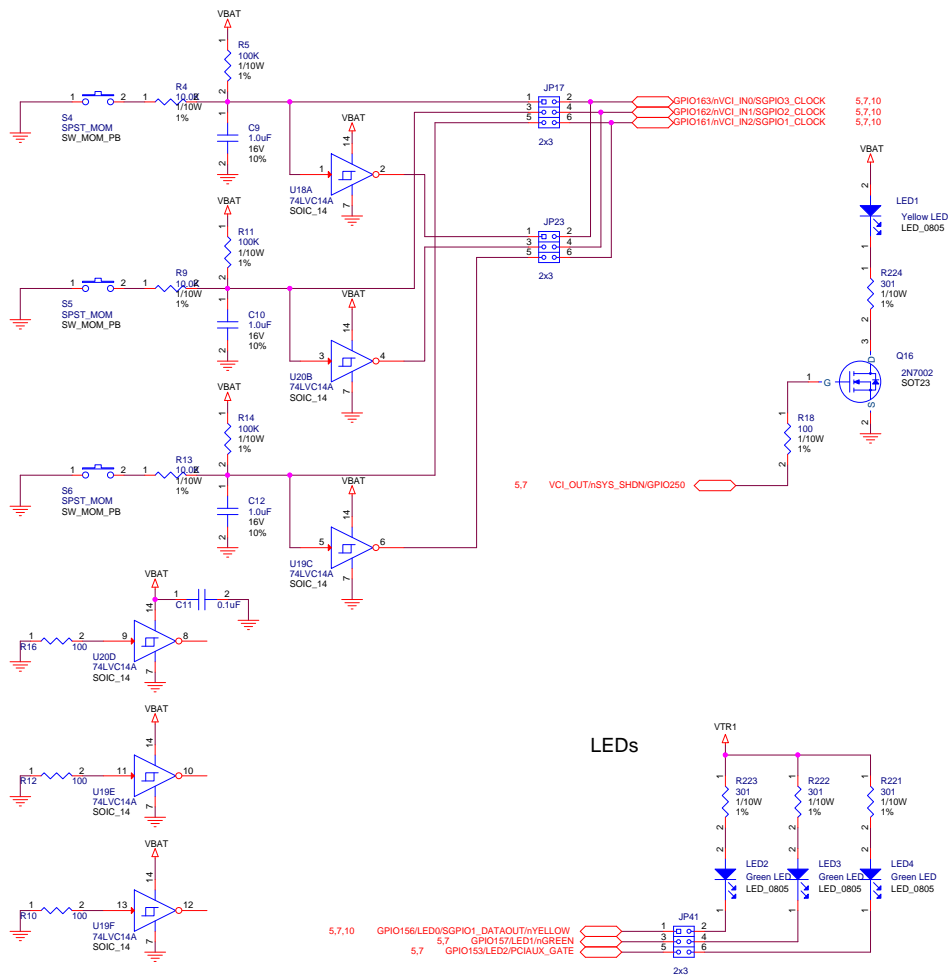
## ADC Access Headers



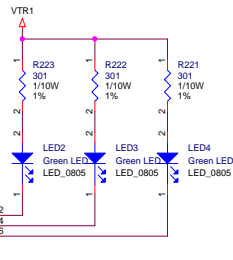
## 0.1% Resistors

NOTE:  
Space 1x2 jumpers on 0.125" grid

# VCI Interface / Battery Well Glue logic

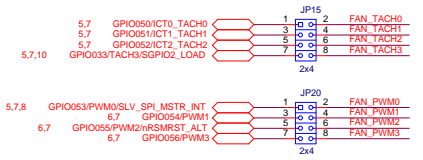
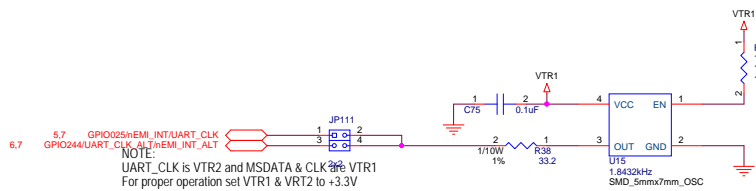
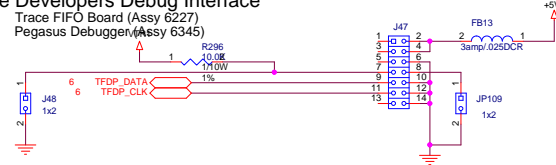


## LEDs

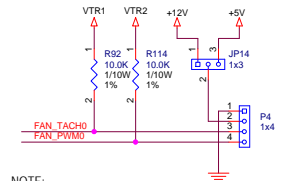


## Software Developers Debug Interface

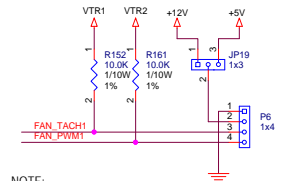
Mates with: Trace FIFO Board (Assy 6227)  
Pegasus Debugger (Assy 6345)



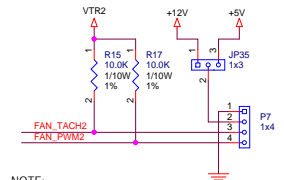
## FAN0



## FAN1



## FAN2



## FAN3

