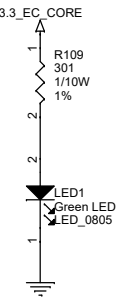


BLOCK DIAGRAM

Description	Sheet
Title Page	1
MEC1723	2
EC Connector, SPI Flash	3
H1 Controller, Keyboard Scan, Trace FIFO, JTAG	4
Servo Header	5
C2D2 Muxing, UART Switches	6
TBD	7
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TBD	13
TBD	14

Rev 1.0:
Initial Release

NOTE:
VTR3 jumper on pg3



EVB Number: TBC

Page Title: MEC1723 Modular EC Card

Size:	Engineer:
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Assembly Number

PCB Rev:

ch. Re

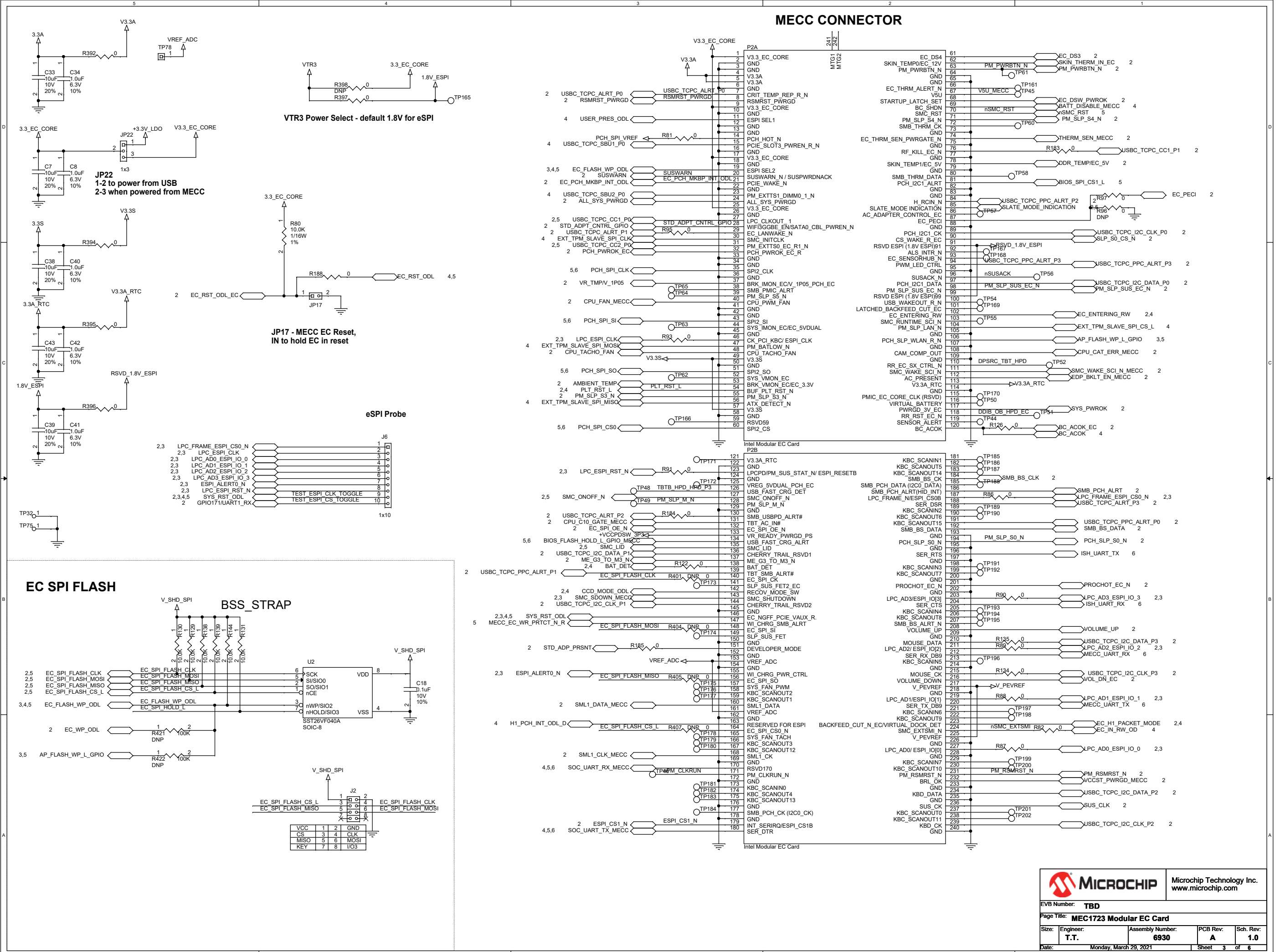
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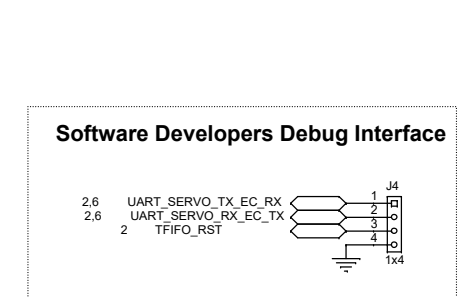
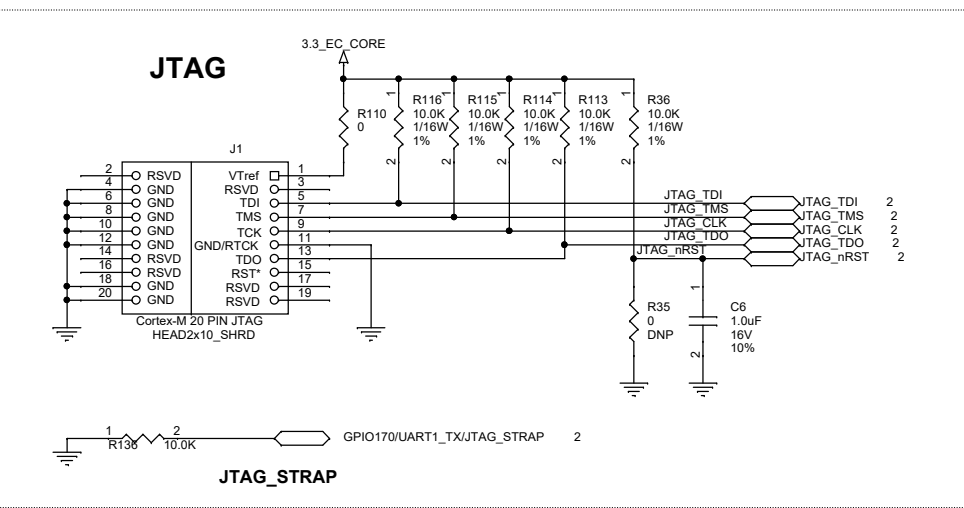
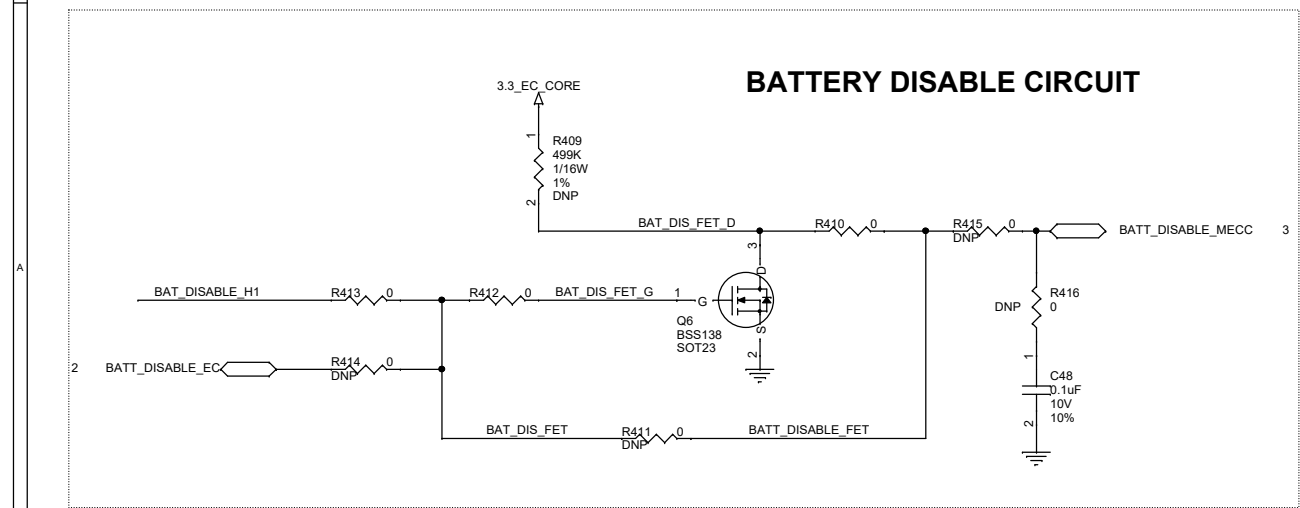
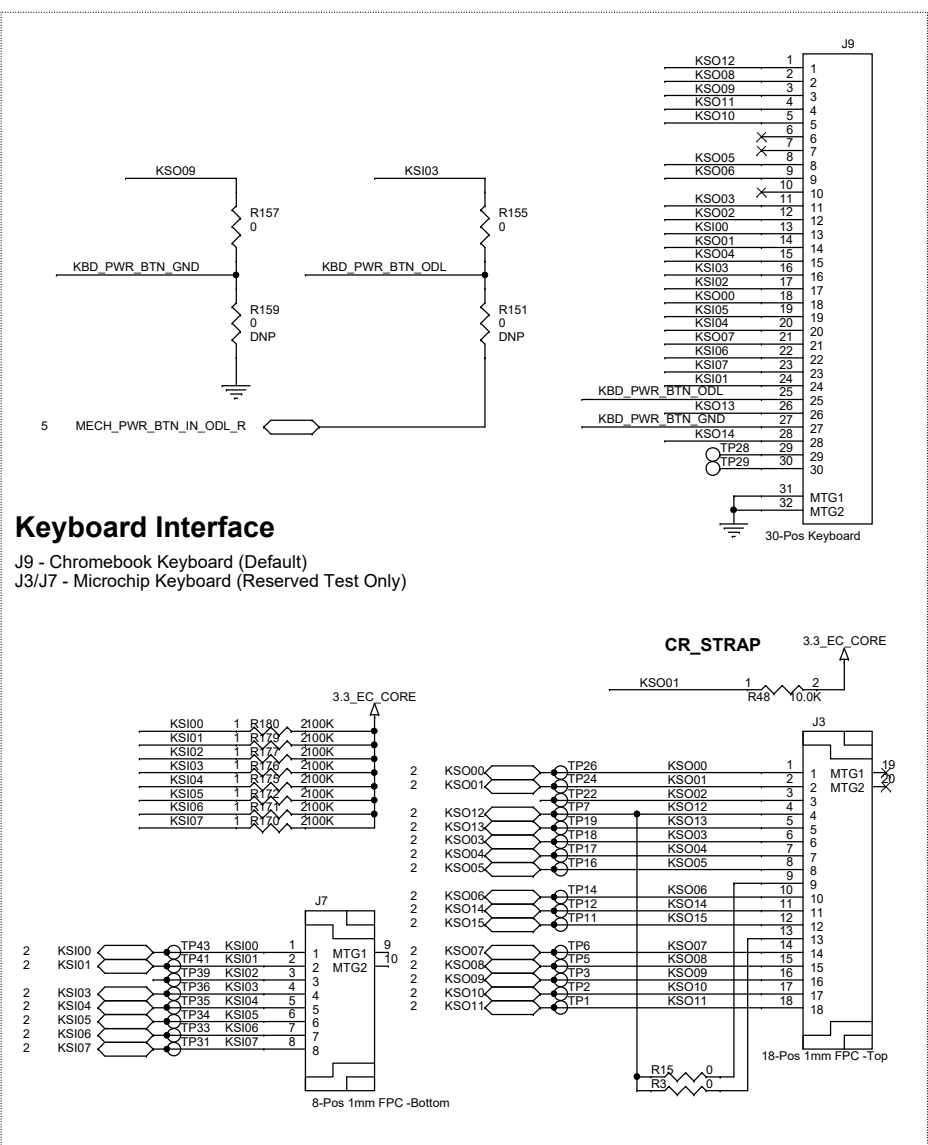
Monday, March

Sheet 2

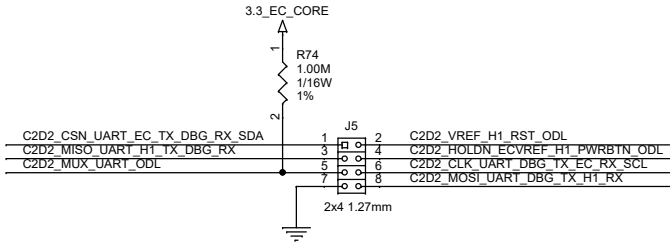
6

JP23 IN - Enable UART Crisis Recovery Flash Programming

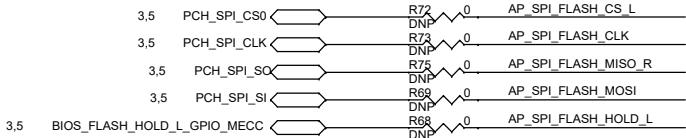




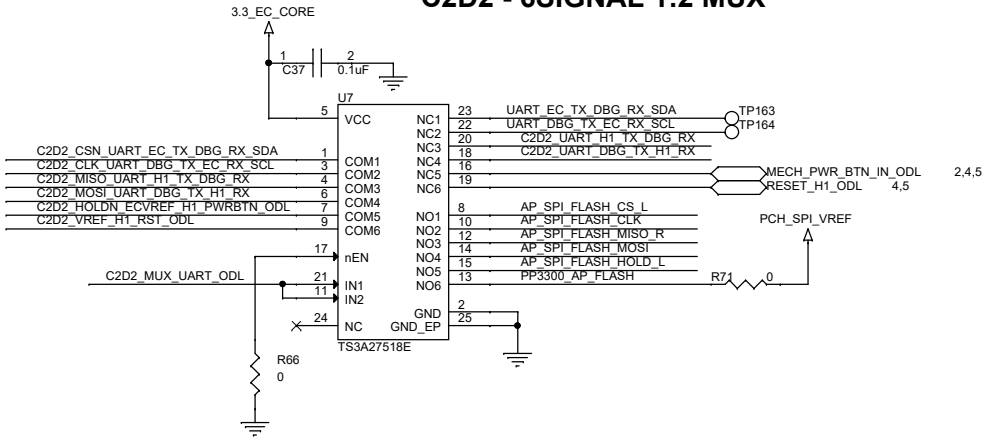
C2D2 HEADER



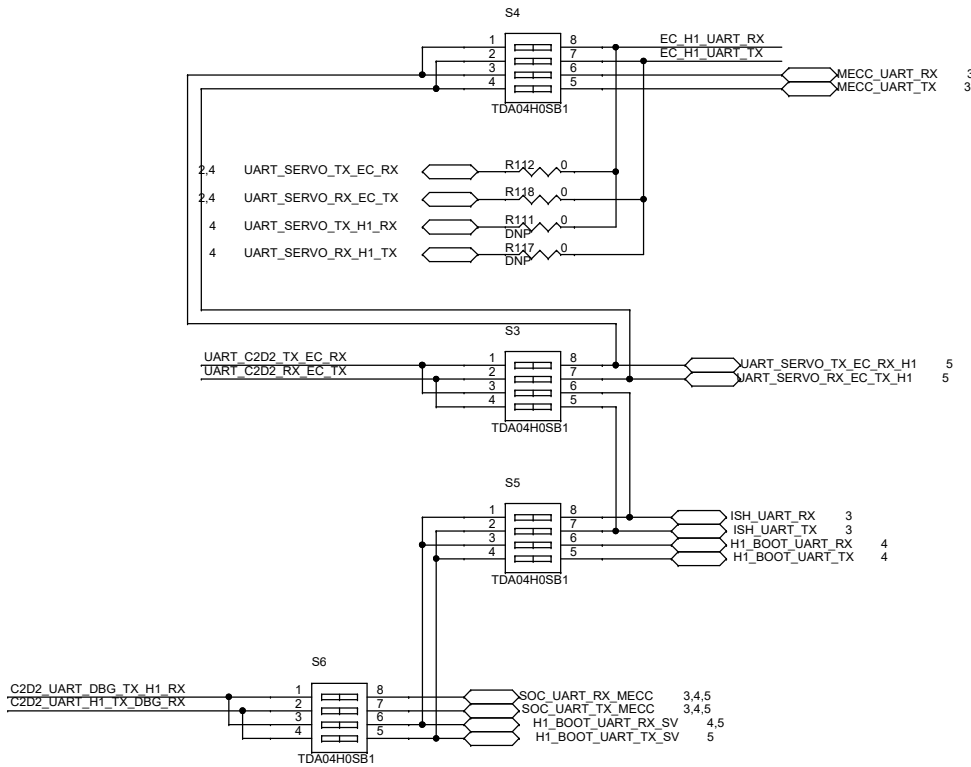
C2D2 TO PCH SPI STUFFING OPTION



C2D2 - 6SIGNAL 1:2 MUX



UART SELECTION SWITCH CIRCUIT



S.NO.	C2D2 MUX INPUT	C2D2 MUX OUTPUT	NC/NO	FUNCTION
1	C2D2_CSN_UART_EC_TX_DBG_RX_SDA	UART_EC_TX_DBG_RX_SDA	NC1	EC
		AP_SPI_FLASH_CS_L	NO1	SPI ROM
2	C2D2_CLK_UART_DBG_TX_EC_RX_SCL	UART_DBG_TX_EC_RX_SCL	NC2	EC
		AP_SPI_FLASH_CLK	NO2	SPI ROM
3	C2D2_MISO_UART_H1_TX_DBG_RX	C2D2_UART_H1_TX_DBG_RX	NC3	H1
		AP_SPI_FLASH_MISO_R	NO3	SPI ROM
4	C2D2_MOSI_UART_DBG_TX_H1_RX	C2D2_UART_DBG_TX_H1_RX	NC4	H1
		AP_SPI_FLASH_MOSI	NO4	SPI ROM
5	C2D2_HOLDN_ECVREF_H1_PWRBTN_ODL	MECH_PWR_BTN_IN_ODL	NC5	PWR BTN
		AP_SPI_FLASH_HOLD_L	NO5	SPI ROM
6	C2D2_VREF_H1_RST_ODL	RESET_H1_ODL	NC6	H1
		PP3300_AP_FLASH	NO6	POWER
7	GROUND	GROUND		GND
8	C2D2_MUX_UART_ODL	MUX SWITCH	IN1	SEL1
			IN2	SEL2

Switch	Switch Position	Servo Config				
		EC UART		SOC UART	ISH UART	H1 UART
		Mica EC	RVP EC			
S6	On					
	Off					
S5	On				1-8, 2-7	3-6, 4-5
	Off				3-X, 4-X	1-X, 2-X
S4	On	1-8, 2-7	3-6, 4-5			
	Off	3-X, 4-X	1-X, 2-X			
S3	On					
	Off					

Switch	Switch Position	C2D2 Config					
		EC UART		EC UART	SOC UART	ISH UART	H1 UART
		Mica EC	RVP EC				
S6	On				1-8, 2-7		3-6, 4-5
	Off				3-X, 4-X		1-X, 2-X
S5	On						
	Off						
S4	On	1-8, 2-7	3-6, 4-5				
	Off	3-X, 4-X	1-X, 2-X				
S3	On			1-8, 2-7		3-6, 4-5	
	Off			3-X, 4-X		1-X, 2-X	