

Peripheral Register
Definitions

SYSCFG Register Map



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graph LR; A[SYSCFG Register Map] --> B[Peripheral Register Definitions]
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The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'Peripheral Register Definitions' in two lines. The box on the right is gray with a black border and contains the text 'SYSCFG Register Map' in one line. A black arrow points from the right side of the gray box to the left side of the white box, indicating a directional relationship or flow from the SYSCFG Register Map to the Peripheral Register Definitions.