

STM32F401xx MCU Header File

```
graph LR; A[Cortex-M4 Peripherals Base Addresses] --> B[STM32F401xx MCU Header File]
```

The diagram consists of two rectangular boxes. The box on the right is shaded gray and contains the text 'Cortex-M4 Peripherals Base Addresses'. The box on the left is white with a black border and contains the text 'STM32F401xx MCU Header File'. A black arrow points from the right box to the left box, indicating a dependency or relationship where the header file is derived from or uses the peripheral base addresses.

Cortex-M4 Peripherals
Base Addresses