

STM32F401xx MCU Header File



```
graph LR; A[APB2 Peripherals Base Addresses] --> B[STM32F401xx MCU Header File]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow pointing from right to left. The box on the right is shaded gray and contains the text 'APB2 Peripherals Base Addresses'. The box on the left is white with a black border and contains the text 'STM32F401xx MCU Header File'.

APB2 Peripherals Base  
Addresses