



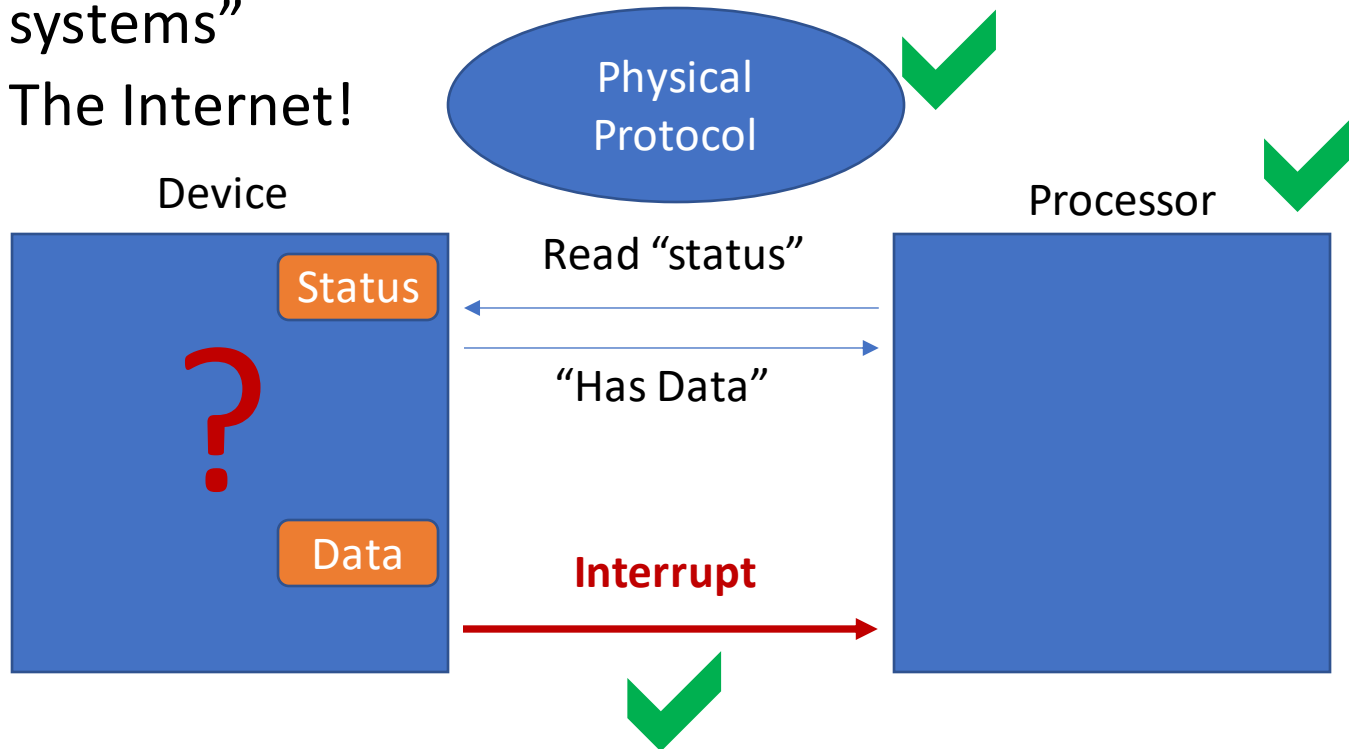
# **I/O Devices – ADC/DAC**

EECS 388 – Fall 2022

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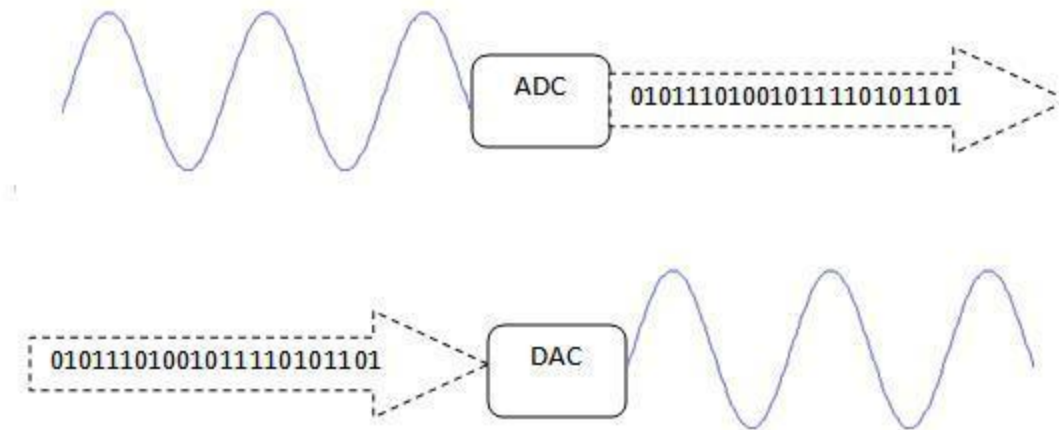
# Context

- Recommended reading:
  - Datasheet of the devices
  - Chapter 13 of “AVR Microcontroller and Embedded systems”
  - The Internet!

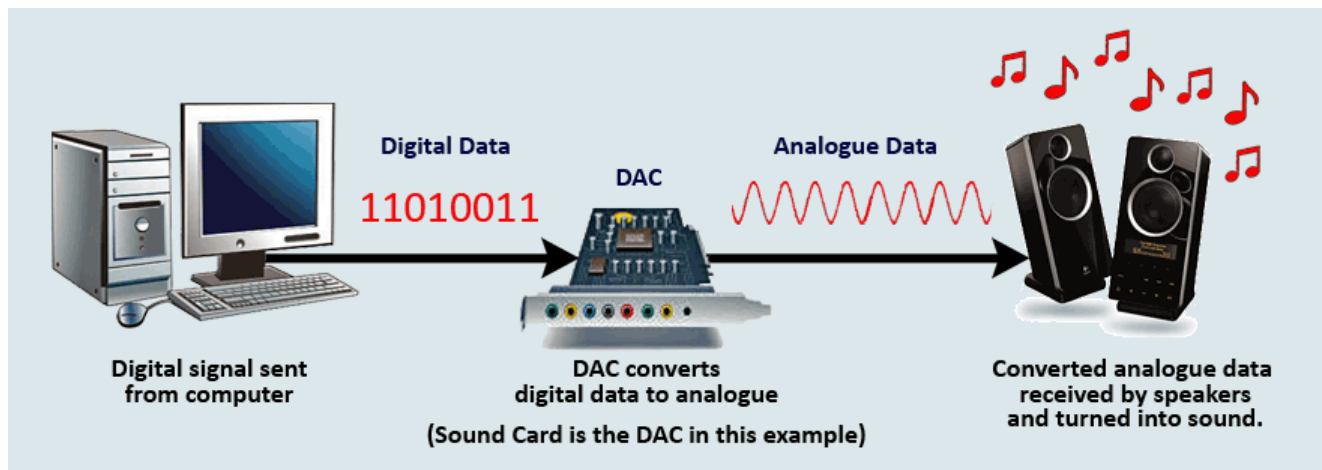
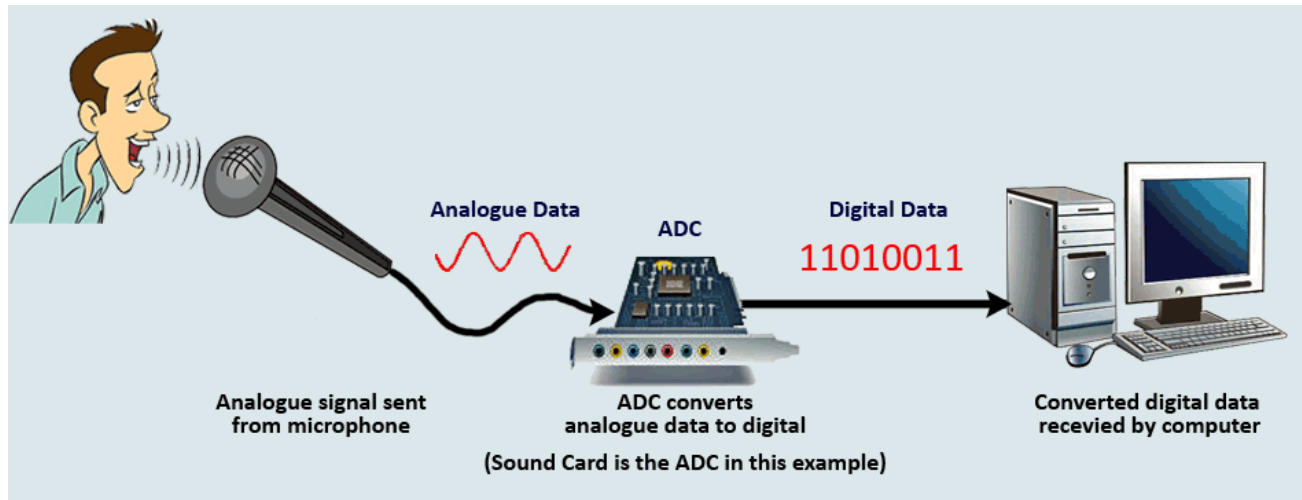


# ADC & DAC

- ADC (analog-to-digital converter)
  - Convert an analog signal into a digital one
- DAC (digital-to-analog converter)
  - Convert a digital signal into an analog one



# Analog to Digital, Digital to Analog



# Digital to Analog Convertor (DAC)

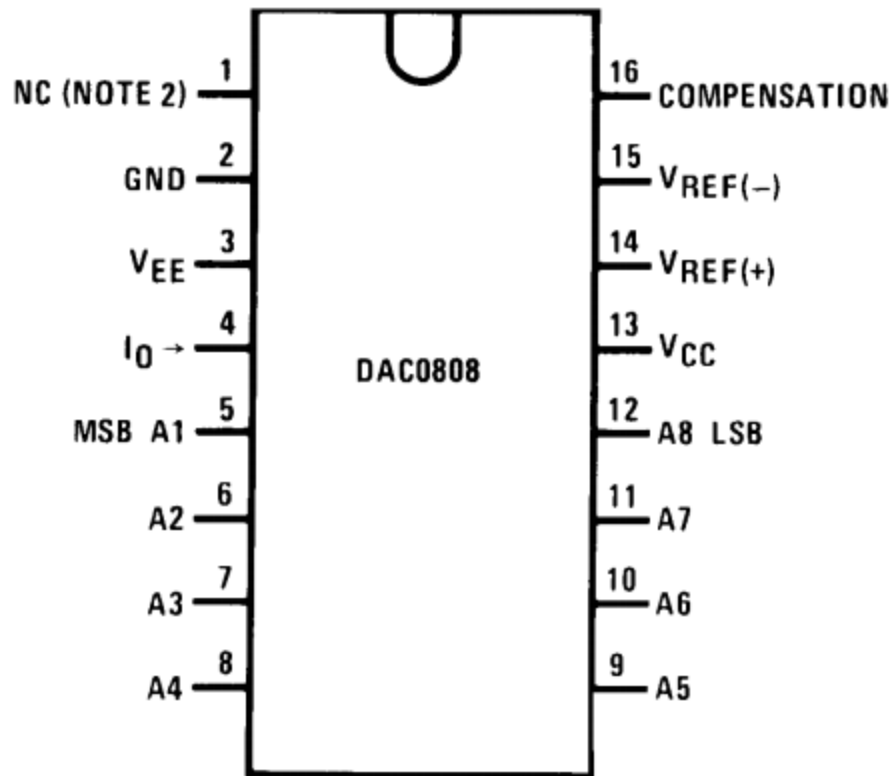
- The digital inputs are converted to current ( $I_{out}$ )
- By connecting  $I_{out}$  to a negative feedback op-amp we convert current to voltage
- The current provided by  $I_{out}$  is a function of input binary numbers and the reference current
  - E.g., for an 8 bit DAC:

$$I_{out} = I_{ref} \left( \frac{A1}{2} + \frac{A2}{4} + \dots + \frac{A8}{256} \right)$$

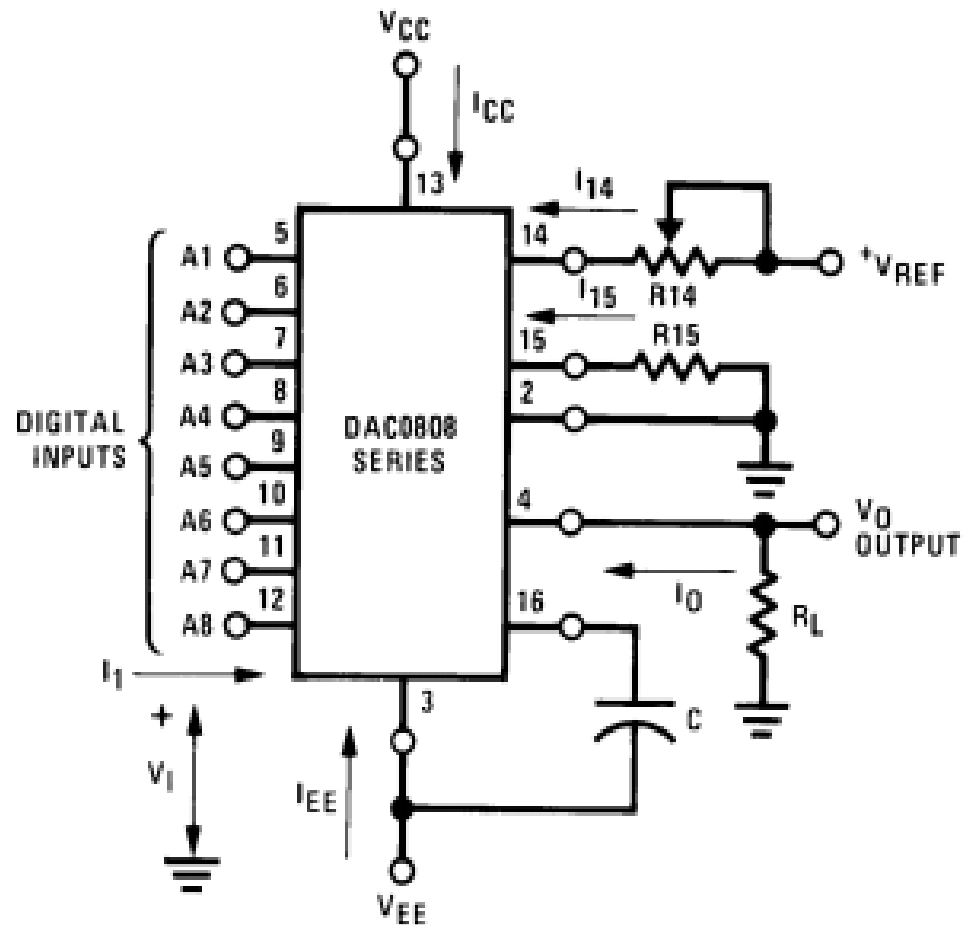
# DAC0808: 8-Bit D/A Converter



## Dual-In-Line Package



Source: DAC0808 datasheet

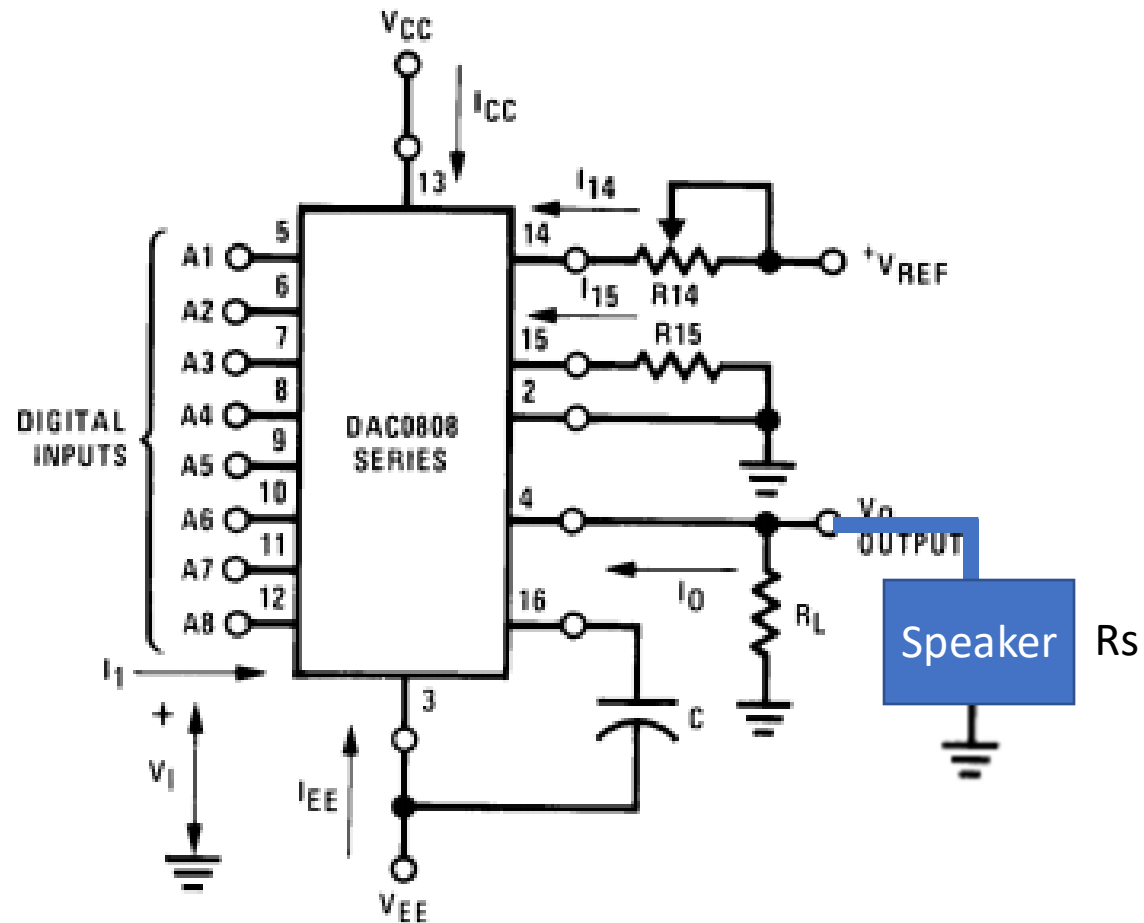


$$I_O = K \left( \frac{A_1}{2} + \frac{A_2}{4} + \frac{A_3}{8} + \frac{A_4}{16} + \frac{A_5}{32} + \frac{A_6}{64} + \frac{A_7}{128} + \frac{A_8}{256} \right)$$

$$\text{where } K \cong \frac{V_{REF}}{R_{14}}$$

# But, $V_{out}$ is not fixed!

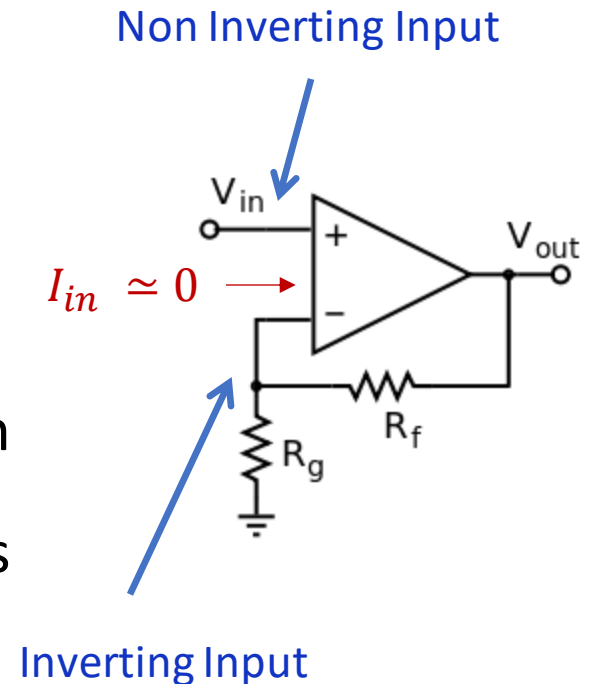
- $V_o = I_o * \frac{R_s + R_L}{R_s * R_L}$
- How to convert the fixed  $I_o$  current to a fixed voltage?



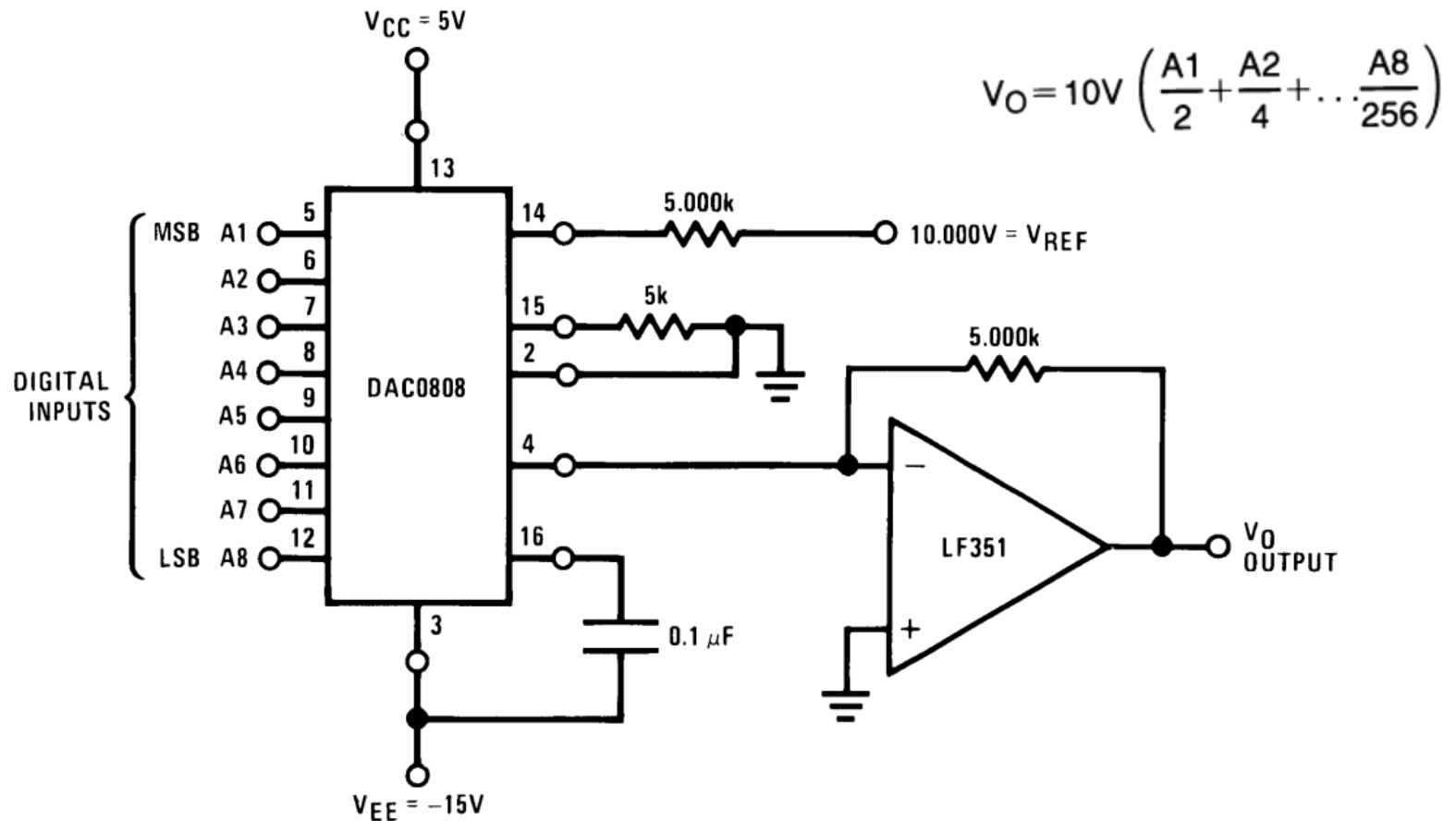


# Operational amplifier (OpAmp)

- Negative feedback OpAmp
- \*\* OpAmp has high impedance at input and low impedance at output**
- Behavior at equilibrium
  - “Equilibrium will be established when  $V_{out}$  is just sufficient to pull the inverting input to the same voltage as  $V_{in}$ ”
- **E.g., if  $R_g == R_f$  and  $V_{in} == 1V$  then  $V_{out} == 2V$**



# +10V Output Digital to Analog Converter

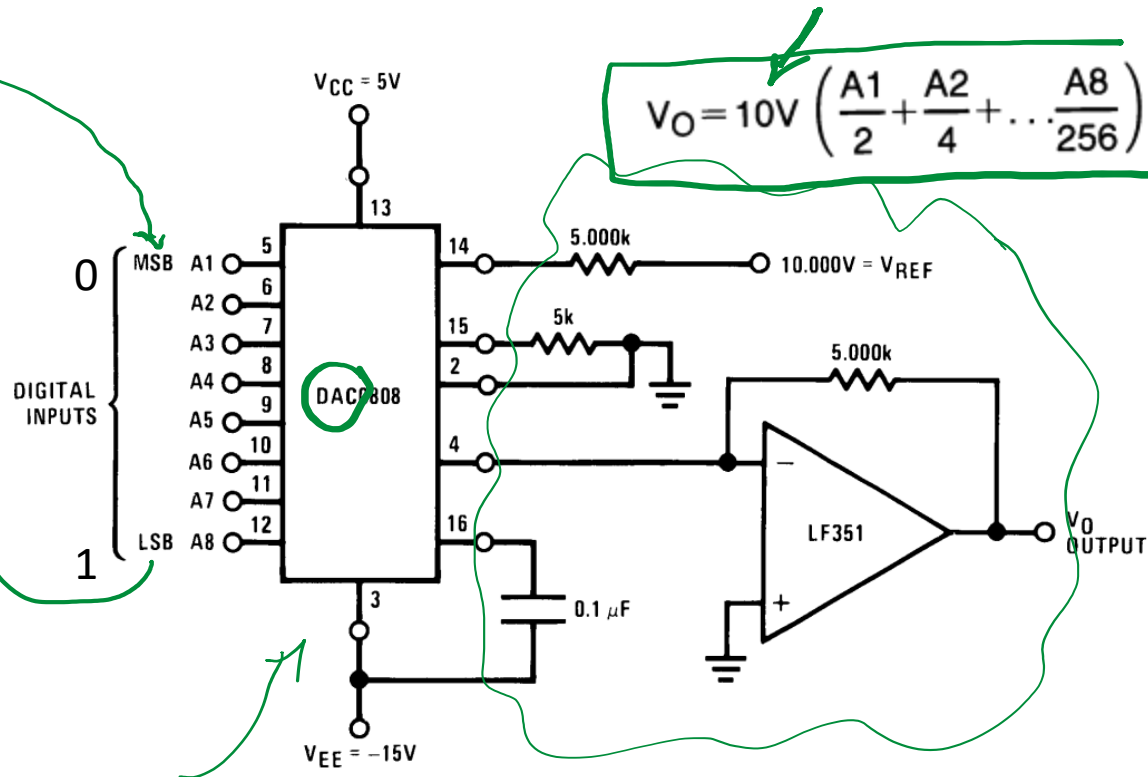


Source: DAC0808 datasheet

# Example of Digital to Analog Conversion

- Considering the following +10V DAC, fill in the table:

Digital In [MSB .. LSB]	Vo (V)
0x00	0
0x01	10/256
0x13=0b00010011	10 ( $1/2^4 + 1/2^7 + 1/2^8$ )
0xF0	$10(1/2 + 1/4 + 1/8 + 1/16)$
0xFF	



# Example: Step Ramp

```
int step_ramp(int num_steps)
```

```
{
```

```
    unsigned int ramp = 0;
```

```
    for (ramp = 0; ramp < num_steps; i++ ) {
```

```
        uint32_t val = *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL);
```

```
        val &= 0xFFFFF00;
```

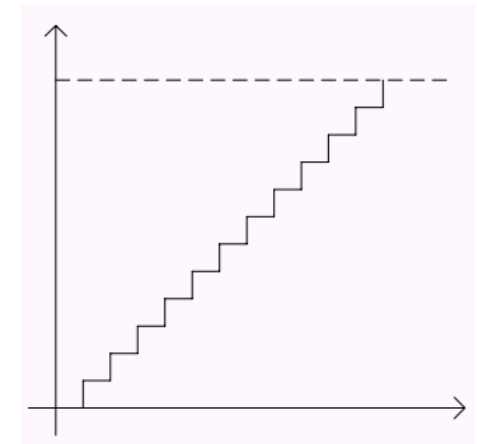
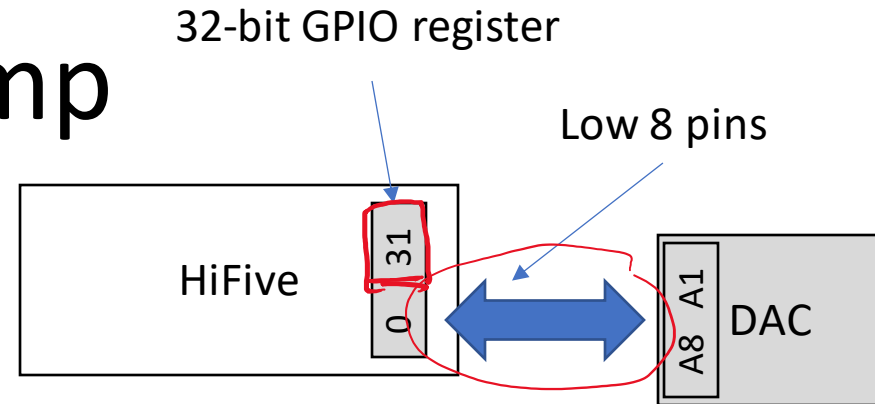
```
        val |= ramp;
```

```
        *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL) = val;
```

```
    }
```

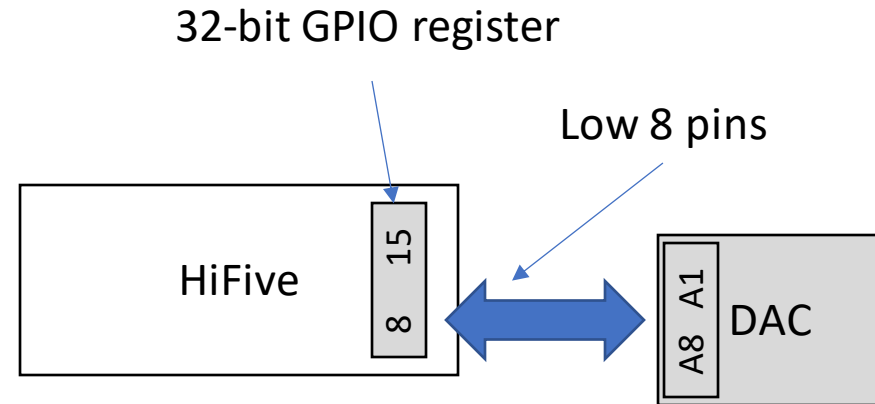
```
    return 0 ;
```

```
}
```



# Example: in class

```
uint32_t dac = 0xFFFF40FF
```



```
uint32_t val = *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL);
```

```
val = val & 0xFFFF00FF; //clear [15-8]
```

```
val = val & dac; //set
```

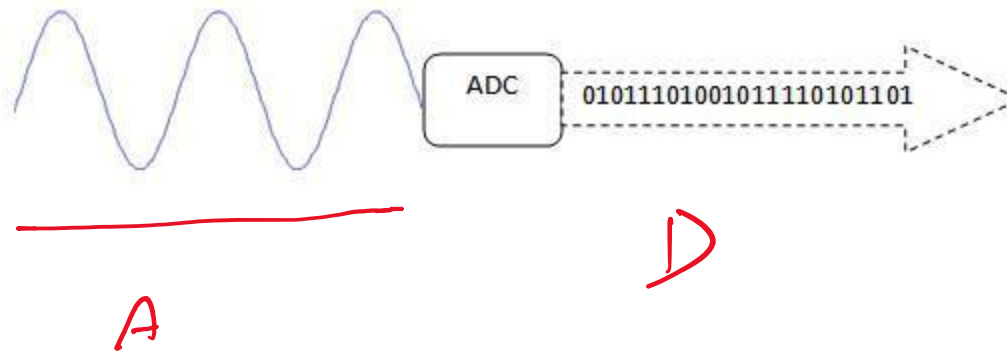
```
*(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL) = val;
```

# Bit Manipulation in C

- Setting a bit in a binary number
- Re-setting a bit a binary number
- Updating a bit a binary number
- Updating a range in a binary number
- Look at “Bonus Lecture - Bit Manipulation in C.pptx”

# Analog to Digital Convertor

- Periodically samples the input and converts it to an unsigned number proportional to the input




256




16



# Vref/2 Relation to Vin Range

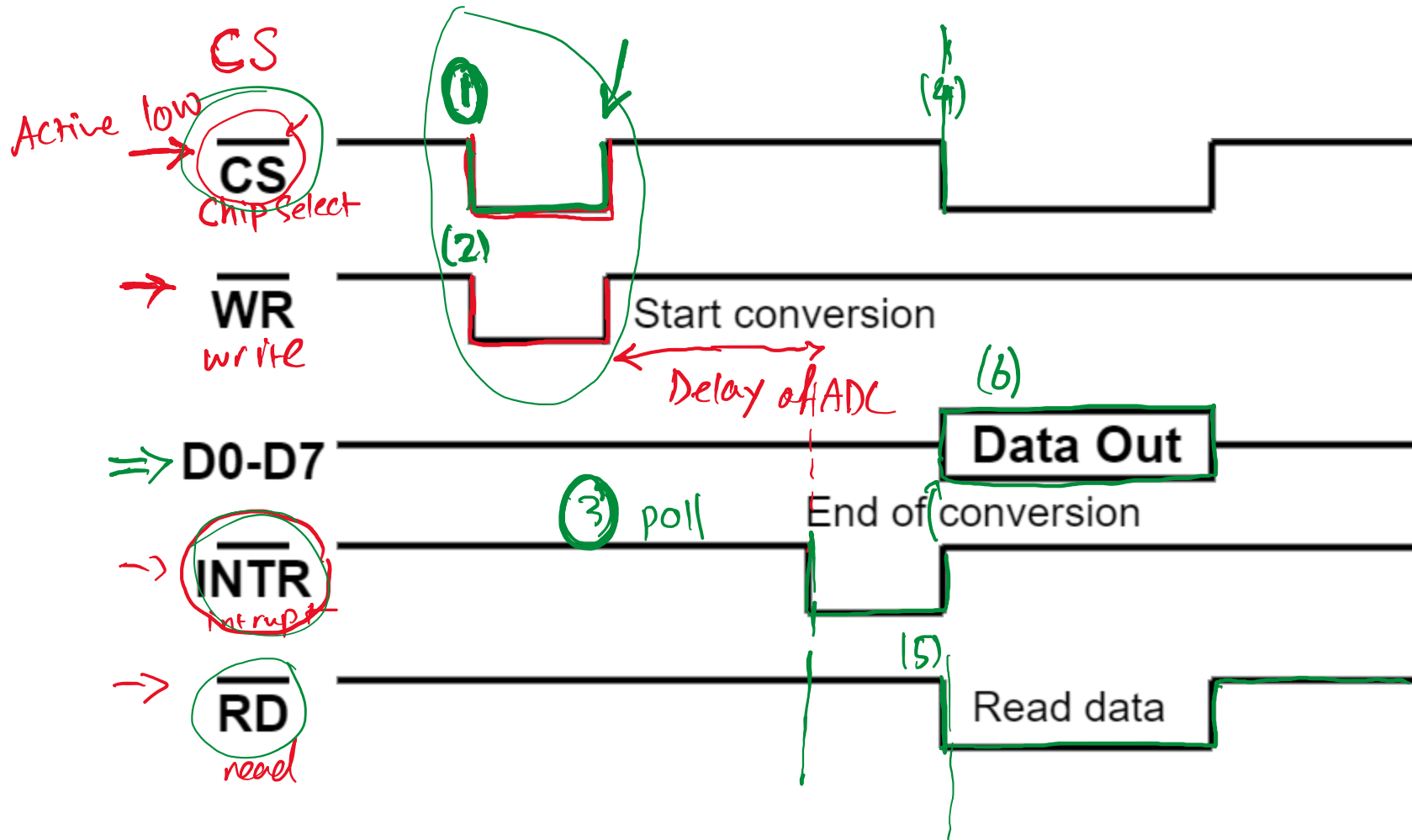


Vref/2 (V)	Vin (V)	Step Size (mV)
<del>Not connected</del>	0 to 5	$5/256 = 19.53$
2	<u>0 to 4</u>	$4/256 = 15.62$
1.5	0 to 3	$3/256 = 11.71$
1.28	0 to 2.56	$2.56/256 = 10$
1	0 to 2	$2/256 = 7.81$
0.5	0 to 1	<u><math>1/256 = 3.90</math></u>



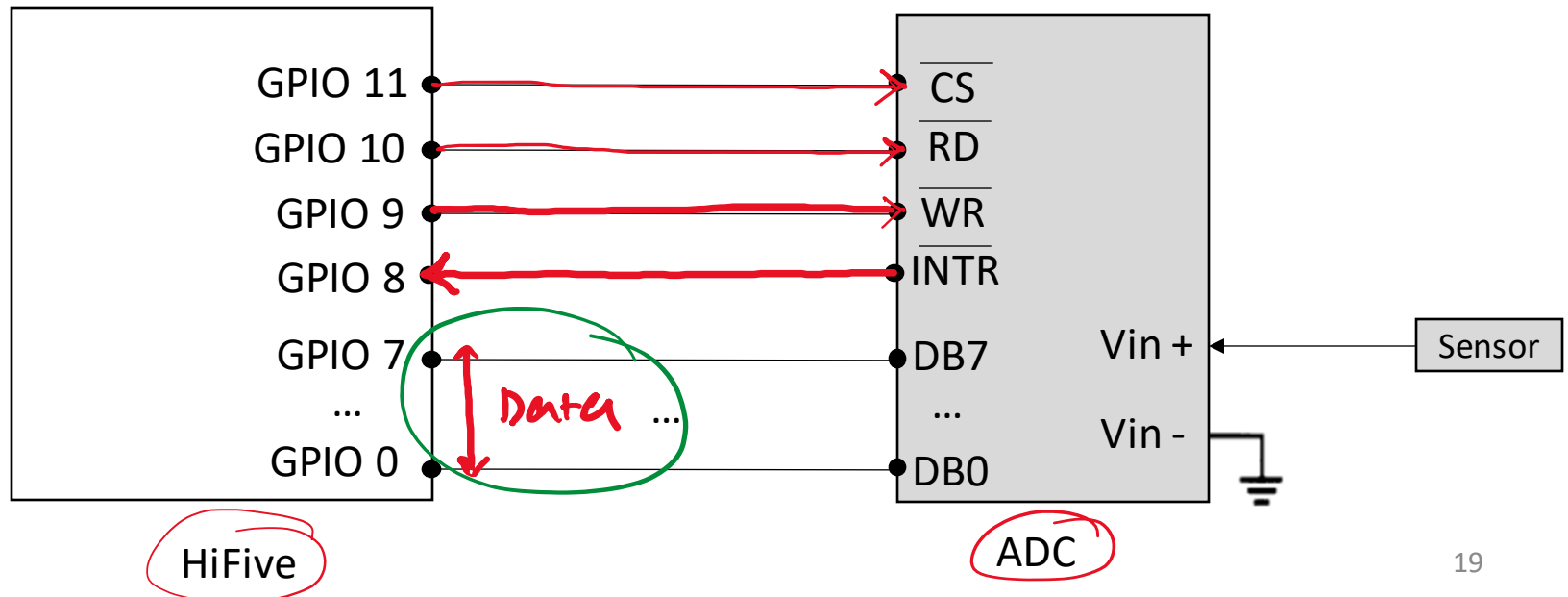
# Timing

Hand shaking protocol



# Example

- Write a C function that reads one digital sample from the connected sensor. Timing of the ADC is shown in the previous slide. Assume that GPIO pins 0 to 8 are already configured as input, and pins 9, 10, 11 are configured as output. Use `GPIO_CTRL_ADDR` and `GPIO_OUTPUT_VAL` parameters.



```

#define CS 11 ←
#define RD 10
#define WR 9 ←
#define INTR 8 ←

uint8_t func(){
    uint32_t val = *(volatile uint32_t *) (GPIO_CTRL_ADDR +
    GPIO_OUTPUT_VAL);
    val = val & ~(1 << CS); (1) ←
    val = val & ~(1 << WR); (2)
    *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL) = val; ⇐
    do {
        (3)
        val = *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL);
    }
    while (val & 1 << INTR);
    val = val & ~(1 << CS); (4)
    val = val & ~(1 << RD); (5)
    *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL) = val; ←
    val = *(volatile uint32_t *) (GPIO_CTRL_ADDR + GPIO_OUTPUT_VAL); ←
    return (uint8_t) val; 8 0x00 00 00 ff;
}

```