

| Base | Top | Attr. | Description | Notes |
|-------------|-------------|-------|------------------------|------------------------------|
| 0x0000_0000 | 0x0000_0FFF | RWX A | Debug | Debug Address Space |
| 0x0000_1000 | 0x0000_1FFF | R XC | Mode Select | On-Chip Non Volatile Memory |
| 0x0000_2000 | 0x0000_2FFF | | Reserved | |
| 0x0000_3000 | 0x0000_3FFF | RWX A | Error Device | |
| 0x0000_4000 | 0x0000_FFFF | | Reserved | |
| 0x0001_0000 | 0x0001_1FFF | R XC | Mask ROM (8 KiB) | |
| 0x0001_2000 | 0x0001_FFFF | | Reserved | |
| 0x0002_0000 | 0x0002_1FFF | R XC | OTP Memory Region | |
| 0x0002_2000 | 0x001F_FFFF | | Reserved | |
| 0x0200_0000 | 0x0200_FFFF | RW A | CLINT | On-Chip Peripherals |
| 0x0201_0000 | 0x07FF_FFFF | | Reserved | |
| 0x0800_0000 | 0x0800_1FFF | RWX A | E31 ITIM (8 KiB) | |
| 0x0800_2000 | 0x0BFF_FFFF | | Reserved | |
| 0x0C00_0000 | 0x0FFF_FFFF | RW A | PLIC | |
| 0x1000_0000 | 0x1000_0FFF | RW A | AON | |
| 0x1000_1000 | 0x1000_7FFF | | Reserved | |
| 0x1000_8000 | 0x1000_8FFF | RW A | PRCI | |
| 0x1000_9000 | 0x1000_FFFF | | Reserved | |
| 0x1001_0000 | 0x1001_0FFF | RW A | OTP Control | |
| 0x1001_1000 | 0x1001_1FFF | | Reserved | |
| 0x1001_2000 | 0x1001_2FFF | RW A | GPIO | |
| 0x1001_3000 | 0x1001_3FFF | RW A | UART 0 | |
| 0x1001_4000 | 0x1001_4FFF | RW A | QSPI 0 | |
| 0x1001_5000 | 0x1001_5FFF | RW A | PWM 0 | |
| 0x1001_6000 | 0x1001_6FFF | RW A | I2C 0 | |
| 0x1001_7000 | 0x1002_2FFF | | Reserved | |
| 0x1002_3000 | 0x1002_3FFF | RW A | UART 1 | |
| 0x1002_4000 | 0x1002_4FFF | RW A | SPI 1 | |
| 0x1002_5000 | 0x1002_5FFF | RW A | PWM 1 | |
| 0x1002_6000 | 0x1003_3FFF | | Reserved | |
| 0x1003_4000 | 0x1003_4FFF | RW A | SPI 2 | |
| 0x1003_5000 | 0x1003_5FFF | RW A | PWM 2 | |
| 0x1003_6000 | 0x1FFF_FFFF | | Reserved | |
| 0x2000_0000 | 0x3FFF_FFFF | R XC | QSPI 0 Flash (512 MiB) | Off-Chip Non-Volatile Memory |
| 0x4000_0000 | 0x7FFF_FFFF | | Reserved | On-Chip Volatile Memory |
| 0x8000_0000 | 0x8000_3FFF | RWX A | E31 DTIM (16 KiB) | |
| 0x8000_4000 | 0xFFFF_FFFF | | Reserved | |

Table 4: FE310-G002 Memory Map. Memory Attributes: **R** - Read, **W** - Write, **X** - Execute, **C** - Cacheable, **A** - Atomics