



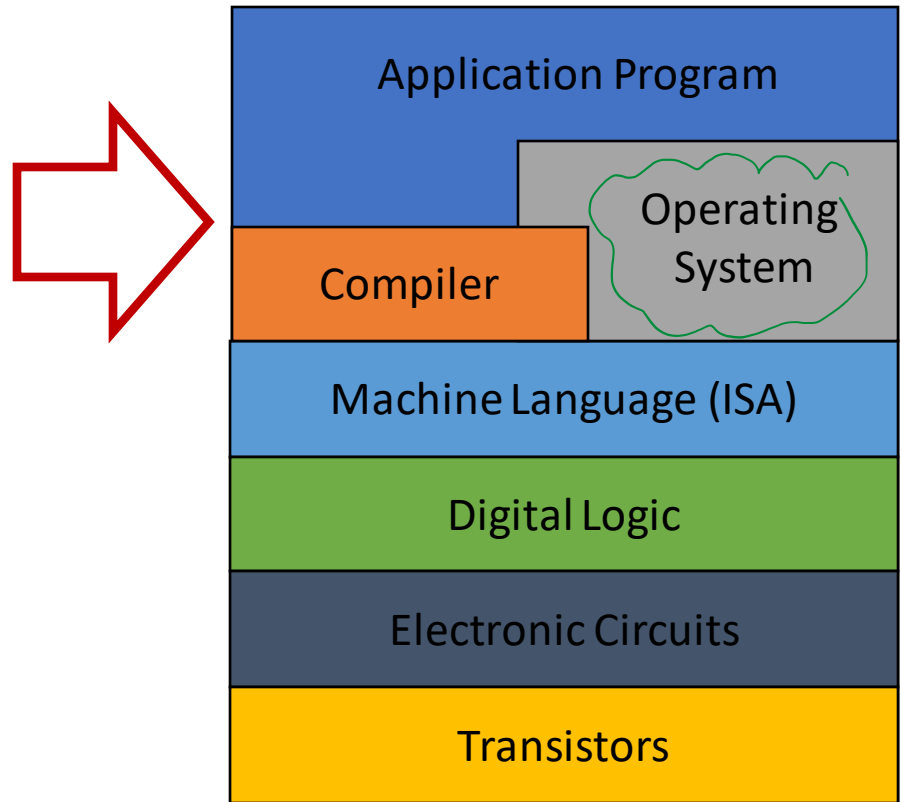
Operating System

EECS388 Fall 2022

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Context

- Recommended reading
Chapter 9 of “Introduction to Computing,” Patt, Patel



TRAP (System Call)



- TRAP calls the operating system to do something
 - E.g., access I/O or Halt
- But what is an operating system?

Operating System

- Microsoft windows, MacOS, Linux, Android, etc.
- Goal: efficiently share resources amongst users while ensuring the users does not do harmful things to any program or data stored in memory
- We need to know three concepts: Privilege, priority, memory address space

Privilege and Priority

- Two concepts associated with processing instructions
- Privilege
 - Right to do something
- Priority
 - Urgency of doing something

Privilege

- Right to execute an instruction or access a memory address
 - E.g., if a computer is shared among users, we don't want all users to be able to execute HALT instruction
 - Or we don't want all users to access all memory addresses and cause system to crash
- Each program is either *privileged* or *unprivileged*

Supervisor vs. User Processing Modes

- A program executing in Supervisor mode is privileged
 - Can execute all instructions and access all memory addresses
- A program executing in User mode is unprivileged
 - Cannot execute privileged instructions or access privileged memory addresses.

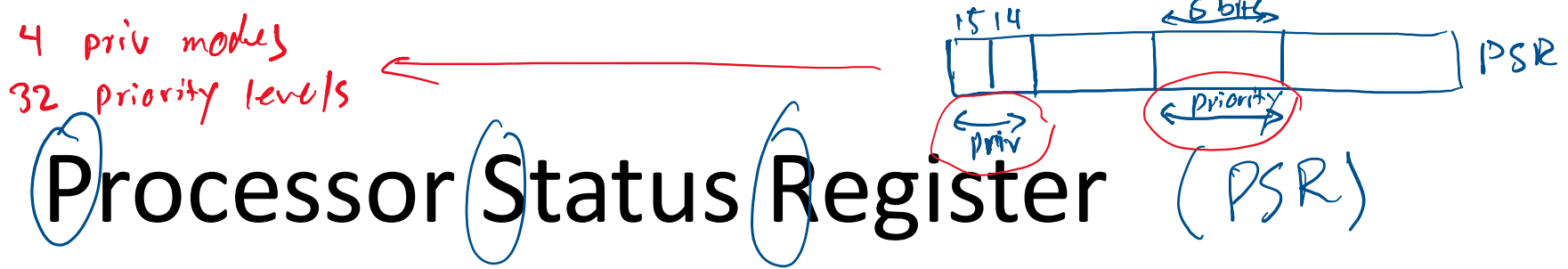
T/F: prog 1 has higher priority than
Read from key bound SR.

- ## Service routines



Privilege and Priority are Orthogonal

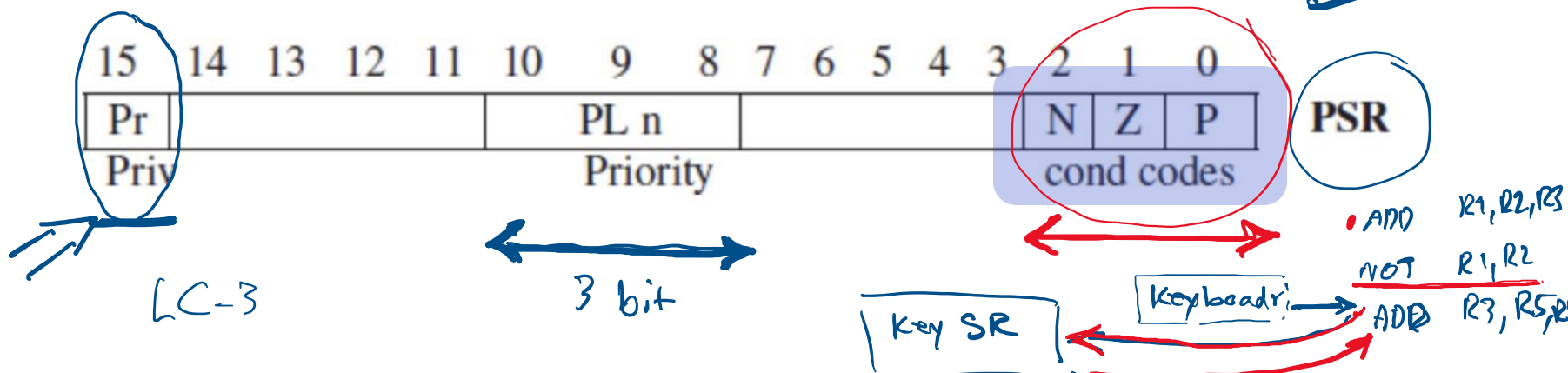
- They have nothing to do with each other!
- E.g., a program reading sensor value in a nuclear reactor has more priority than reading from keyboard.
 - Sensor read: unprivileged, high priority
 - Keyboard service routine: privileged, low priority



- Each program executing on a computer is associated with two registers: PC and

PSR

privilege modes in LC3
User
Supervisor



We will see why PSR need to include CC's later in the course

Organization of Memory

Contains data structures and code of the OS

System SP
SSP →

x0000
x3000
x3000

System Space

Supervisor Stack

User Space

User Stack

I/O Page

USP →

xFE00
xFE00
xFFFF

x0000
x00FF
x0100
x01FF

Trap Vector Table

Interrupt Vector Table

SR

Privileged Memory

.ORIG xFF00
ADD R1, R2, R3
;

User Space Memory

Privileged Memory

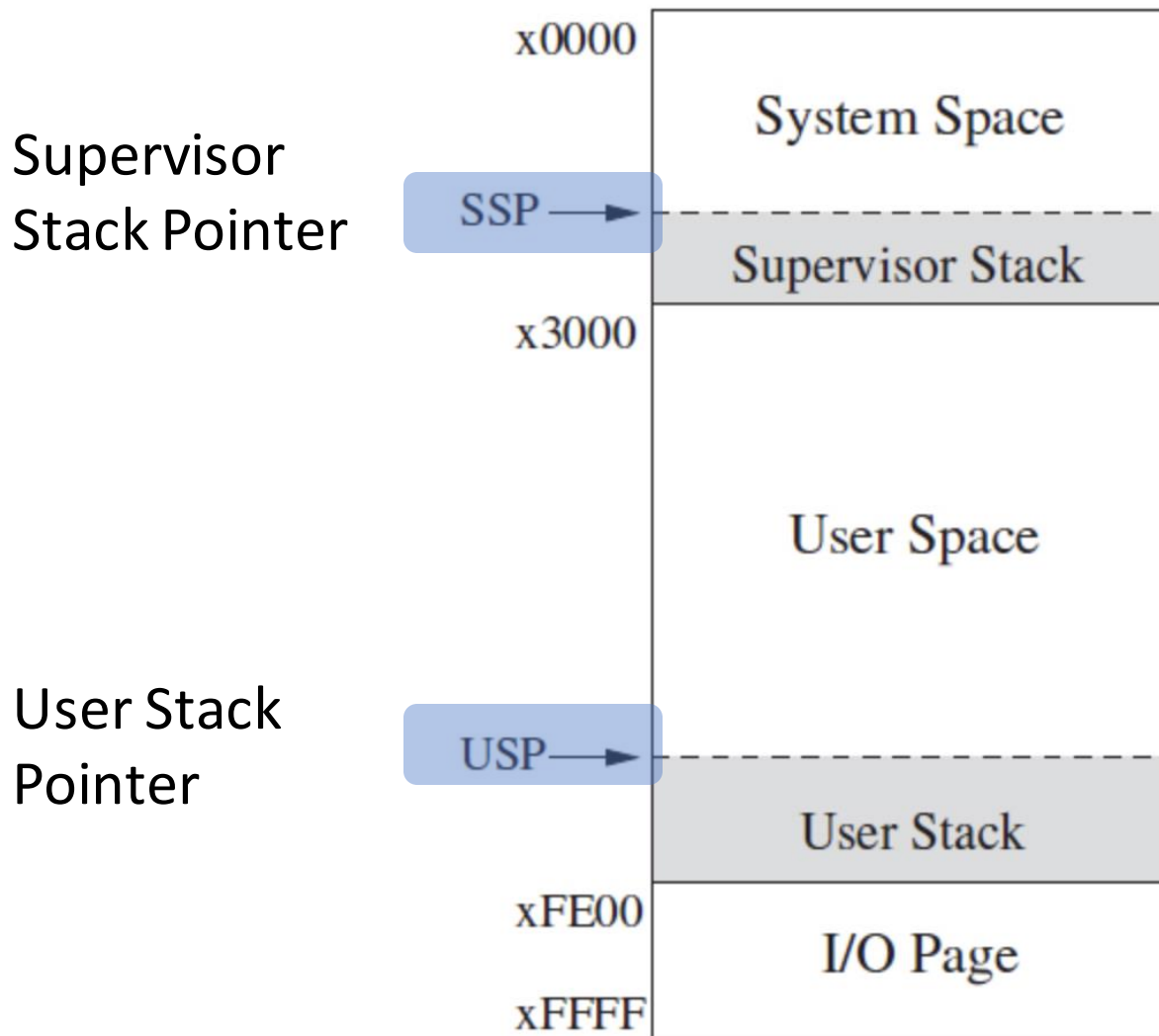
LL-3

User Stack pointed MMIO

Memory mapped registers and I/O

e.g., PSR is at address xFFFC

Organization of Memory

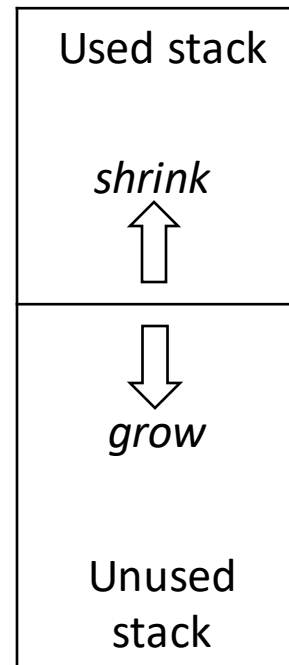
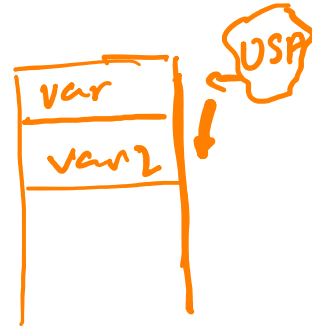


Stack

FILO

- Temporary storage
 - For functions
- Grow/shrink dynamically
 - Call a function → grow
 - Exit a function → shrink

```
func () {  
    int var;  
    ? var2  
    return;  
}
```



Recap

- Operating System Concept
- Privilege and Priority
- Memory Address Space