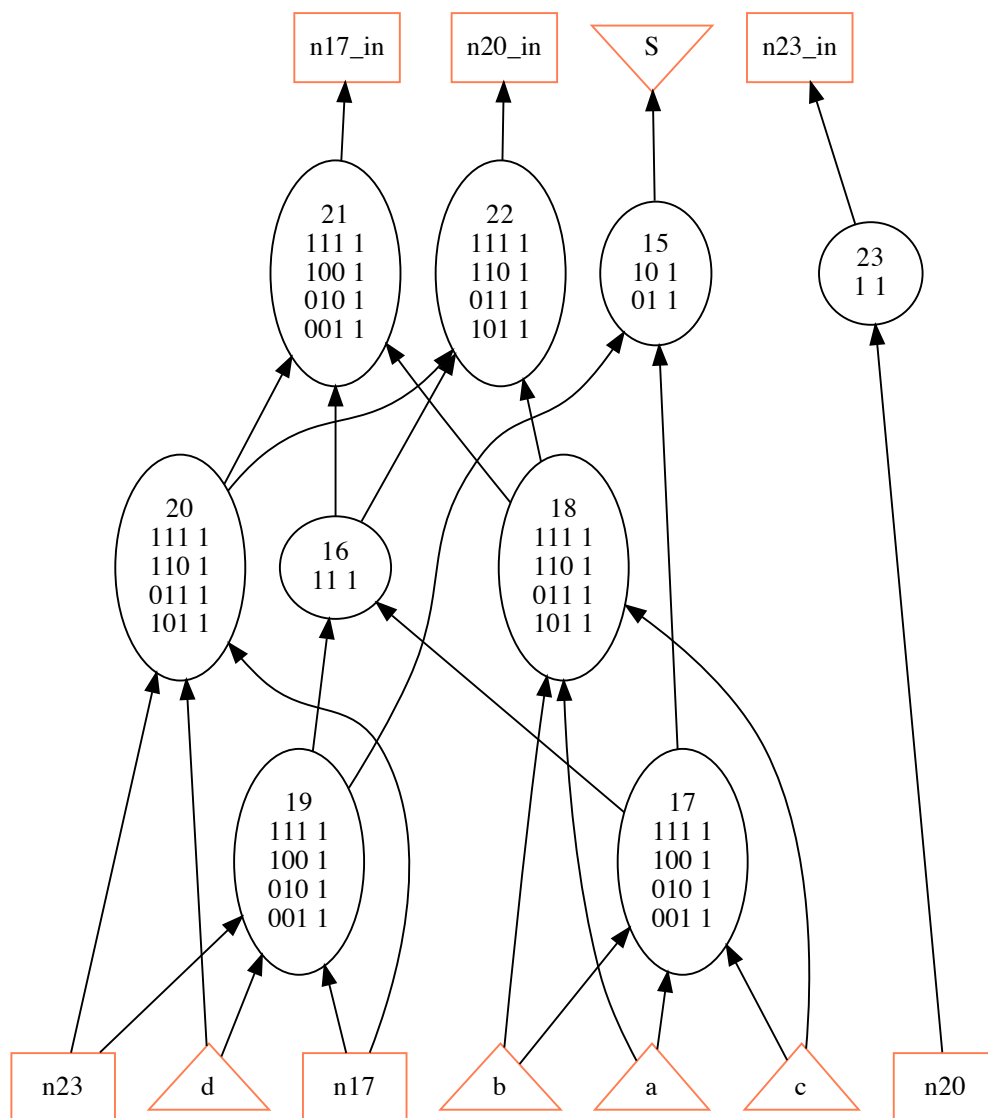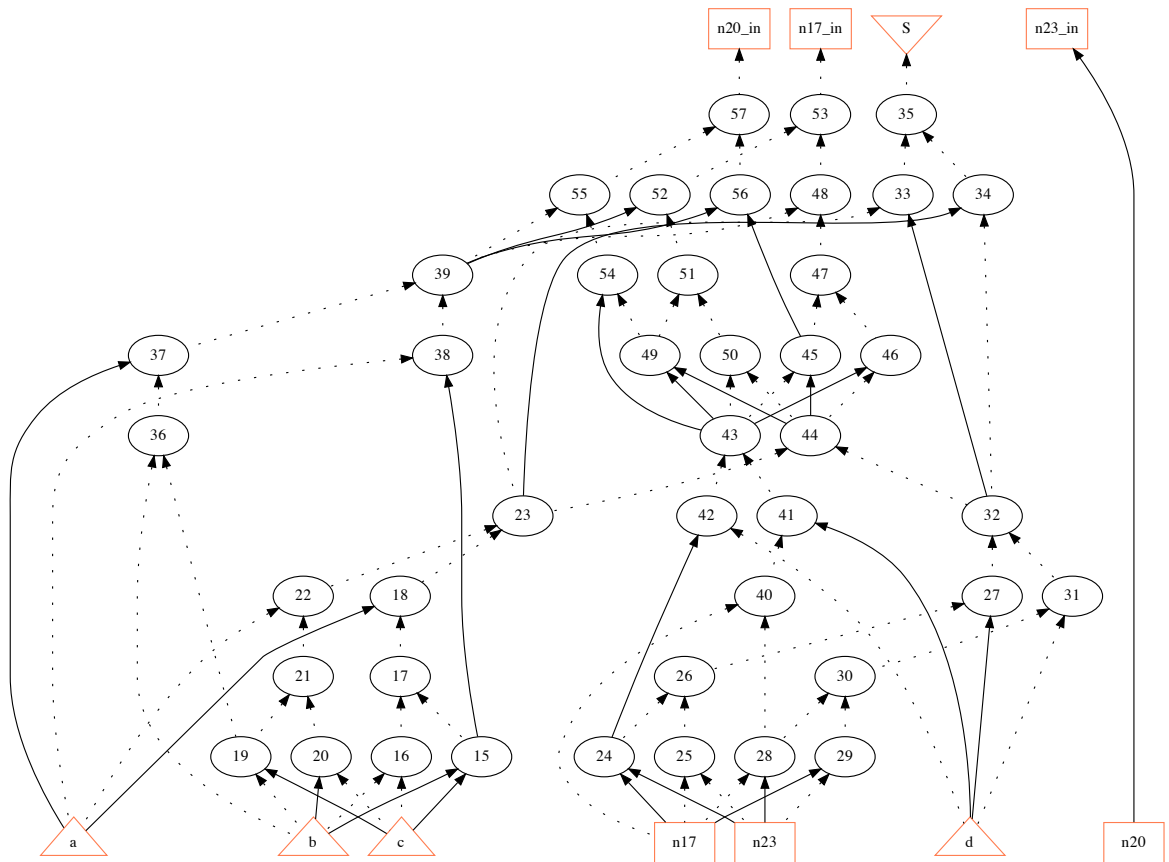# Report

B07901067 電機四 吳浩瑜

Part1

Step3

The network contains 9 logic nodes and 3 latches.
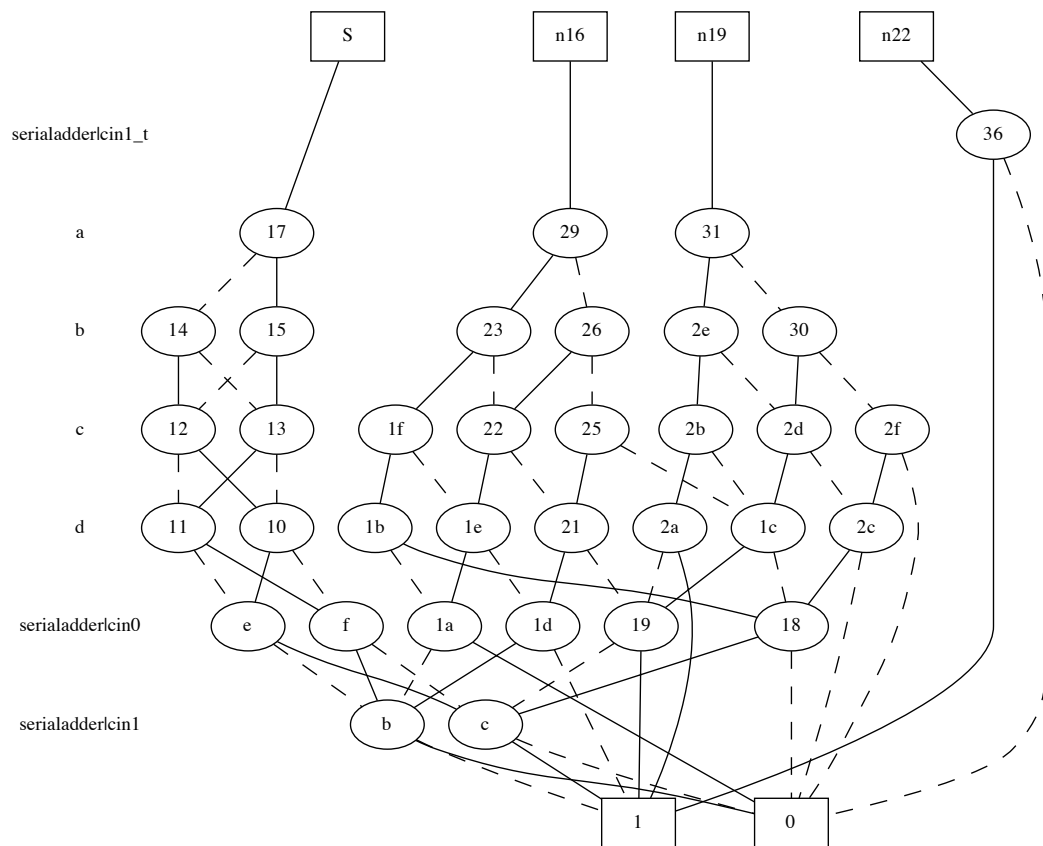
Step5

The network contains 43 logic nodes and 3 latches.
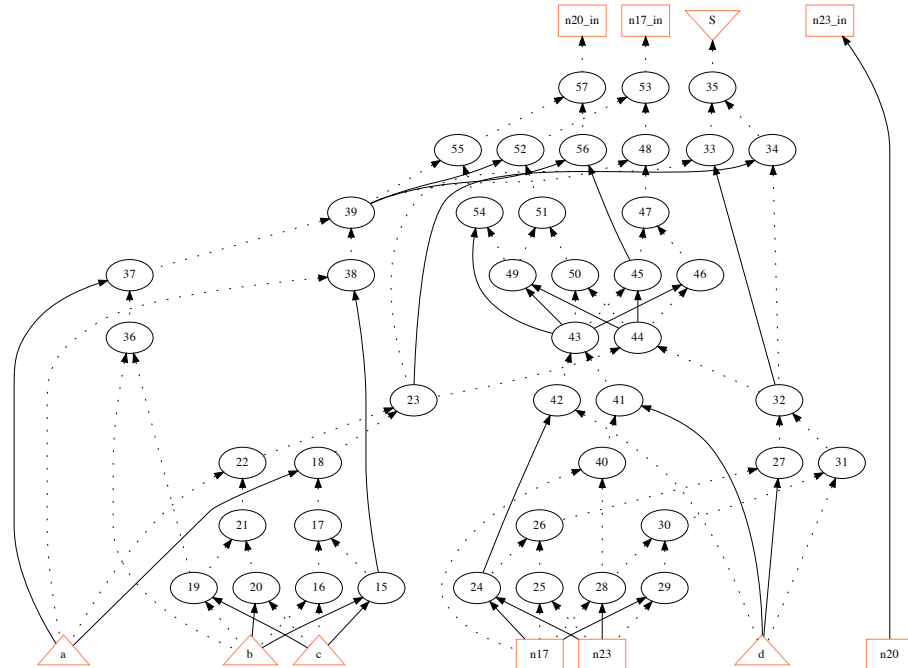
Step7

serialadder|cin1_t
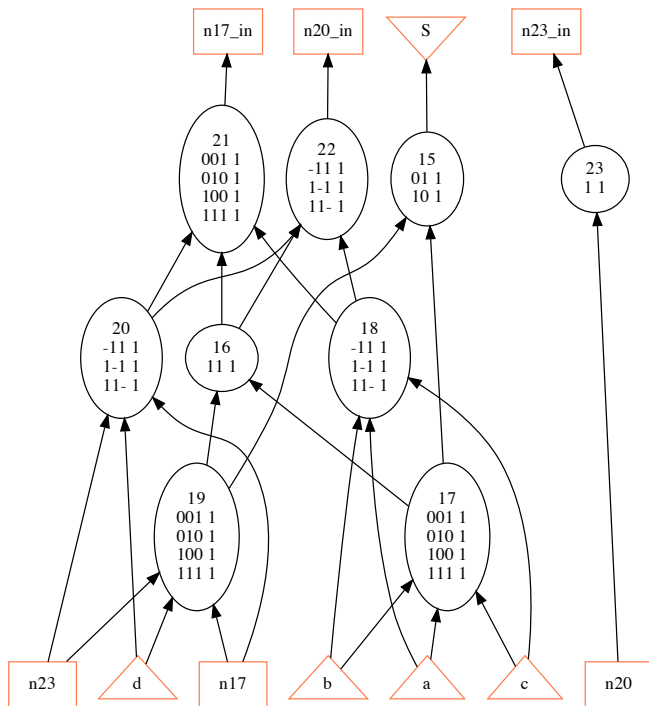
a

b

c

d

serialadder|cin0

serialadder|cin1

S   n16   n19   n22

Part2

(a) 1.

Command aig

Command strash

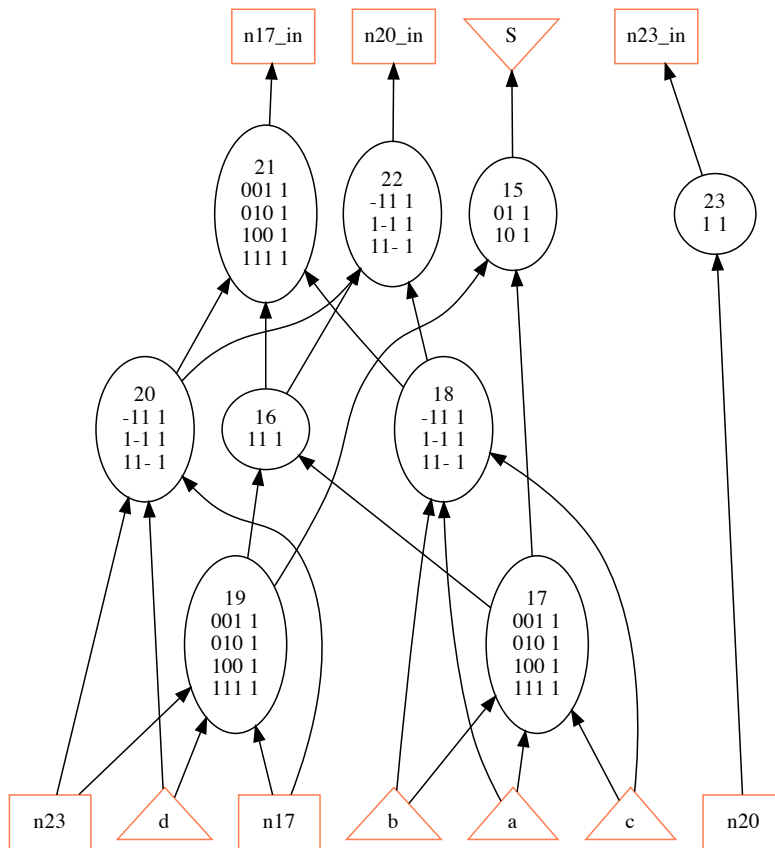The network contains 9 logic nodes and 3 latches.

After command **aig**, it looks nothing different with the one in step3.
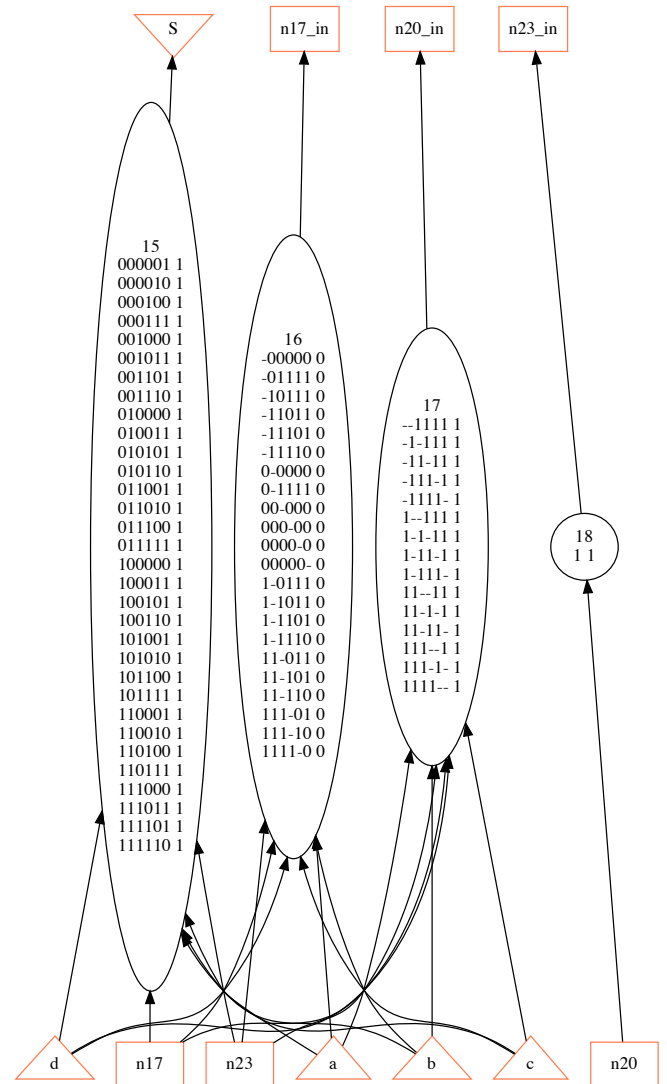
But **strash** do convert it to an AIG.

(a) 2.

Command bdd&show
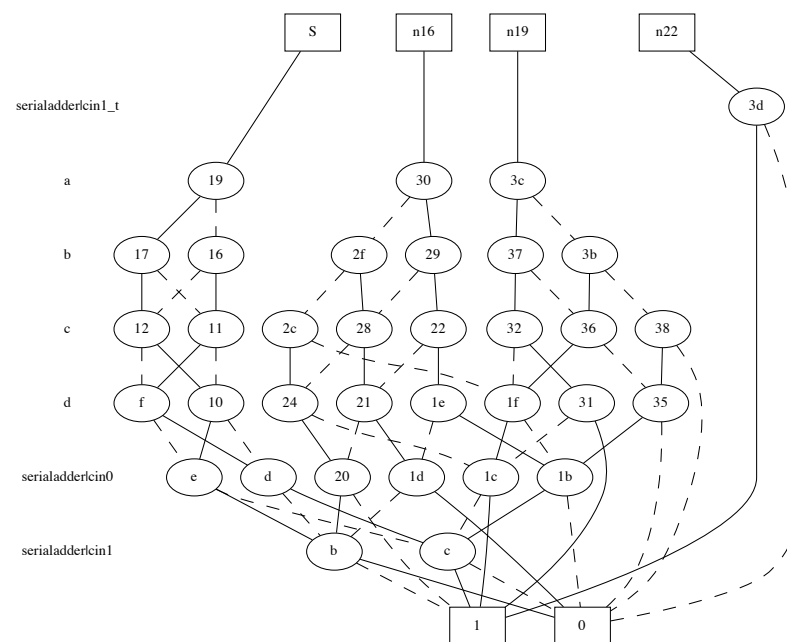
The network contains 9 logic nodes and 3 latches.
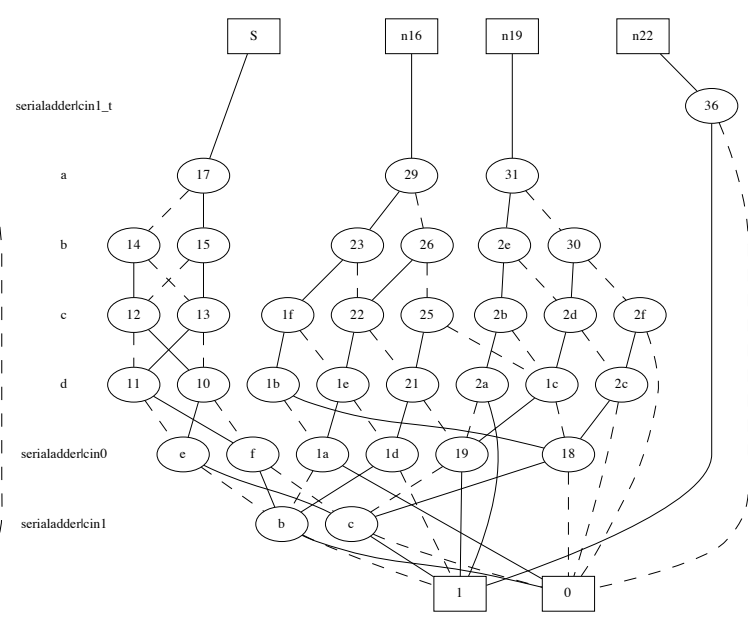

Command collapse&show

The network contains 4 logic nodes and 3 latches.

Command bdd&show_bdd -g   Command collapse&show_bdd -g

serialadderlcin1_t

a

b

c

d

serialadderlcin0

serialadderlcin1

Command **collapse** would destroy all the nodes in the graph.

So we have only one big node for each output after **collapse.**

(b)

read the file->strash->renode->sop

The network contains 35 logic nodes and 3 latches.