

# DSAR IRAM Package Alpha Release 107\_4\_2\_Candidate1

## **General**

This IRAM package includes the following main features: Custom Classification (CC), Independent-Mode (IM), Host-Commands (HC) and Deep Sleep Auto Response (DSAR). For DSAR the following features are supported: ARP, ICMP, ND and SNMP. For more information see FMan-Controller RM chapter.

## **Availability**

The package is currently available for the following devices.

**Table 1. Package Availability by Device**

Device	Version Number	Compiler version	Loader file name (.h .bin)
<a href="#">T1040 rev 1.0</a>	107_4_2_Candidate1	—	t1040_r1.0.h fsl_fman_ucose_t1040_r1.0_107_4_2_Cand idate1.bin

## Revision History

**Table 2. Revision History for Alpha Release 107.4.2 Candidate1**

Release Date: May 29, 2014	
<b>New Features</b>	None.
<b>New Features (Not in spec)</b>	None.
<b>Spec Un-Supported Features</b>	<p>The following IP Acceleration features are not supported in this package:</p> <p>IP fragmentation, IP reassembly, Header Manipulation, Frame Replication.</p> <p>Please note that CC STD (statistics table descriptor) is supported.</p>
<b>Bug Fixes/CCB</b>	<p>This release is a result of integration with Linux environment and full power down management system testing. The following are the work-arounds implemented:</p> <ul style="list-style-type: none"> <li>• Set FMFP_BRKC[SLP] bit just before event of wake-up.</li> <li>• Eliminate internal syncbefore transmitting AR packets.</li> </ul>
<b>Known Issues</b>	<p>The following FMD updates to the deep sleep transition steps are:</p> <ul style="list-style-type: none"> <li>• The Auto-Response TX IM port is enabled during deep sleep and its FMBM_TFDNE set to NIA 0x005000C0 (BMI Release).</li> <li>• The FMFP_BRKC[SLP] is not set by the FMD instead it is set in firmware.</li> </ul>
<b>Restrictions</b>	None.

**Table 3. Revision History for Alpha Release 107.4.1**

Release Date: May 13, 2014	
<b>New Features</b>	None.
<b>New Features (Not in spec)</b>	None.
<b>Spec Un-Supported Features</b>	<p>The following IP Acceleration features are not supported in this package:</p> <p>IP fragmentation, IP reassembly, Header Manipulation, Frame Replication.</p> <p>Please note that CC STD (statistics table descriptor) is supported.</p>

Release Date: May 13, 2014	
<b>Bug Fixes/CCB</b>	<p>Following Errata were fixed:</p> <p>DSAR1:<b>SNMP</b>: Intermittent incorrect UDP checksum on response packet.</p> <p>DSAR2:<b>SNMP</b>: The beginning of the frame is corrupted for the NOSUCHNAME response</p> <p>DSAR3:<b>SNMP</b>: The UDP checksum of the NOSUCHNAME response is not correct if response frame size exceed 256 bytes.</p> <p>DSAR4:<b>SNMP</b>: The Active Mode Hardware Parser NIA is not invoked for the case of set-request message is received.</p> <p>DSAR5:<b>VLAN</b> tagged frames are not matched correctly in the IP address tables. Frames with VLAN tags are considered mismatch in the IP/VLAN matching.</p>
<b>Known Issues</b>	None.
<b>Restrictions</b>	None.

**Table 4. Revision History for Alpha Release 107.4.0**

Release Date: March 30, 2014	
<b>New Features</b>	Deep Sleep Auto-Response: ARP, ICMP, ND and SNMP. Also provide AR Pass filter.
<b>New Features for B4860/T4240</b>	See above.
<b>New Features (Not in spec)</b>	None.
<b>Spec Un-Supported Features</b>	<p>The following IP Acceleration features are not supported in this package:</p> <p>IP fragmentation,</p> <p>IP reassembly,</p> <p>Header Manipulation,</p> <p>Frame Replication.</p> <p>Please note that CC STD (statistics table descriptor) is supported.</p>
<b>Bug Fixes/CCB</b>	None.
<b>Known Issues</b>	None.
<b>Restrictions</b>	None.

## **How to Reach Us:**

### **Home Page:**

[www.freescale.com](http://www.freescale.com)

### **Web Support:**

<http://www.freescale.com/support>

### **USA/Europe or Locations Not Listed:**

Freescale Semiconductor, Inc.  
Technical Information Center, EL516  
2100 East Elliot Road  
Tempe, Arizona 85284  
1-800-521-6274 or  
+1-480-768-2130  
[www.freescale.com/support](http://www.freescale.com/support)

### **Europe, Middle East, and Africa:**

Freescale Halbleiter Deutschland GmbH  
Technical Information Center  
Schatzbogen 7  
81829 Muenchen, Germany  
+44 1296 380 456 (English)  
+46 8 52200080 (English)  
+49 89 92103 559 (German)  
+33 1 69 35 48 48 (French)  
[www.freescale.com/support](http://www.freescale.com/support)

### **Japan:**

Freescale Semiconductor Japan Ltd.  
Headquarters  
ARCO Tower 15F  
1-8-1, Shimo-Meguro, Meguro-ku  
Tokyo 153-0064  
Japan  
0120 191014 or  
+81 3 5437 9125  
[support.japan@freescale.com](mailto:support.japan@freescale.com)

### **Asia/Pacific:**

Freescale Semiconductor China Ltd.  
Exchange Building 23F  
No. 118 Jianguo Road  
Chaoyang District  
Beijing 100022  
China  
+86 10 5879 8000  
[support.asia@freescale.com](mailto:support.asia@freescale.com)

### **For Literature Requests Only:**

Freescale Semiconductor  
Literature Distribution Center  
1-800 441-2447 or  
+1-303-675-2140  
Fax: +1-303-675-2150  
[LDCForFreescaleSemiconductor@hibbertgroup.com](mailto:LDCForFreescaleSemiconductor@hibbertgroup.com)

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale are trademarks or registered trademarks of Freescale Semiconductor, Inc. in the U.S. and other countries. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc., 2008, 2009. All rights reserved.