DSAR IRAM Package Alpha Release 107_4_2

General

This IRAM package includes the following main features: Custom Classification (CC), Independent-Mode (IM), Host-Commands (HC) and Deep Sleep Auto Response (DSAR). For DSAR the following features are supported: ARP, ICMP, ND and SNMP. For more information see FMan-Controller RM chapter.

Availability

The package is currently available for the following devices.

Table 1. Package Availability by Device

Device	Version Number	Compiler version	Loader file name (.h .bin)
T1040 rev 1.0	107_4_2	1	t1040_r1.0.h fsl_fman_ucode_t1040_r1.0_107_4_2.bin



Revision History

Table 2. Revision History for Alpha Release 107.4.2

Release Date: June 24, 2014		
New Features	None.	
New Features (Not in spec)	None.	
Spec Un-Supported Features	The following IP Acceleration features are not supported in this package: IP fragmentation, IP reassembly, Header Manipulation, Frame Replication. Please note that CC STD (statistics table descriptor) is supported.	
Bug Fixes/CCB	Following Errata were fixed: DSAR6 SNMP: Varbind with length field span between 256 bytes blocks cause varbind interpreted incorrectly. DSAR7 SNMP: SNMP: If the community string is larger than the remaining byte length next to the community string, community mismatch may occur even the string matches. DSAR8 SNMP: Community mismatch may occur if the community string is located across 256 bytes boundary of the frame. DSAR9 SNMP: Incorrect interpretation on length field if the length is 2-bytes (i.e. 256~65535) and located across 256 bytes boundary of the SNMP frame.	
Known Issues	None.	
Restrictions	None.	

Table 3. Revision History for Alpha Release 107.4.1

Release Date: May 13, 2014		
New Features	None.	
New Features (Not in spec)	None.	
Spec Un-Supported Features	The following IP Acceleration features are not supported in this package: IP fragmentation, IP reassembly, Header Manipulation, Frame Replication. Please note that CC STD (statistics table descriptor) is supported.	

Release Date: May 13, 2014		
Bug Fixes/CCB	Following Errata were fixed: DSAR1:SNMP: Intermittent incorrect UDP checksum on response packet. DSAR2:SNMP: The beginning of the frame is corrupted for the NOSUCHNAME response DSAR3:SNMP: The UDP checksum of the NOSUCHNAME response is not correct if response frame size exceed 256 bytes. DSAR4:SNMP: The Active Mode Hardware Parser NIA is not invoked for the case of set-request message is received. DSAR5:VLAN tagged frames are not matched correctly in the IP address tables. Frames with VLAN tags are considered mismatch in the IP/VLAN matching.	
Known Issues	None.	
Restrictions	None.	

Table 4. Revision History for Alpha Release 107.4.0

Release Date: March 30, 2014		
New Features	Deep Sleep Auto-Response: ARP, ICMP, ND and SNMP. Also provide AR Pass filter.	
New Features for B4860/T4240	See above.	
New Features (Not in spec)	None.	
Spec Un-Supported Features	The following IP Acceleration features are not supported in this package: IP fragmentation, IP reassembly, Header Manipulation, Frame Replication. Please note that CC STD (statistics table descriptor) is supported.	
Bug Fixes/CCB	None.	
Known Issues	None.	
Restrictions	None.	

How to Reach Us:

Home Page:

www.freescale.com

Web Support:

http://www.freescale.com/support

USA/Europe or Locations Not Listed:

Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
1-800-521-6274 or
+1-480-768-2130
www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd. Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku Tokyo 153-0064
Japan
0120 191014 or
+81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor China Ltd. Exchange Building 23F No. 118 Jianguo Road Chaoyang District Beijing 100022 China +86 10 5879 8000 support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor
Literature Distribution Center
1-800 441-2447 or
+1-303-675-2140
Fax: +1-303-675-2150
LDCForFreescaleSemiconductor
@hibbertgroup.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale are trademarks or registered trademarks of Freescale Semiconductor, Inc. in the U.S. and other countries. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc., 2008, 2009. All rights reserved.

DSAR_107_4_2 June 24, 2014 Rev. 1

