Lab Class 1
21.09.2021

- **Frequency divider and counters.**

Problems

1. Development of an 8–bit, 1 Hz counter with an 8–LED array display, working from 0 to 255.

2. Development of an 8–bit, 1 Hz counter with an 8–LED array display, working from 0 to 9.

3. Development of an 8–bit, 10 Hz counter with an 8–LED array display, working from 0 to 9.

Additional problems

1. Development of a 10 Hz counter, working from 0 to 99, with a 2–digits BCD coding and a 2 x 4–LED array display.

2. Development of a 10 Hz down counter.