

**ARM® Cortex®-M0**  
**32-bit Microcontroller**

**NuMicro® Family**  
**M071Q/M071V Series BSP**  
**Revision History**

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### Revision 3.00.002 (Released 2023-05-19)

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1. Fix sample code build error with compiler 6.
2. Modify SYS\_UnLockReg() time-out handler.
3. Call UART FIFO size from uart.h in USB VCOM sample code.
4. Fix USB\_MassStorage\_CDROM crash on Linux.
5. Fix return in main() cause hardfault issue.
6. Add timeout handler for infinite loop.
7. Add NuMicro.h header.
8. Add SYS\_PowerDown\_MinCurrent sample code.
9. Add SPI\_TRIGGER\_TX\_RX\_PDMA and SPI\_DISABLE\_TX\_RX\_PDMA API in SPI driver.
10. Fix SC and EBI sample codes run error.
11. Fix I2C\_EEPROM, I2C\_GCMode and I2C\_Loopback failed in GCC and IAR.

### Revision 3.00.001 (Released 2020-07-10)

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1. Initial Release

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