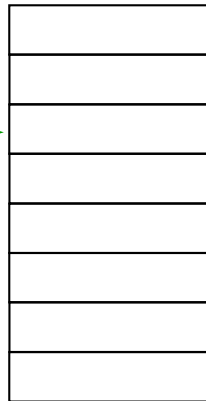


n-bit Branch Address



k-bit Branch Address

BHT



2^k entries



T/NT (1-bit)

1-bit