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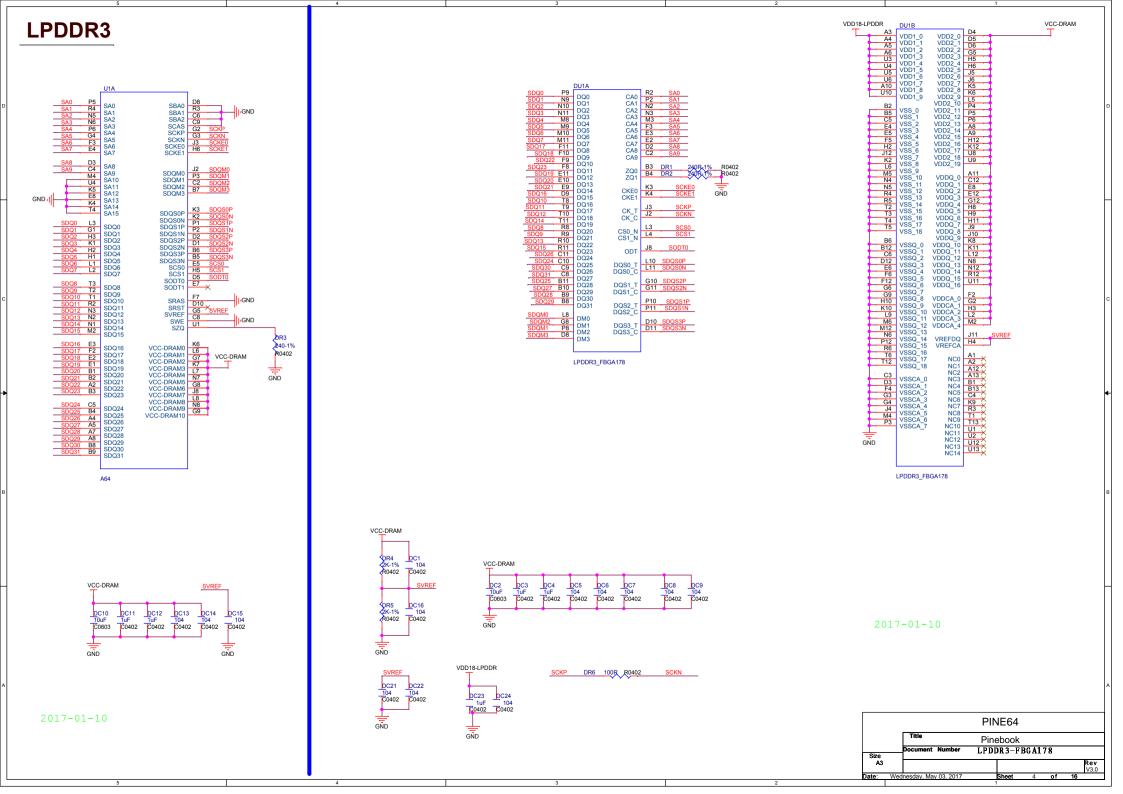
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Revision	Description	Date	Drawn	Checked	Approved
Ver 3.0	Release version	2017-05-03			

PINE64 Title Pinebook VERSION HISTORY

BLOCK DIAGRAM 24MHz DDR2/DDR3/DDR3L/LPDDR2/LPDDR3 DRAMC OSC 24M NAND/EMMC STORAGE ☐ 32768Hz OSC 32K SD/MMC0 SD/MMC CARD TWI1 SENSOR KEY KEY ADC USB2.0 Port USB2.0 OTG Capacitive Touch Screen Controller TWIO USB0 USB1 RGB LVDS A64 MIPI-DSI 3G BGA 396 AUDIO DVIDEO 2G/3G PCM0 Baseband Front camera Parallel CSI PCM1 SDIO WIFI +BT SDC1 Rear camera INTERRUPT 2G IRQ UART1 RSB UART2 **AXP803** Battery with temperature detect Voltage Supply 3.55V-4.35V PINE64 Title Pinebook BLOCK DIAGRAM Size A3

POWER TREE 5V@1A Switch for USB Host 5V@100mA MT3608 DVIDEO **AXP803** 3.5V-5V@3A PS 3.3V Nand/eMMC/SDCARD(ON)/WIFI-IO 1.6~3.4V@1.5A DC/DC1 100mΩ from DCDC1 DC1SW DC/DC2 0.5~1.3V@3A 0.5~1.3V@3A 1.1V CPUX(ON) DC/DC3 0.5~1.3V@3A DC/DC4 Battery 5V@2A 0.8~1.84V@2.5A Charger VBUS DC/DC5 1.5V DRAM(ON) 0.6~1.52V@2.5A DC/DC6 1.1V VDD-SYS(ON) Power 3.5-4.35V Detect BAT 0.7~3.3V@500mA ALD01 0.7~3.3V@300mA ALDO2 1.8V VCC-PL(ON) 0.7~3.3V@200mA 3.0V AVCC/VCC-PLL(ON) ALD03 0.7~3.3V@500mA DLD01 0.7~4.2V@400mA DLD02 0.7~3.3V@300mA DLD03 0.7~3.3V@500mA DLDO4 0.7~1.9V@400mA ELD01 1.8V CPVDD/VCC18-LPDDR 0.7~1.9V@200mA ELDO2 0.7~1.9V@200mA ELDO3 0.7~1.45V@300mA FLD01 0.7~1.45V@100mA FLD02 .1V CPUS (ON) 0.7~3.3V@100mA GPIO0LD 0.7~3.3V@150mA GPIO1LD0 3.0V@60mA RTCLDO 3.0V VCC-RTC(ON) MODEM/WIFI PINE64 Title Pinebook POWER TREE



CPU U1H VCC-PI VCC-IO VCC3V3-DVIDEO VCC3V3-DSI R10 X32KOUT N19 C4 104 | GND GND0 C2 104 GND U2 C3 M3 J16 GND | C3 | 104 T M16 X32KIN X32KFOUT GND1 GND2 VCC-PI VCC-DSI VCC-IO0 E21 G21 E20 F21 VCC-IO1 X24MOUT < X24MO X24MO HHPD 2 DSI-CKP DSI-CKN DSI-D0P DSI-D0N DSI-D1N DSI-D1N DSI-D1N DSI-D1N DSI-D2P 8-SCK/S-TWI-SCK C17 8-SDA/S-TWI-SDA A19 PL2/S-UART-TX E19 VDD-CPUS H4 L4 N4 L5 R5 U5 G6 R6 C7 D7 PL0/S-RSB-SCK/S-TWI-SCK HSCI GND4 X24MI C1 104 GND I PL1/S-RSB-SDA/S-TWI-SDA PMII-SDA HSDA HSDA GND5 VDD-CPUS N18 P16 DCDC1 VCC-FFUSE C5 4.7uF HCEC GND6 VDD-CPUX VDDBP-EFUSE PL3/S-UART-RX WI WAKE AD GND7 H22 VDD-CPUX0 VDD-CPUX1 PLA/S- ITAG-MS HTYC GND8 GND9 H16 PL4/S-JTAG-MS D19 H23 G22 VCC-RTC VCC-RTC RT-WAKE-AP HTXCN (HTXCN AP-WAKE-BT PL6/S-JTAG-DO PL7/S-JTAG-DI DSI-D2P DSI-D2N GND10 GND11 VDD-CPUX2 VDD-CPUX3 G23 E23 HTX0P HTX0N HTX1P HTX1N HTX1N HTXOE N16 N22 L23 HTX0N HTX1P VCC-PLL VCC-PLL LCD-HPD VDD-CPUX4 VDD-CPUX5 VDD-CPUX5 AC2 PL8/S-TWI-SCK PL9/S-TWI-SDA DSI-D3P DSI-D3N GND12 GND13 HPOUT EN MIC2 EN GND | C6 4.7ul G16 HTX1N RTC-VIO HTX2P GND14 GND15 VDD-CPUX6 VDD-CPUX7 PI 10/S-PWM E22 HTX2N HTX2P × H14 E17 G18 R7 U7 K8 TEST PL11/S-CIR-RX VDD-CPUX8 VDD-CPUX9 VDD-CPUX9 AA3 HALL SEN AP-RESET# RESET PL12 GND16 M8 P8 R8 GND17 A64 VDD-CPUX9 VDD-CPUX10 VDD-CPUX11 VDD-CPUX12 GND18 GND19 GND20 TP1 CPUS-UTX
CPUS-URX
TP114 TEST2 VDD-CPHY13 GNID21 GND22 VDD-CPUX14 GND23 GND24 VDD-CPUX15 A64 VDD-CPUX16 GND25 GND26 VDD-CPUX1 M9 N9 P9 R9 T9 U9 Away from the board outline and senstive signal. Around GND line. GND27 VDDFB-CPUX →>> VDDFB-CPUX 6 GND28 Away from th Around GND 1 board outline and senstive signal GND29 GND30 Routing in Power Layer. VDD-SYS GND31 AP-RESET# VDD-SYS0 J12 GND32 GND33 VDD-SYS1 VCC3V3-USB VDD-SYS2 VDD-SYS3 VDD-SYS3 R13 GND34 GND35 C7 472 . C8 472 104 | GND AVCC AGND A11 I 16 VCC-USE GND36 GND37 VDD-SYS4 K14 VDD-SYS5 L14 C0402 C0402 N10 AGND VDD-SYS5 VDD-SYS6 VDD-SYS7 VDD-SYS8 VDD-SYS9 VDD-SYS9 VDD-SYS9 R14 Close to AP CPVEE.VER CPVD. @P. CP N GND38 GND39 LISB0-DE Max 200mA. CPVDD CPVEE USB0-DM 6,9 CPVEE VRP GND40 GND41 USB1-DP GND GND GND41 GND42 GND43 VDD-SYS10 VDD-SYS11 CPP USB1-DM USB1-DM E10 G13 CPN CPVEE and VEE were Kelvin VEE HSIC-VCC VCC1V2-HSIC GND44 GND45 GND46 VRA-UBOOT <<-VRA2 HSIC-DAT HSIC-STR GND47 GND48 R11 HRIAS TS 1187 GND72 GND71 T13 MBIAS GND49 GND50 SW9 C10 SW9 HPOUTFB GND51 GND52 GND70 GND69 HP-FB 100 B13 A13 HPOUTL EAROUTP HPOUTR HPOUTR EAROUTN GND68 GND67 ≥ 8mil D36 HP-DFT GND54 GND55 GND66 D16 C16 HS-MIC LINEOUTP MIC-DET GND56 GND65 GND64 R15 MIC1P MIC1N MICIN1P LINEOUTN LINFOUTN GND57 GND58 GND59 GND63 A23 GND62 AC23 MICINIAN A14 B14 MIC2F MICIN2P LINEINP MICIN2N LINEINN GND60 GND61 PHONEOUTP E16 × F16 × X C14 PHONEINP PHONEINN GND GND 2017-01-10 A64 2016-12-20 OSC-RTC PB0/UART2-TX/JTAG-MS0
PB1/UART2-RX/JTAG-CK0
PB2/UART2-RTS/JTAG-D00
PB2/UART2-RTS/JTAG-D00 NSMD_7015 324768 R1 10N DECOUPLE CAP PB3/UART2-CTS/I2S0-MCLK/JTAG-DI0 W7
PB4/AIF2-SYNC/PCM0-SYNC
AA6 ₹ 10M-1% CPVDD VDD-CPUS VDD-CPUX VDD-SYS VCC-RTC GND PB5/AIF2-BCLK/PCM0-BCLK W8
PB6/AIF2-DOUT/PCM0-DOUT R0402 GND PB7/AIF2-DUUT/PCM0-DOUT
PB7/AIF2-DINPCM0-DIN
PB8/UART0-TX
PB9/UART0-RX
PB9/UART0-RX UARTO_TX
UARTO_RX

TP9887 UARTO-TX
UARTO_RX

UARTO_RX

UARTO_RX C20 C21 C22 C314 C23 C 104 104 104 104 10uF 1 C0402 C0402 C0402 C0603 C C14 1uF C15 C20 104 C35 104 C18 C28 104 GND C0402 C0402 CC30 18pF C0402 GND GND GND GND GND GND AVCC VRP VCC-PLL DCDC1 KEYADC VRA2 VRA1 ХЗ 1 XIN GND 4 GND XOUT 3 CPVEE 24M-12pF-20PPM SMD_M49 C40 C41 104 4.7uF C0402 C0402 C41 4.7uF C43 2.2uF C0402 C31 102 C39 R2 C32 C33 10uF 10uF 104 C0603 C315 104 C0402 200K-1% 24M-10PPM T C0402 R0402 C0402 GND CRY-TSX3225 PINE64 C0603 C0402 C0402 C0402 C0603 X24MOR3 0R GND C44 = GND Pinebook R4 OR R0402 GND GND CPU Size A3 **Rev** V3.0

