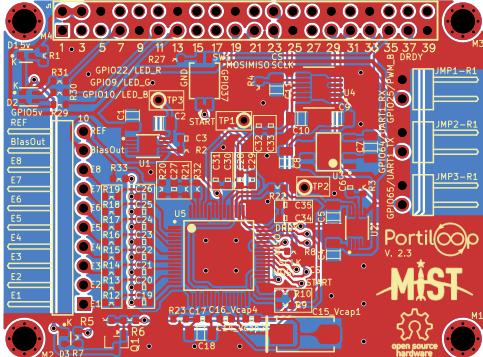


A

A



B

B

Designed by: S. Ehsan Marjani B.

MIST Lab.

Sheet:

File: portiloop.kicad\_pcb

**Title: Portiloop\_PCB**

Size: A5 Date: 2025-12-09

KiCad E.D.A. 9.0.6

Rev: 2.3

Id: 1/1

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