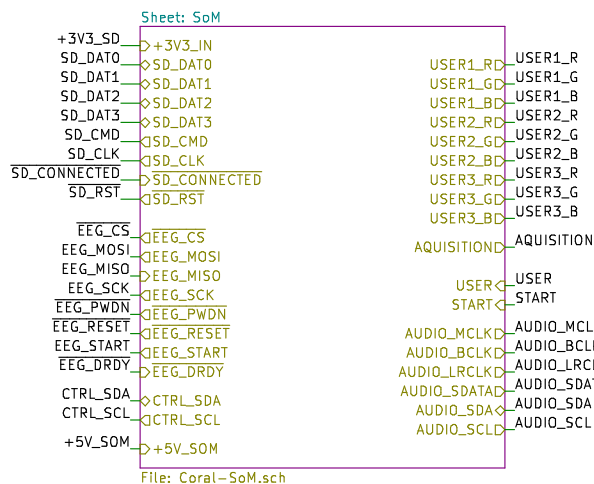
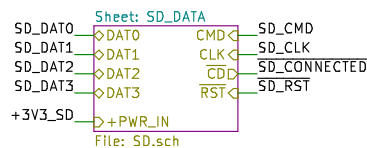
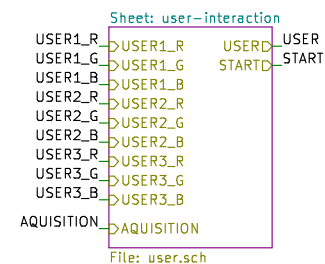


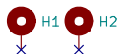
PS I2C addresses:
- 0x5C: HW monitor

Audio I2C address: 0x0A



State:

- Preliminary schematics: done
- Layout: done
- Revised schematic: done
- Schematic validation: done
- Layout validation: done
- BOM optimization: done
- Physical testing: to do



Designed by Xavier L'Heureux

MIST Lab

Sheet: /

File: mist-eeg.sch

Title: Portiloop

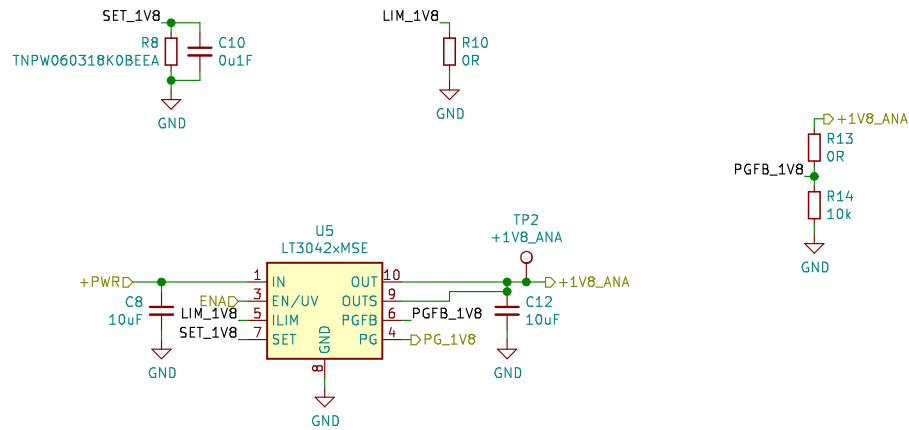
Size: USLetter Date: 2021-08-05

KiCad E.D.A. kicad 5.1.10

Rev: 1E

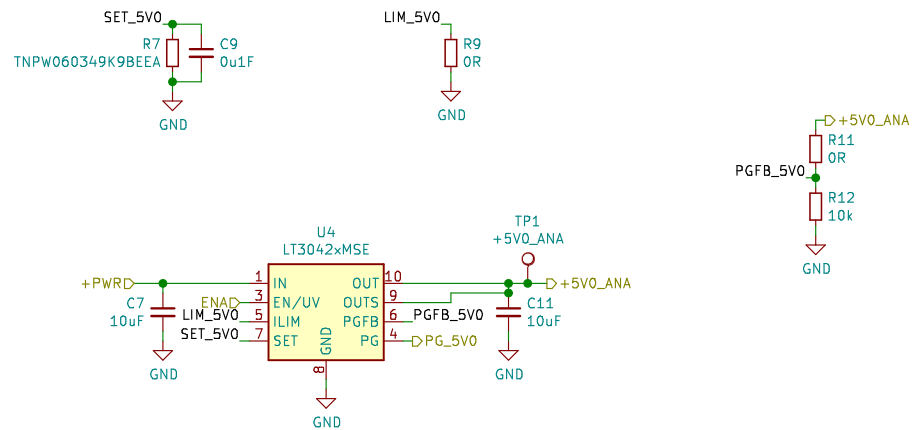
Id: 1/8

1V8 (clean regulator)



$$R_{set} = V_{out} / 100\mu A$$

5V0 (clean regulator)



Designed by Xavier L'Heureux

MIST Lab

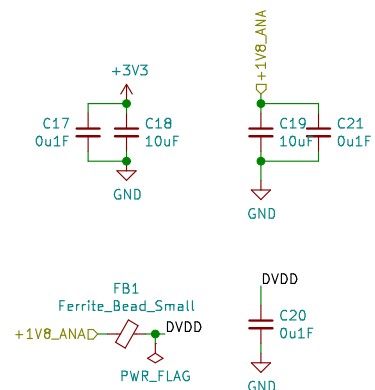
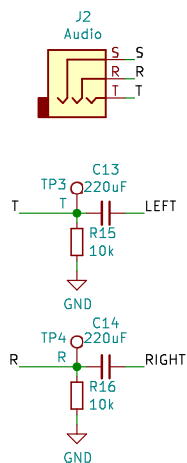
Sheet: /power/audio-power/
File: audio-power.sch

Title: Portiloop

Size: USLetter | Date: 2021-08-05
KiCad E.D.A. | kicad 5.1.10

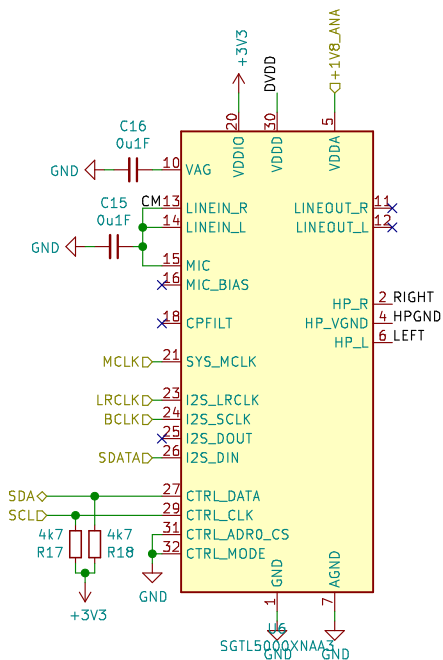
Rev: 1E
Id: 3/8

According to wikipedia:
T: Left
R: Right
S: GND



Jack output

Power & decoupling



CODEC

Designed by Xavier L'Heureux

MIST Lab

Sheet: /audio/

File: audio.sch

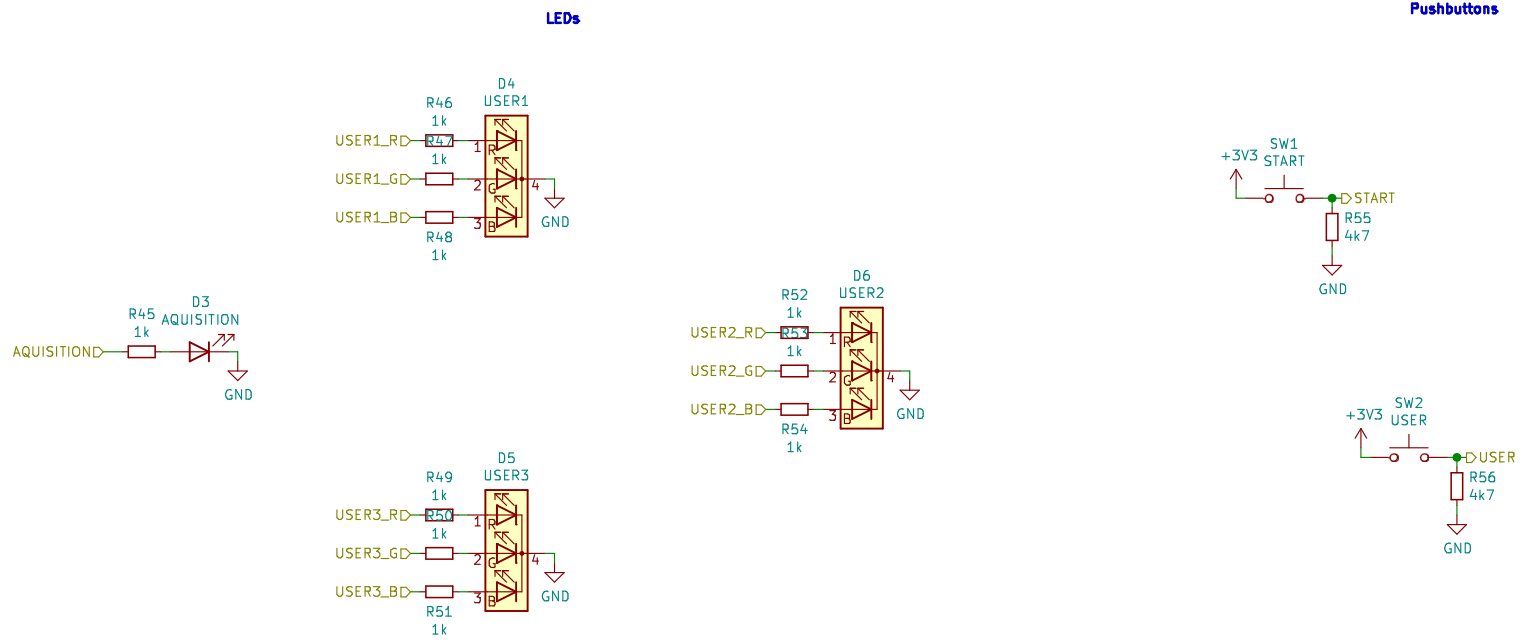
Title: Portiloop

Size: USLetter Date: 2021-08-05

KiCad E.D.A. kicad 5.1.10

Rev: 1E

Id: 4/8



Designed by Xavier L'Heureux

MIST Lab

Sheet: /user-interaction/

File: user.sch

Title: Portilloop

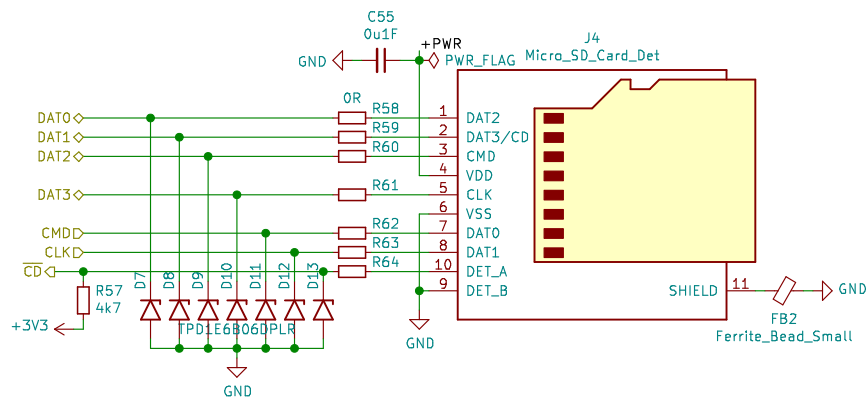
Size: USLetter Date: 2021-08-05

KiCad E.D.A. kicad 5.1.10

Rev: 1E

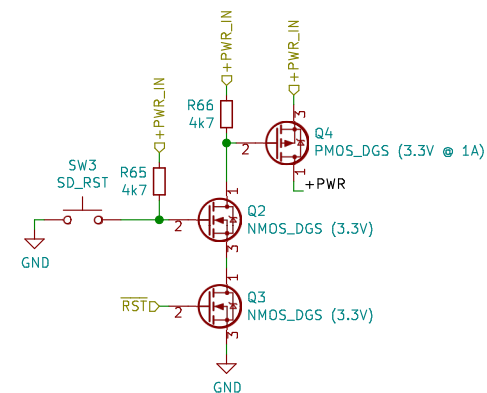
Id: 6/8

SD Card module



Resistor array: test points

Reset switch



Designed by Xavier L'Heureux

MIST Lab

Sheet: /SD_DATA/

File: SD.sch

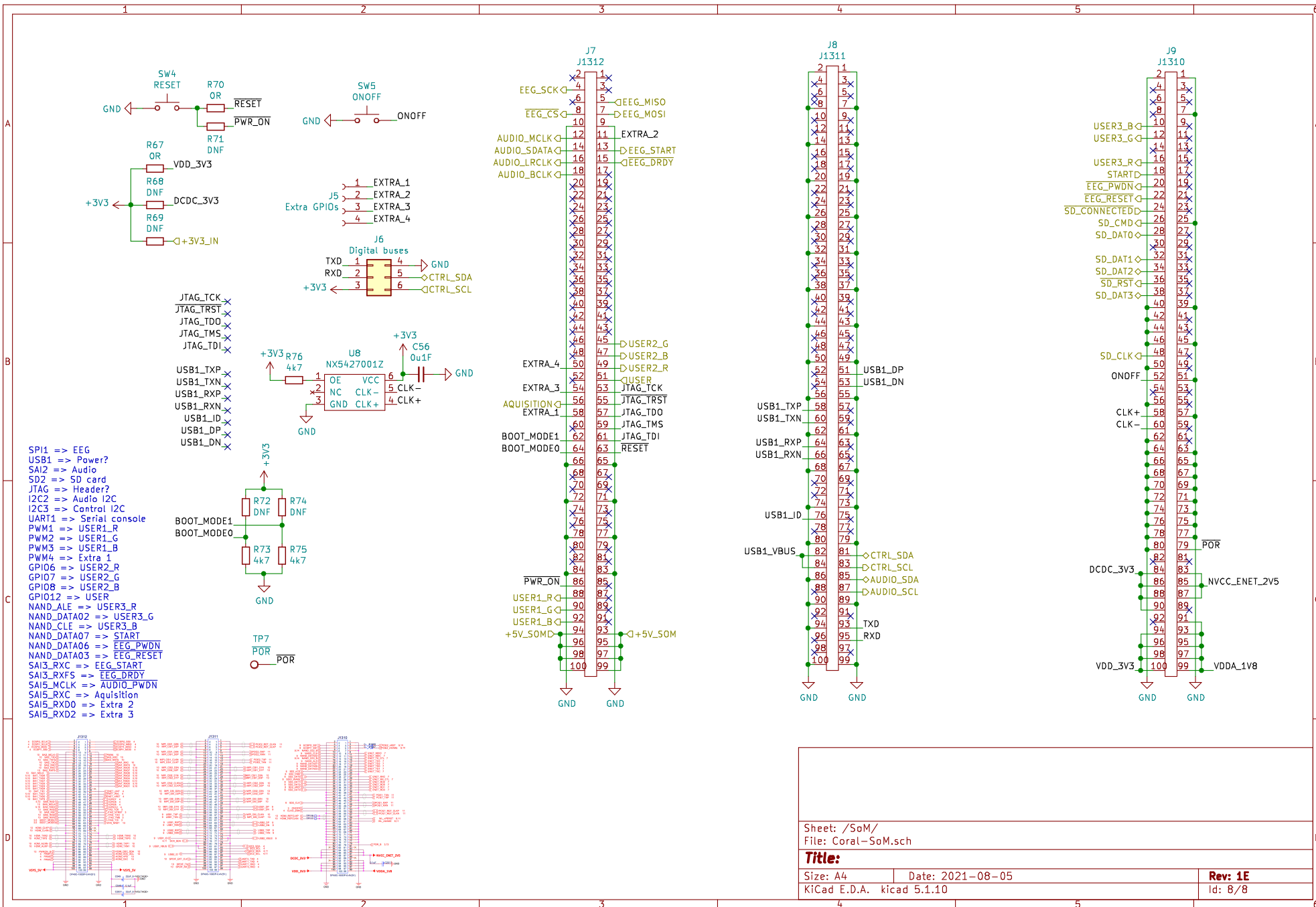
Title: Portiloop

Size: USLetter Date: 2021-08-05

KiCad E.D.A. kicad 5.1.10

Rev: 1E

Id: 7/8



Sheet: /SoM/ File: Coral—SoM.sch		
Title:		
Size: A4	Date: 2021—08—05	Rev: 1E
KiCad E.D.A. kicad 5.1.10		Id: 8/8