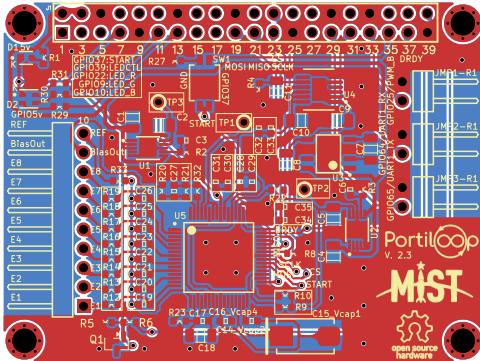


A

A

B

B



Designed by: S. Ehsan Marjani B.

**MIST Lab.**

Sheet:

File: portiloop.kicad\_pcb

**Title: Portiloop\_PCB**

Size: A5 Date: 2025-12-10

KiCad E.D.A. 9.0.6

**Rev: 2.3**

**Id: 1/1**

C

C