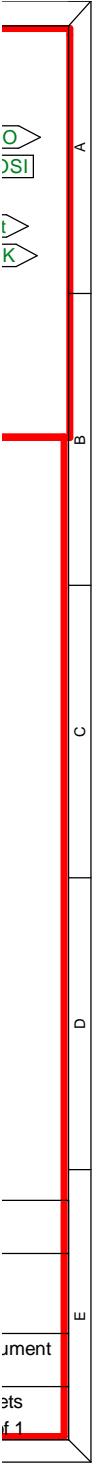
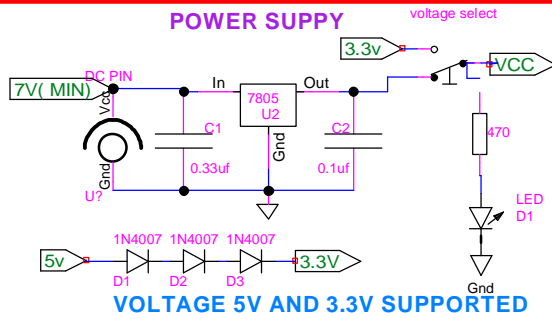


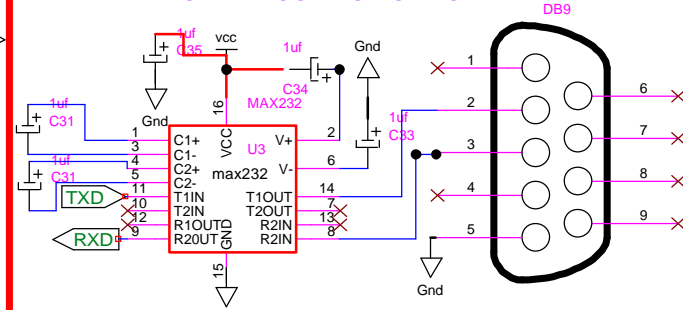
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Author Pradheep(Ref vignesh) TCE		
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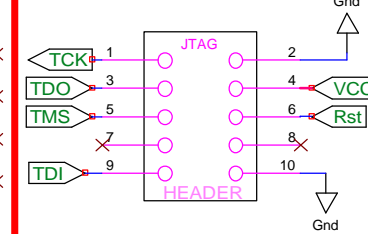
POWER SUPPLY



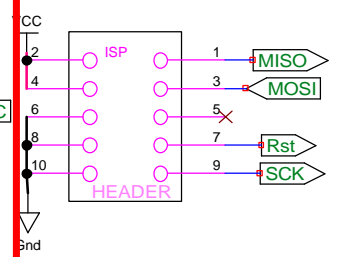
SERIAL COMMUNICATION



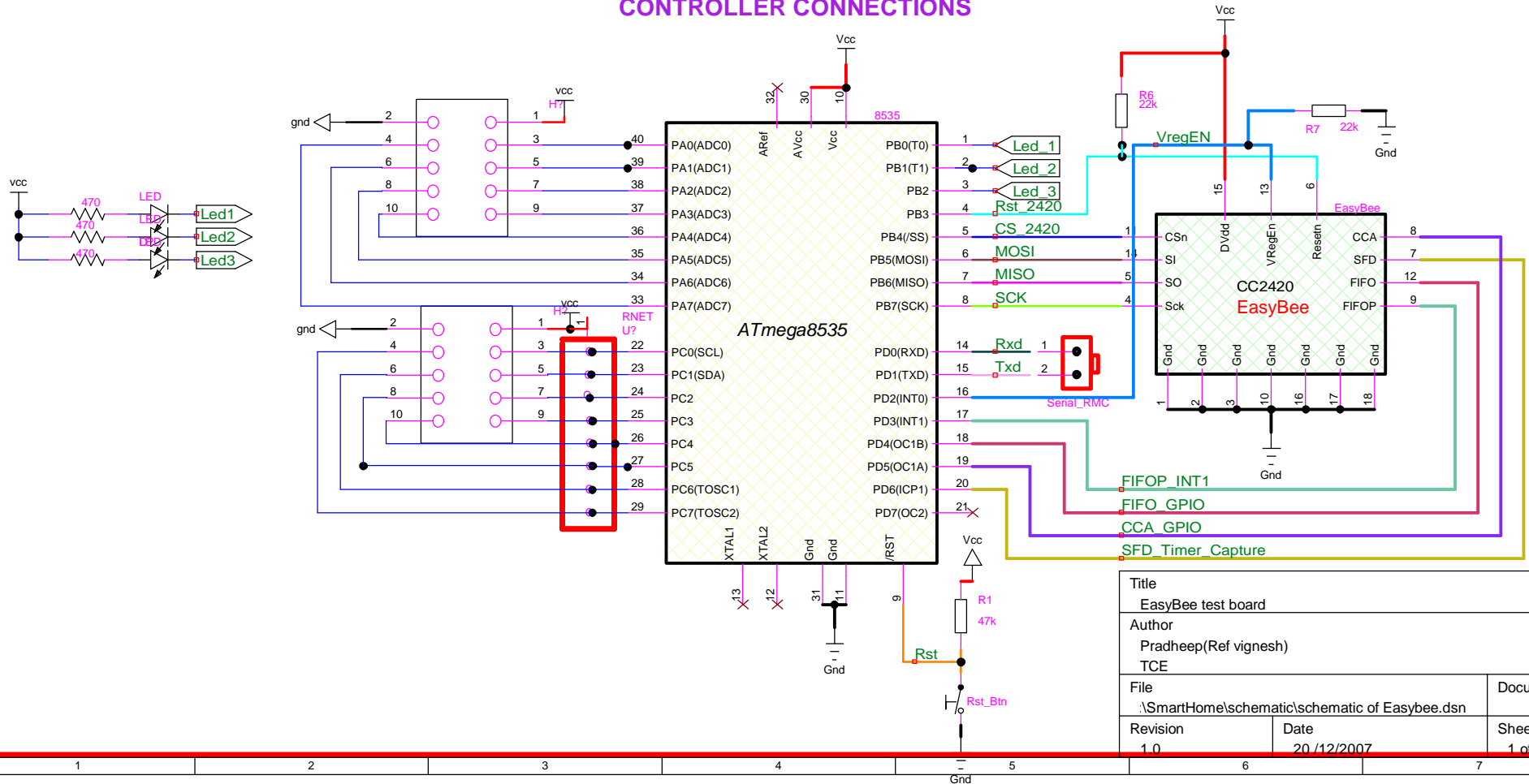
JTAG CONNECTIONS



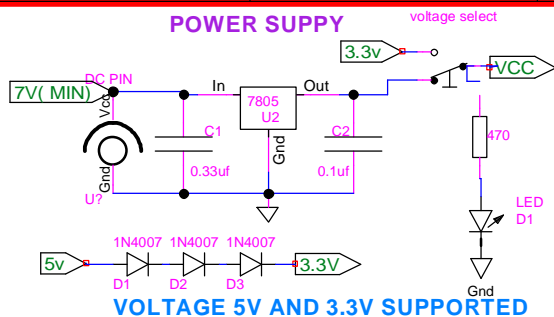
ISP CONNECTIONS



CONTROLLER CONNECTIONS



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JTAG CONNECTIONS

The diagram illustrates the JTAG connections for a microcontroller. A central block labeled 'JTAG' is connected to a 'HEADER' block. The connections are as follows:

- Pin 1: TCK (Test Clock) - Connected to the JTAG block.
- Pin 2: Gnd - Connected to the JTAG block.
- Pin 3: TDO (Test Data Out) - Connected to the JTAG block.
- Pin 4: VCC - Connected to the JTAG block.
- Pin 5: TMS (Test Mode Select) - Connected to the JTAG block.
- Pin 6: Rst (Reset) - Connected to the JTAG block.
- Pin 7: Unconnected (marked with a red X).
- Pin 8: Unconnected (marked with a red X).
- Pin 9: TDI (Test Data In) - Connected to the JTAG block.
- Pin 10: Gnd - Connected to the JTAG block.

ISP CONNECTIONS

Diagram illustrating the ISP (In-System Programming) connections for a microcontroller. The connections are as follows:

- Pin 1: MISO
- Pin 3: MOSI
- Pin 5: ~~Not connected~~
- Pin 7: ~~Not connected~~
- Pin 9: Rst
- Pin 10: SCK

The microcontroller is labeled 'CC' and 'Gnd'.

EasyBee test board

Author: Pradheep(Ref vignesh) TCE

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