

S-attribute

① Synthesized attribute.

② Semantic actions are placed at the end of the right hand side of the production.

③ Evaluated bottom up parsing manner.

L-attribute

① Synthesized / inherited.
(If inherited, only parents and left siblings).

② Anywhere in right hand side.

③ Depth first left to right manner.

$$A \rightarrow BCD$$

$$B \rightarrow C \cdot val \rightarrow B \cdot val$$

Pg) For same operant,

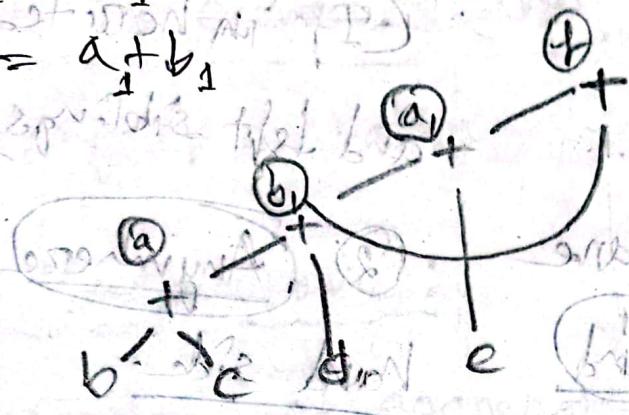
(right, left same operator)

$$\textcircled{a} = b + c$$

$$b_1 = \textcircled{a} + d$$

$$a_1 = b_1 + e$$

$$f = a_1 + b_1$$



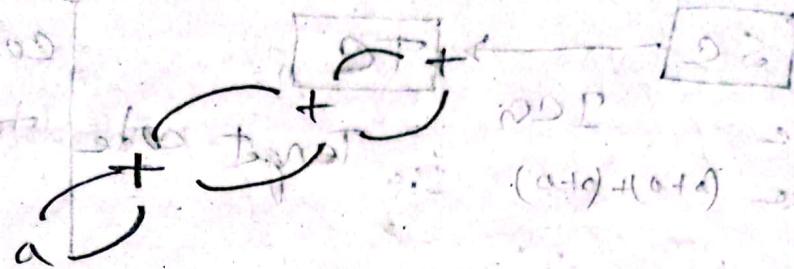
Benefits of Intermediate Code generation:

- ① Portability will be enhanced.
- ② Refactoring is facilitated.
- ③ Machine Independent.

Int. → Low → M

DAG

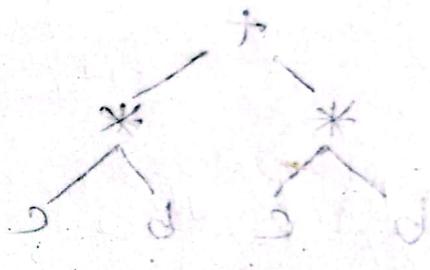
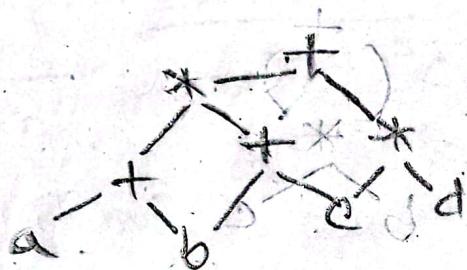
$$((a+a)+a+(a+a)) + ((a+a)+(a+a))$$



(P₂)

$$(a+b) * (b+c) + (c*d)$$

$$(a+b) * (b+c) + (c*d)$$



(P_n)

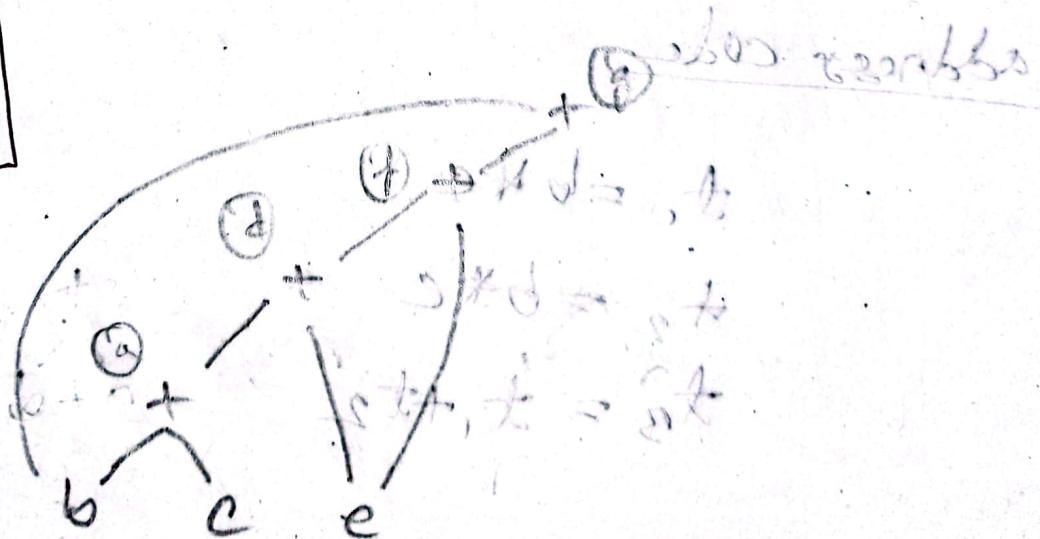
$$a = b+c$$

$$d = a+c$$

$$f = c+d$$

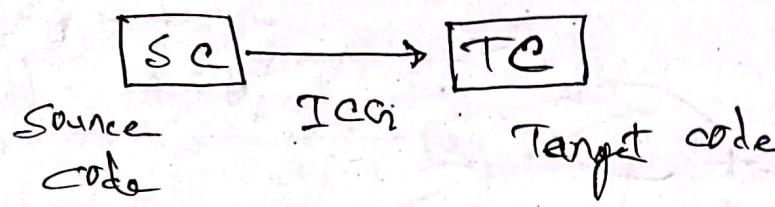
$$g = b+f$$

block of code



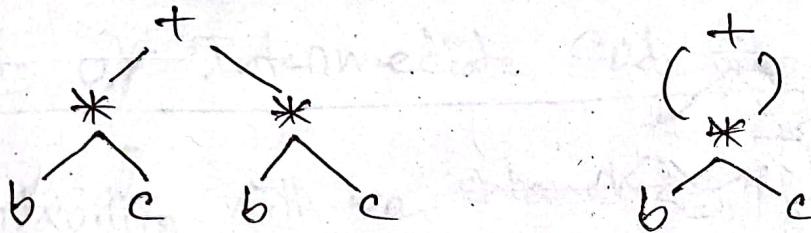
Intermediate Code Generation

Machine Independent



Compiler use intermediate code to generate the target code.

Example: $(b * c) + (b * c)$



Postfix $\rightarrow b c * b c * +$

3 address code

$$t_1 = b * c$$

$$t_2 = b * c$$

$$t_3 = t_1 + t_2$$

$t_1 = b$	$t_2 = c$
$t_3 = t_1 + t_2$	

Intermediate Code generation & representation

① 3 address code.

② Syntax tree.

③ Postfix notations.

④ DAG

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Semantic two rule

• ~~bottom up~~ bottom up (S)

↓
Synthesized

Inherited

$A \oplus B \rightarrow C \oplus D$

$A \rightarrow B \oplus D$

$B \oplus D \rightarrow B \cdot val$

$A \cdot val \rightarrow B \cdot val$

$B \cdot val \rightarrow A \cdot val$

not low off

not high off

not off

No SOT: Local variable (S)

remove local var (S)

Statement notations

two global prims

global obj

Annotated Parse tree

SDD vs SDT difference

SDD

Syntax directed Definition

SDT

Syntax directed translation

④ CFG + Semantic rule.

④ CFG + Semantic rules +
semantic action.

② Mostly used for
'Specification'.

② Implementation used.

③ Ease to use

③ Efficient to use.

⑨ $\text{Eval} = \text{Eval} + \text{Tral}$

⑨ $\{\text{Print}(t')\} = \text{tral}$

⑥ Types of semantic analysis errors:

① Type mismatch

④ Reserve identifier misuse.

② Accessing variable out
of scope.

⑤ Undeclared variable.

③ Declare variable multiple
times within scope.

⑥ Actual and formal
parameters mismatch.

Practice

CLR(1) and LALR(1)

$$S \rightarrow Aa \mid bAc \mid Bc \mid bBa$$

fint
d, b,
d
d

Shallow

\$
a, c,
c, a

$$\begin{array}{l} \text{b} \\ \text{a} \\ \text{B} \\ \text{a} \end{array}$$

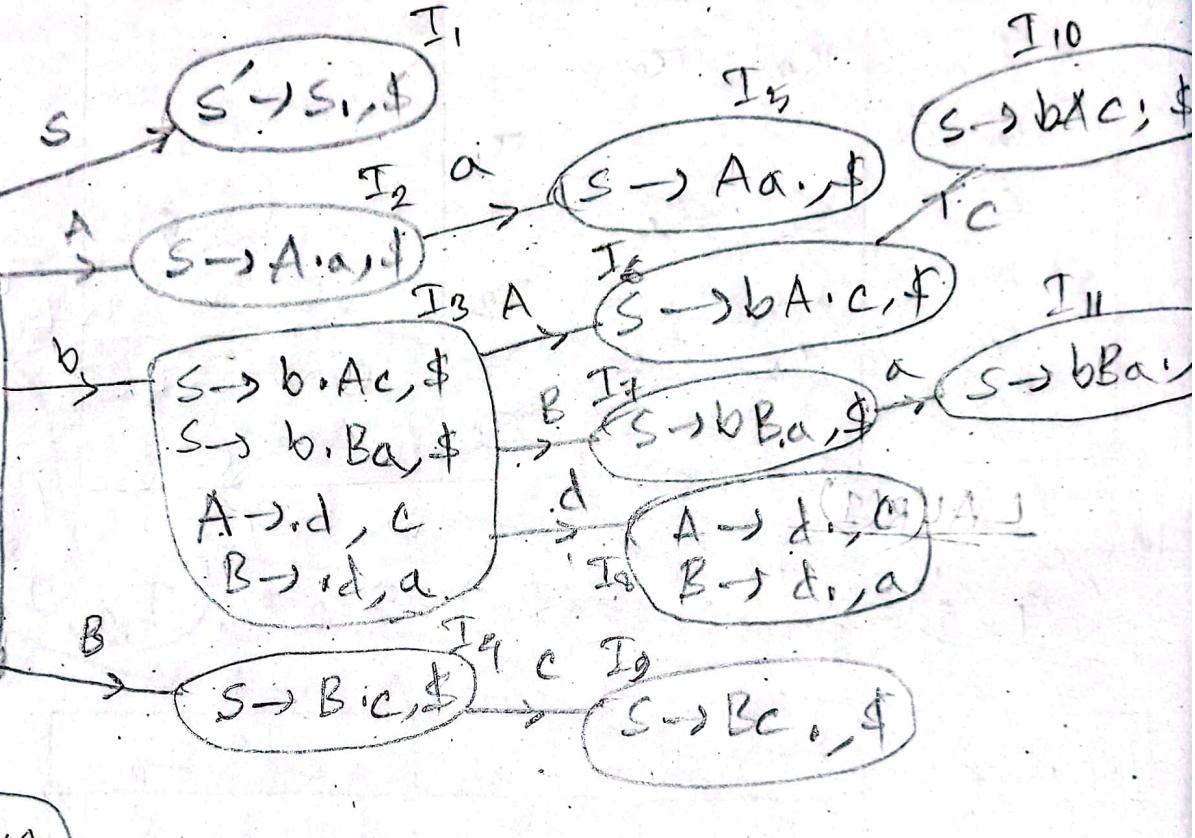
I₀

$$\begin{array}{l} S \rightarrow S, \$ \\ S \rightarrow Aa, \$ \\ S \rightarrow bAc, \$ \\ S \rightarrow Bc, \$ \\ S \rightarrow bBa, \$ \end{array}$$

$$\begin{array}{l} A \rightarrow d, a \\ B \rightarrow d, c \end{array}$$

d

$$\begin{array}{l} A \rightarrow d, a \\ B \rightarrow d, c \end{array}$$



steps

DFA

lookup

reduce production lookup or action

STC

CLR(1)

(DFA1 to DFA2) / states

state	a	Action	b	\$	0 to 1 state	1 to A state
0	s_3	s_4			1	2
1						
2	s_6	s_7				5
3	s_3	s_4				8
4	r_2	r_3				
5			r_1			
6	s_6	s_7				9
7			r_3			
8	r_2	r_2				
9			r_2			

reduce state
Lookup ahead go
action, FGT2 Lookup
2nd action go
states - II terminal
state,

LALR(1)

Merge

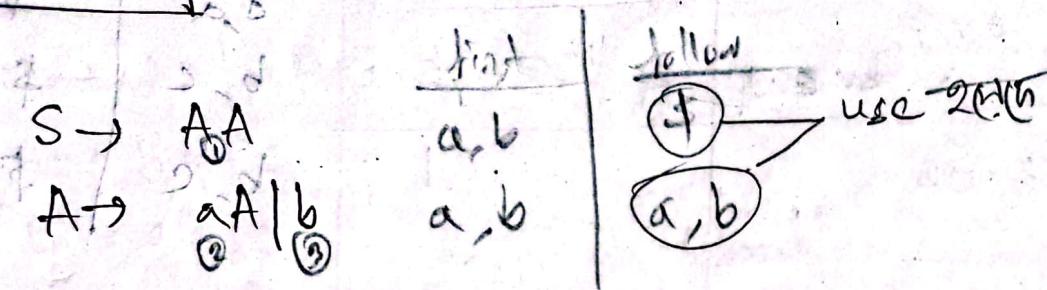
$$(q, \cdot) = I_{q\cdot}, (I_q, I_q) = I_{qq}, (I_q, I_s) = I_{qs}$$

State	Actions			Go to
	a	b	\$	
0	s_6	s_{92}		1
1				
2	s_{36}	s_{92}		5
36	s_{36}	s_{97}		89
97	(with r_3)	r_{300}	r_{300}	not a final state
5			r_1	number ←
89	r_{22}	r_{24}	r_2	1016
-				
-				
-				

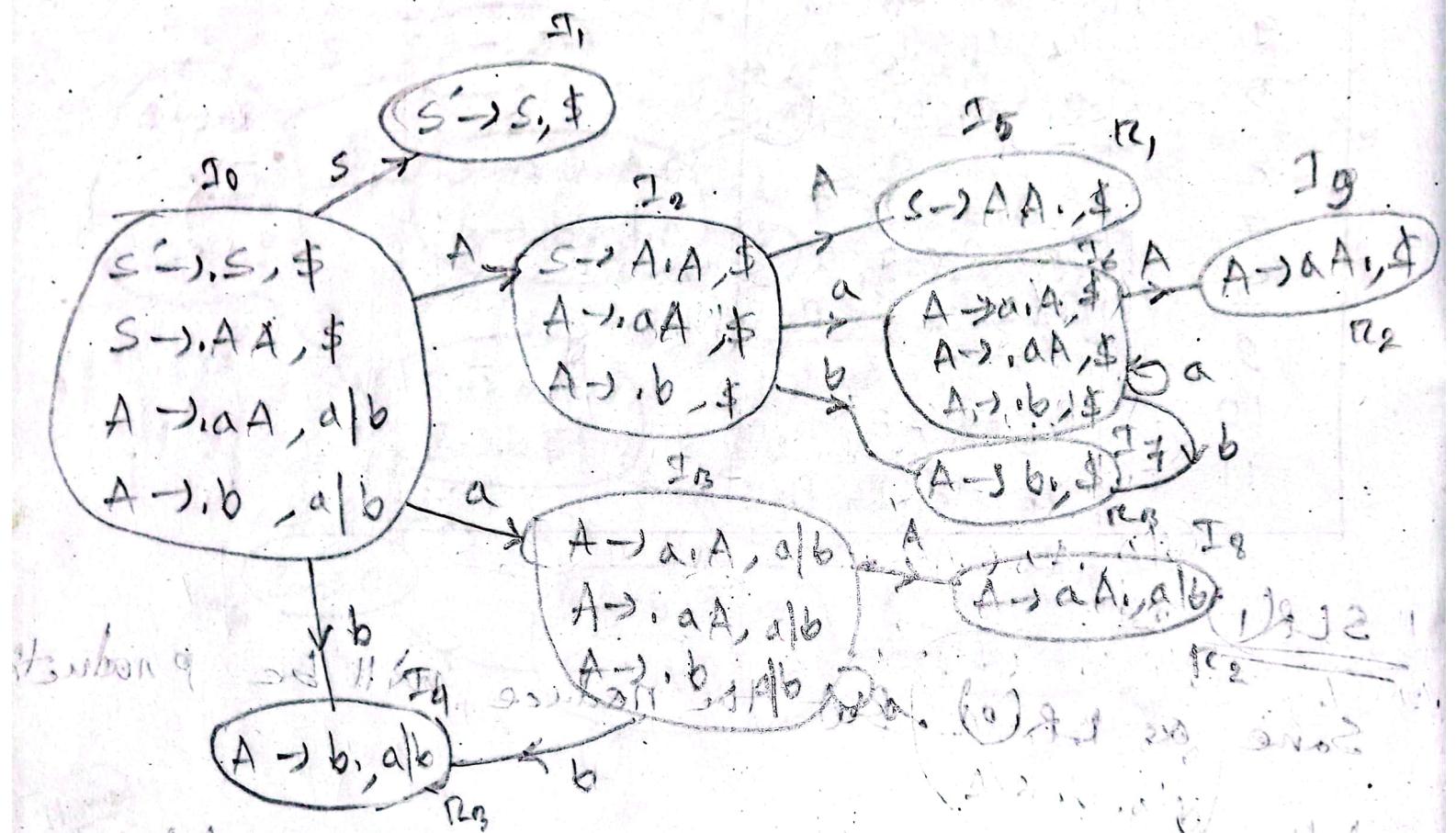
FAT AFD
FAT initial
number ←

Problem

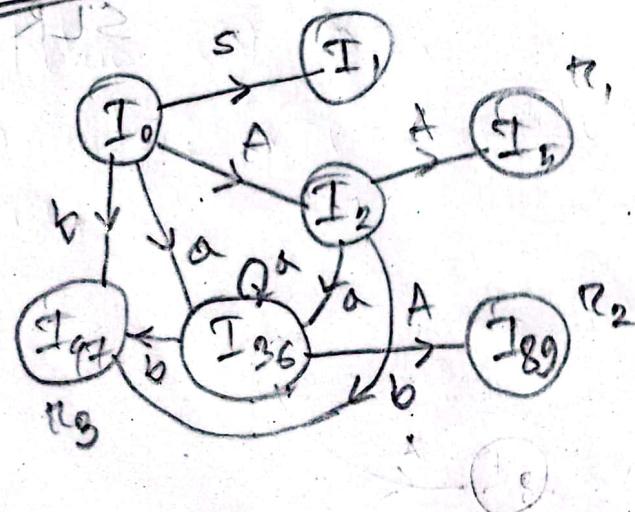
CLR(1) and LALR(1)



DFA



~~FOR LATER~~



LR(0)

C1921A3 Lec 10 (29/04/2021)

first

follow

State	Action	go to	first			follow
			A	B	S	
0	d a b. cc\$	A B S				d, a
1	$s_2 s_3$				1	b, c
2					5	AA
3					8	[AB]
4	$r_2 r_3 r_6 r_4 r_5$					
5	$r_1 r_2 r_3 r_4 r_5$					
6	$s_6 s_7$				10	
7	$r_4 r_3 r_2 r_1 r_0$					
8	$r_2 r_1 r_0$					
9	s_9				11	
10	$r_3 r_2 r_1 r_0$					
11	$r_5 r_4 r_3 r_2 r_1 r_0$					

SLR(1)

Same as LR(0). But the reduce will be productive.

Follow.

LR(0) ✓

SLR(1) ✓

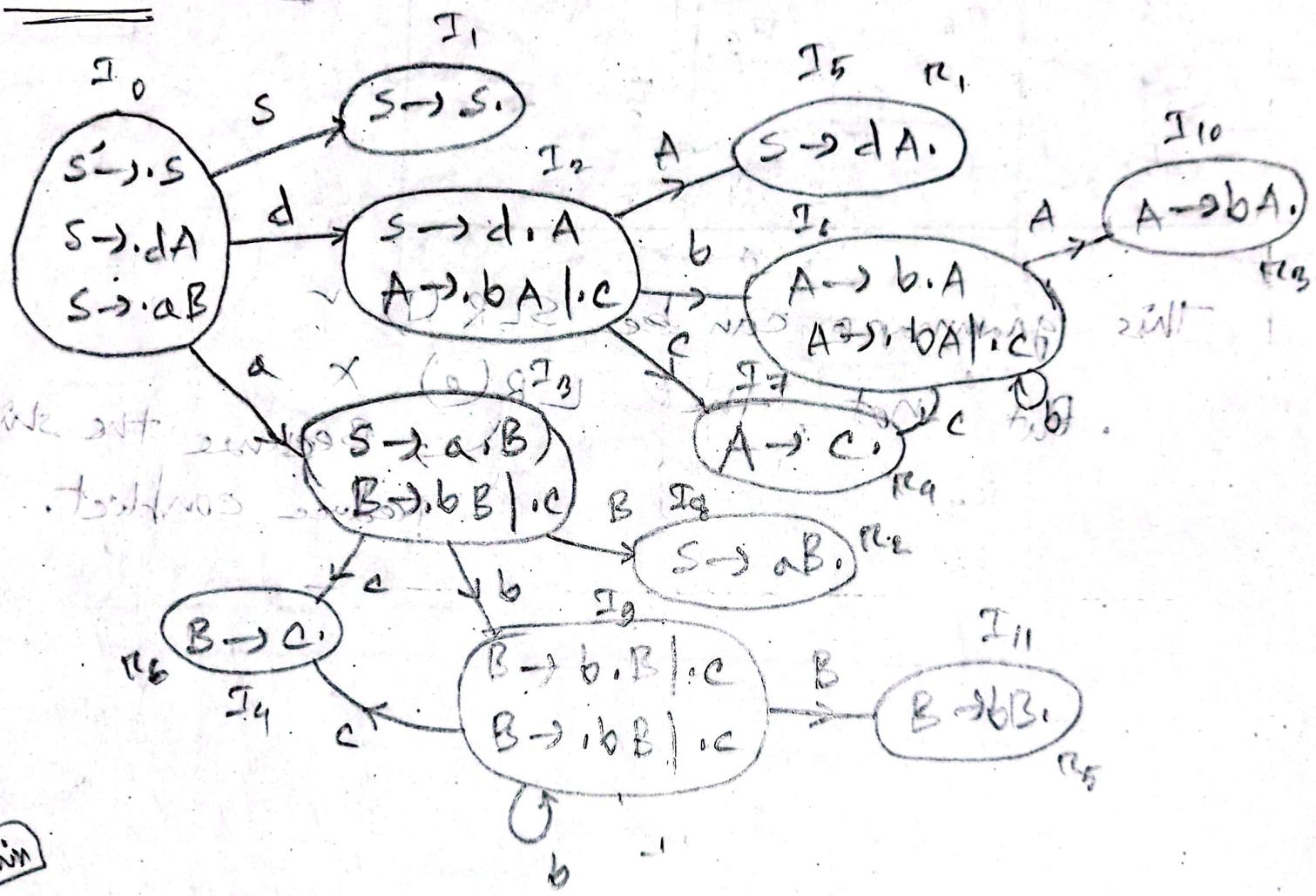
Problem 2 LR(0) and SLR(1)

$$S \rightarrow d.A \mid a.B$$

$$A \rightarrow b.A \mid c$$

$$B \rightarrow b.B \mid c$$

DFA



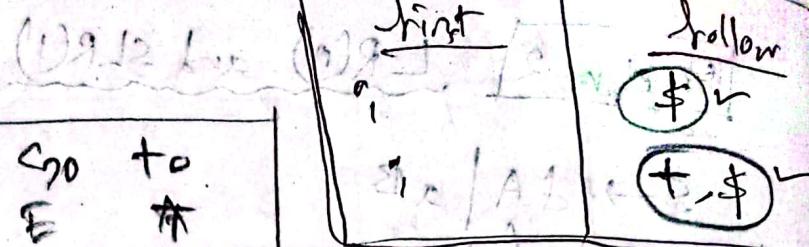
SLR(1)

state	Action + id \$	$S_0 \rightarrow E$	to *
0	S_B	1	2
1		(completed)	
2	S_A	R_2	
3	R_B	R_3	
4	S_B	5	2
5	R_1		

Total 23 min

This grammar can be SLR(1) ✓

But not the LR(0) X
 ↳ Because the shift
 reduce conflict.



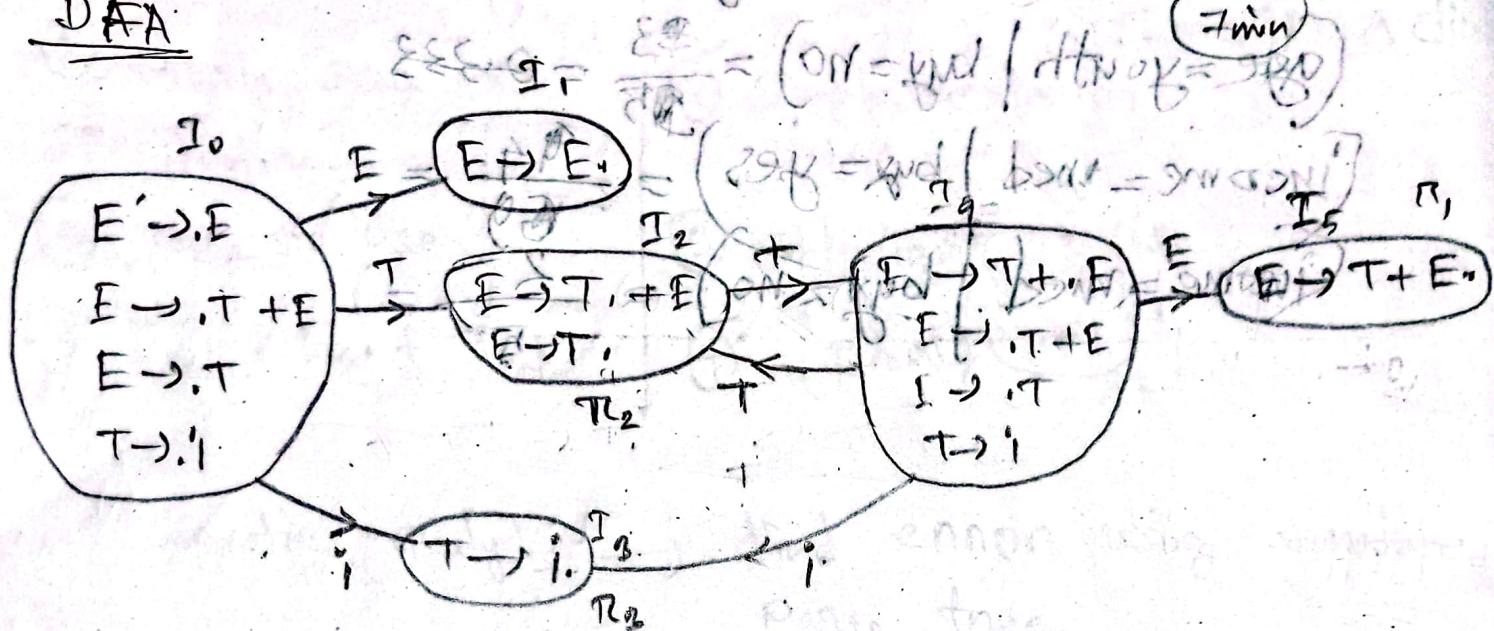
$R_1, R_2 \rightarrow \$$
 $R_3 \rightarrow +, \$$

Problem LR(0) and SLR(1)

$$E \rightarrow T + E \mid I_2$$

$$T \rightarrow S_{11} \cdot D = \frac{S_{11}}{D} = (\Sigma k = \text{odd} \mid d_{Tn} \cdot k = 2k)$$

DFA



LR(0) Parsing table:

State	Action	Go to.
0	+ \$	E T
1	S_3 complete	1 2
2	S_1	1
3	R_2	1
4	R_3	1
5	S_2	5 2
6	R_1	1

Annotated Parse Tree

(cont'd) \rightarrow $\text{m}^{\prime }(\sigma)\otimes j$

SOT

- ① CFG + Semantic rule
 - ② Mostly used from specification.
 - ③ easy to use
 - ④ $Eval = Eval + T_{Eval}$

- ① CFG + Semantic rules +
Semantic Action
 - ② Implementation
 - ③ efficient to use.
 - ④ portability

Semantic analysis

find error using annotations
Parse tree

LALR(1)

CLR(1)

LR(1) Item = (LR(0) item + lookahead)

$S \rightarrow .aA, aAb$

$LR(0), SLR(1), LALR(1)$ go to total state same DFA,

but CLR(1) goes to different states DFA

$CLR(1) \geq LR(0) \geq SLR(1) = LALR(1)$ → Same state

(Rollback)

LR(0) gives non-dfa

→ DFA

→ Dot of LR Production

different LR Production

→ Reduce row করে যাবে,

CLR(1)

→ DFA

→ table go reduce

(n_1, n_2, \dots) প্রয়োজনীয় lookup
go action i ,

SLR(1)

→ DFA

→ table & reduce(n_1, n_2)

production. এর মাধ্যমে

Follow ∞ ক্ষেত্র,

$E \in \text{Follow}(E) \cap (n_1, n_2)$

$E \in \text{Follow}(E) \cap (n_1, n_2)$

LALR(1)

→ DFA

→ Mangle state

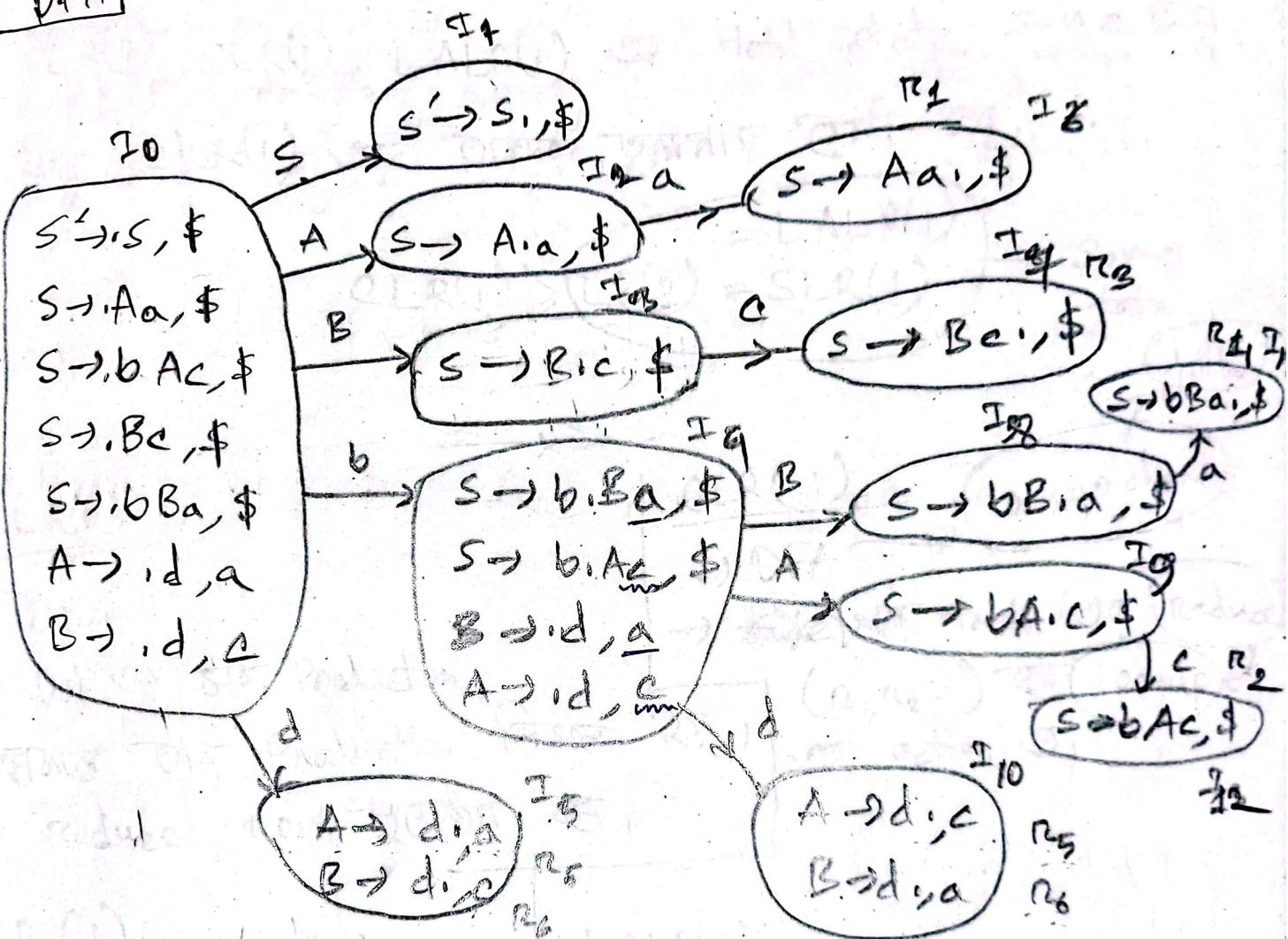
→ table & Mangle state go
mangle যাবে,

$s \rightarrow Aa \mid bAc \mid Bc \mid bBa$

CLR, LALR

 $A \rightarrow d$
 $B \rightarrow d$

DFA



State	Action	$g_0 \rightarrow 0$
0	a s_9 b s_5 c s_5 d s_5 λs_5	$g_0 \rightarrow 0$
2	s_6	
3	s_7	
4	r_5	$g_0 \rightarrow 0$
6	s_{11}	r_6
9	r_6	s_{10}
10	r_6	r_1
11	r_6	r_2
12	r_6	r_0

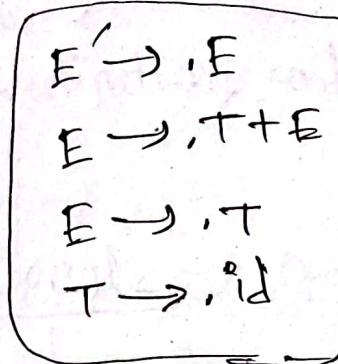
LR(0) Parsing table

Bottom up

LR(0) CI (Canonical Items)

$$E \rightarrow T + E \quad | \quad T$$

$$T \rightarrow id$$



$$E \rightarrow E. \quad I_1$$

$$\begin{array}{l} E' \rightarrow T. + E \\ E \rightarrow T. \end{array} \quad I_2$$

$$T \rightarrow id. \quad I_3$$

I₅ reduced

$$E \rightarrow T + E.$$

I₄

$$\begin{array}{l} E \rightarrow T + E \\ E \rightarrow .T + E \\ E \rightarrow .T \\ T \rightarrow .id \end{array}$$

id

I₃ Reduces

if(.) of all Non-terminal τ_i of τ_i Production

is same

state	'id'	+	\$	E	T
0	S_3			1	2
1	τ_2	S_1/τ_2	τ_2		
2	τ_2	(S_1/τ_2)	τ_2		
3	τ_3	τ_3	τ_3		
4	S_3			5	2
5	τ_1	τ_1	τ_1		

LR(0) X

(Because
Shift-reduce
conflict)

Prob 1

$$E \rightarrow TE'$$

$$E' \rightarrow ?E \mid \epsilon$$

$$T \rightarrow FT'$$

$$T' \rightarrow !T \mid \epsilon$$

$$F \rightarrow WF'$$

$$F' \rightarrow @F \mid \epsilon$$

$$W \rightarrow uv \mid v \mid [E]$$

$$v \rightarrow ab$$

$$u \rightarrow < >$$

Complement

First

[, <, >, a, b

?, \epsilon

a, b, <, >, [,

!, \epsilon

a, b, <, >, [,

@, \epsilon

a, b, <, >, [,

a, b

<, >

Follows

I, \$

], \$

? ,], \$

? ,], \$

! , ?,], \$

! , ?,], \$

@ , ! , ?,], \$

@ , ! , ?,], \$

a, b

Case 1

মনে রাখো non-terminal এরলে পিলের non-terminal রে

First রাখো,

Case 2

মনে রাখো ফিল্ড না রাখে, তাহলে Parent র ফলোর

রাখবে।

ϵ -closed(A) = {A, D, B}

ϵ - π (B) = {B}

ϵ - π (C) = {C, D, B}

ϵ - π (D) = {B, D}

DFA

	a	b
A	②	③ BC
B	φ	D
C	φ	φ
D	φ	φ

NFA

① ADB | BCD | BDK

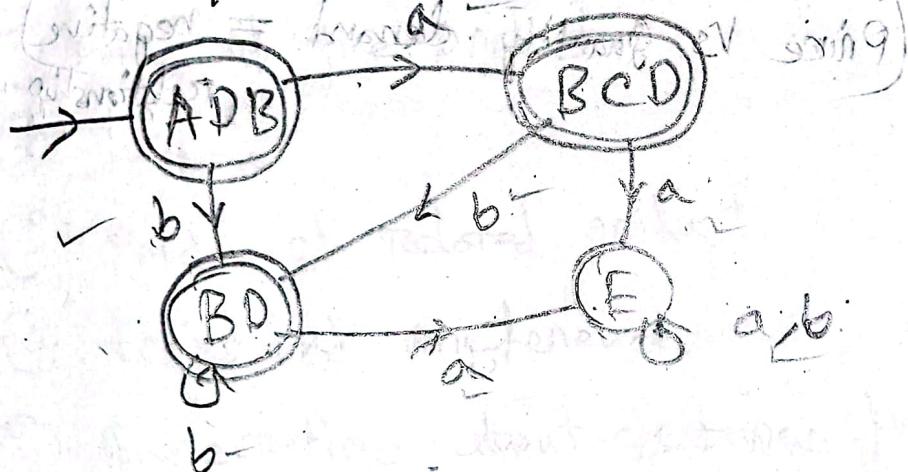
② BCD | E | BD

③ BD | E | BD

④ E | E | E

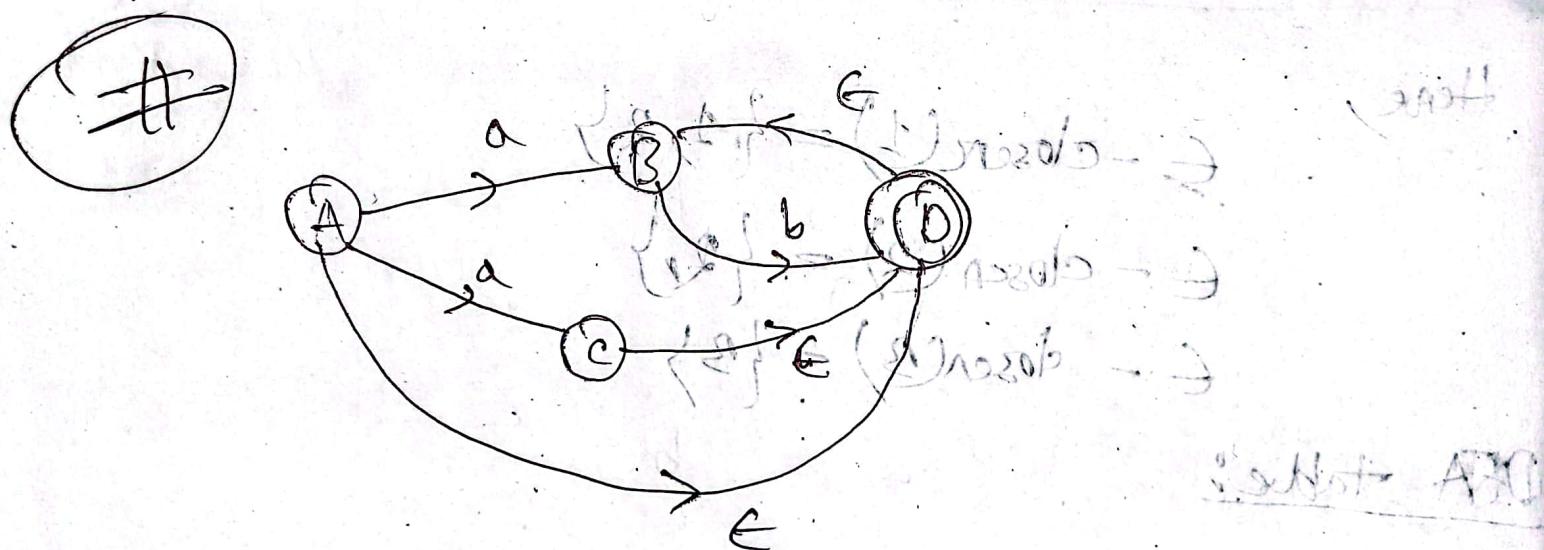
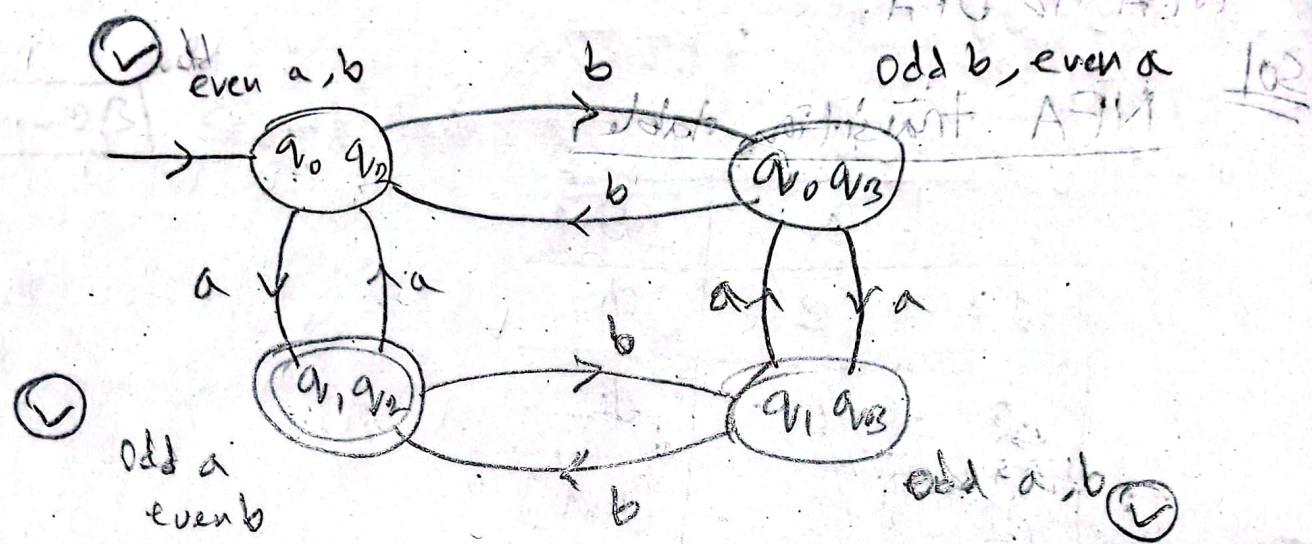
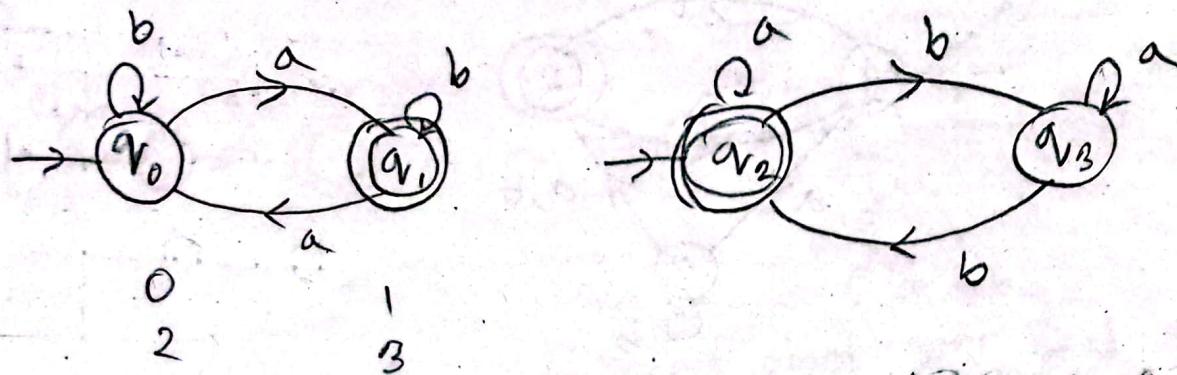
ϵ -go value

↓



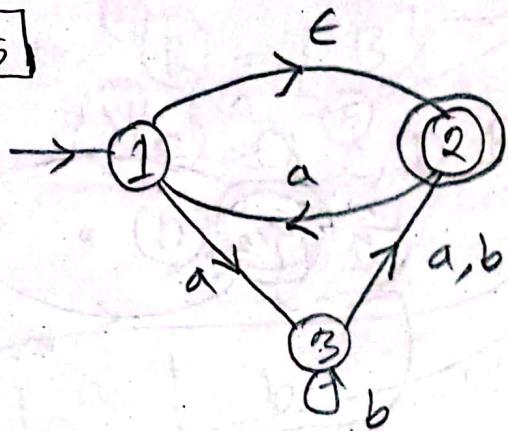
ϵ -go end, DFA go state of [ADB] after
the NFA go state vector 20, then the
current value of ϵ value of
DFA is 20.

Odd 'a' and even 'b'.
 On $\Rightarrow A7G + A7H$



NFA to DFA (ϵ)

Problem-25



NFA to DFA.

Sol

NFA transition table:

	a	b
1	3	∅
2	4	∅
3	2	{2, 3}

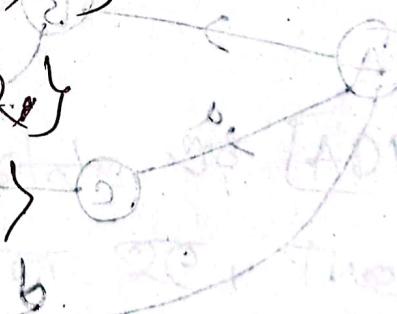
Hence,

$$\epsilon\text{-closure}(1) = \{1, 2\}$$

$$\epsilon\text{-closure}(2) = \{2\}$$

$$\epsilon\text{-closure}(3) = \{3\}$$

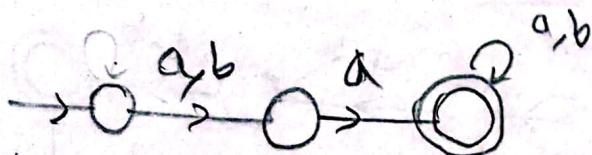
DFA table:



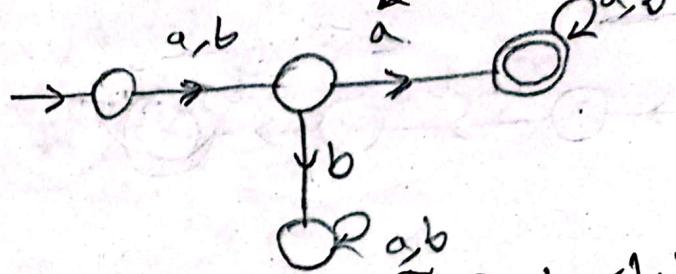
Problem-22

Second a start,

NFA



DFA



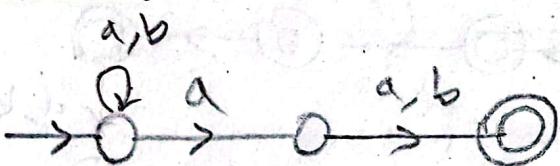
Second start

A3H

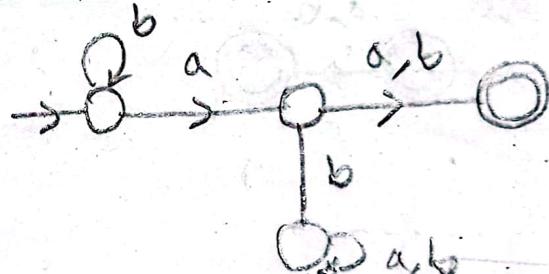
Problem-23

Second last a,

NFA



DFA

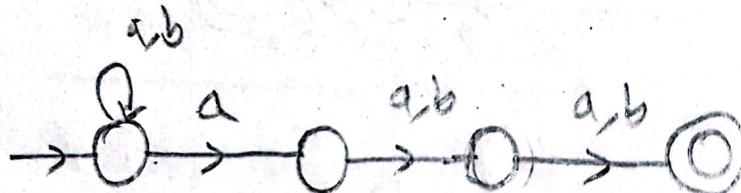


C

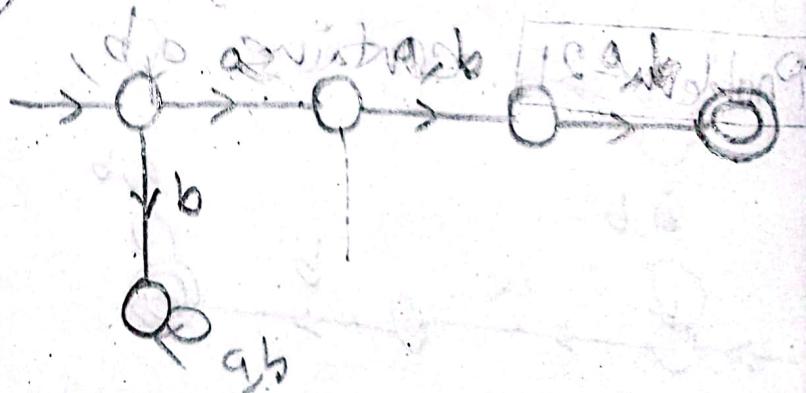
Problem-24

Third last a, 2C,

NFA



DFA

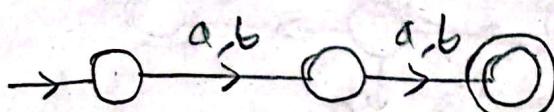
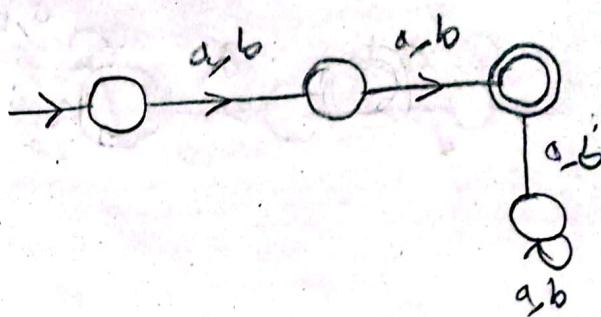


A3H

NFA Practise

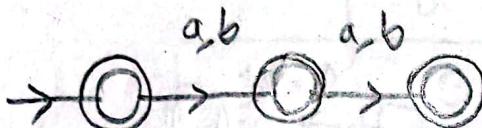
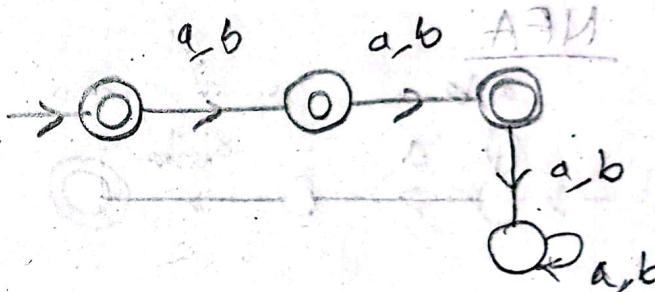
Problem 18

exactly 2,

NFAAPGDFAATM

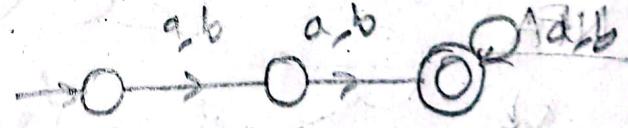
Problem 19

Maximum 2,

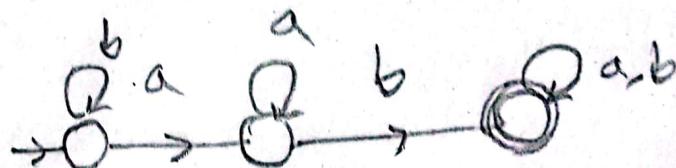
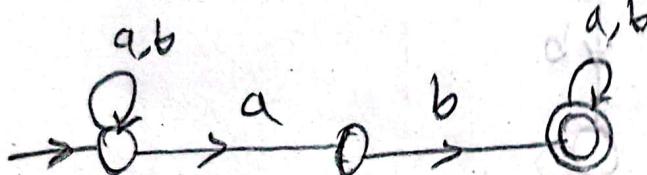
NFAAPGDFAATM

Problem - 20

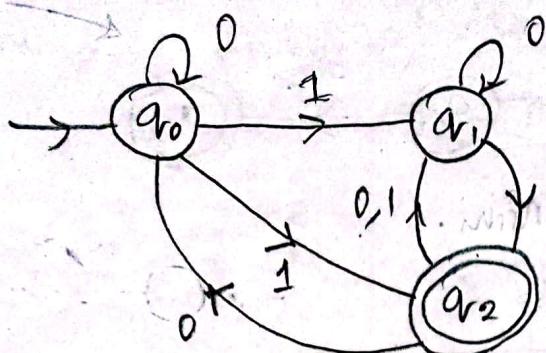
minimum 2,

NFADFAATM

Problem - 21

contains ab ,

Problem 17



NFA to DFA conversion.

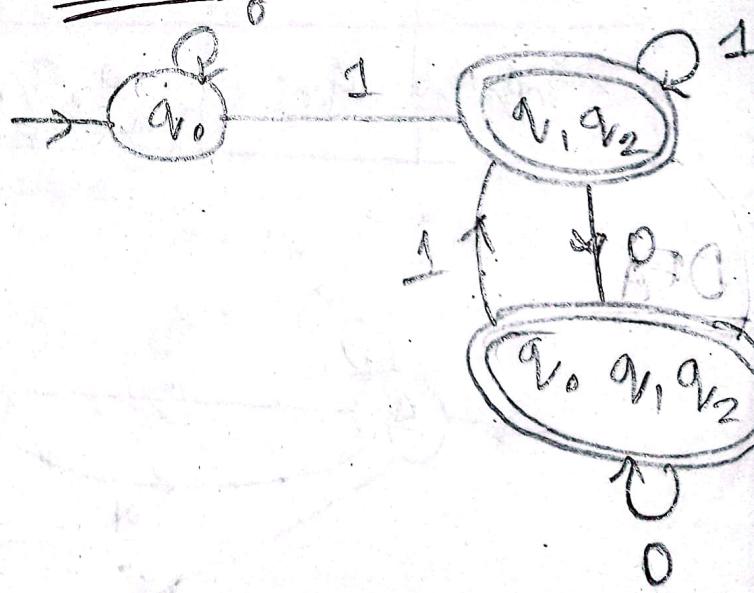
NFA transition table:

	0	1
q_0	q_0, q_2	q_1, q_2
q_1	q_1, q_2	q_2
q_2	q_0, q_1	q_1

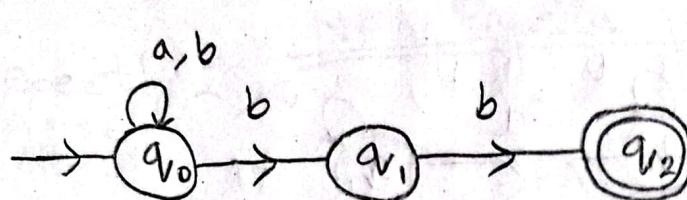
DFA Transition Table:

	0	1
q_0	q_0	q_1, q_2
q_1, q_2	q_0, q_1, q_2	q_1, q_2
q_0, q_1, q_2	q_0, q_1, q_2	q_1, q_2

DFA:



Problem-16



NFA

NFA to DFA conversion.

NFA transition table:

	a	b
q_0	$\{q_0\}$	$\{q_0, q_1\}$
q_1	\emptyset	q_2
q_2	\emptyset	\emptyset

→ NO transition to A3C or A7H

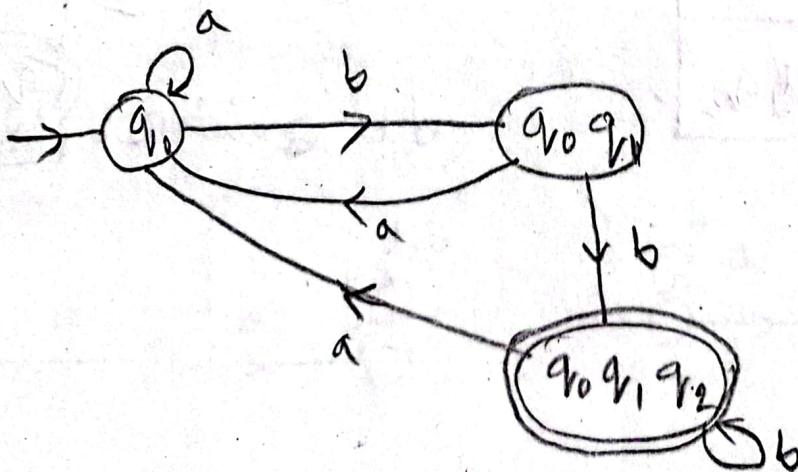
↓ don't write out A7H

DFA table:

	a	b
q_0	q_0	$\{q_0, q_1\}$
$\{q_0, q_1\}$	q_0	$\{q_0, q_1, q_2\}$
$\{q_0, q_1, q_2\}$	q_0	$\{q_0, q_1, q_2\}$

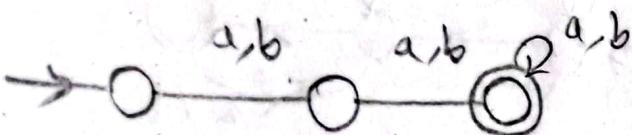
q_2 final state

DFA

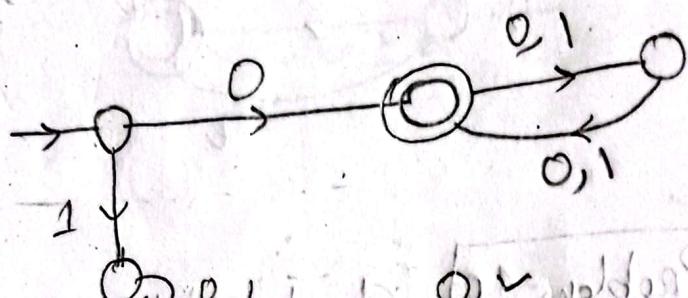


Problem-12 minimum length

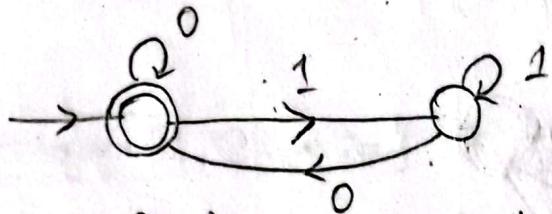
2.



Problem-13 Start with 0 & has odd length.



Problem-14 Divisible by 2.



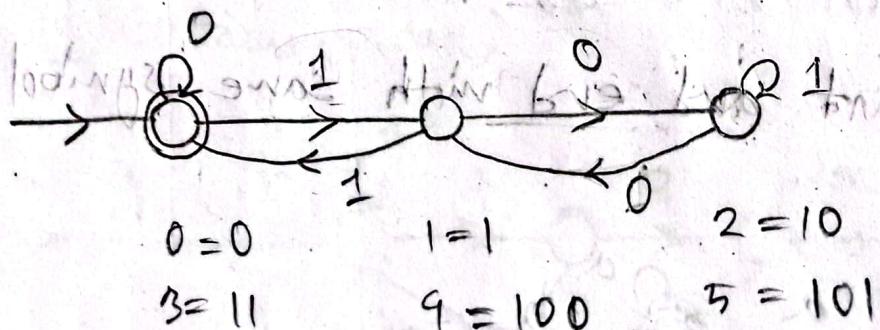
$$\begin{array}{l} 0=0 \\ 2=10 \\ 4=100 \end{array}$$

$$\begin{array}{l} 1=1 \\ 3=11 \\ 5=101 \end{array}$$

$$\begin{array}{l} 0,1,2 \\ \downarrow \quad \downarrow \quad \downarrow \\ 0 \quad 1 \quad 10 \end{array}$$

q20

Problem-15 Divisible by 3.



$$0=0$$

$$3=11$$

$$1=1$$

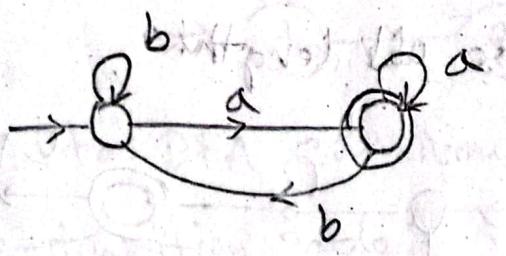
$$9=100$$

$$2=10$$

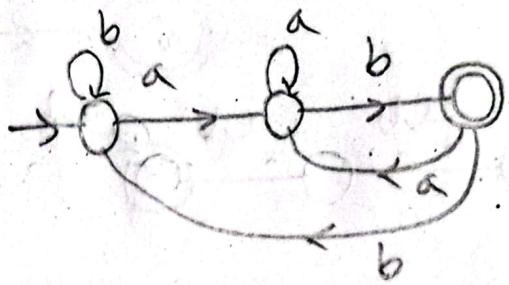
$$5=101$$

11-modulo 9

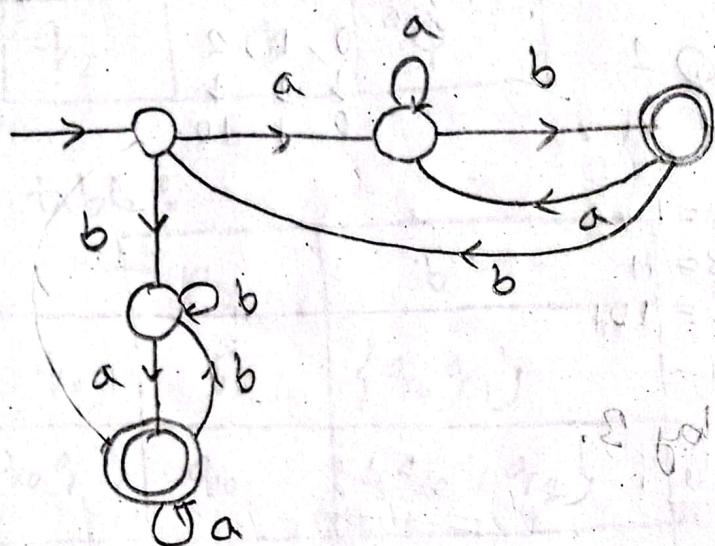
Problem-8 end with 'a',



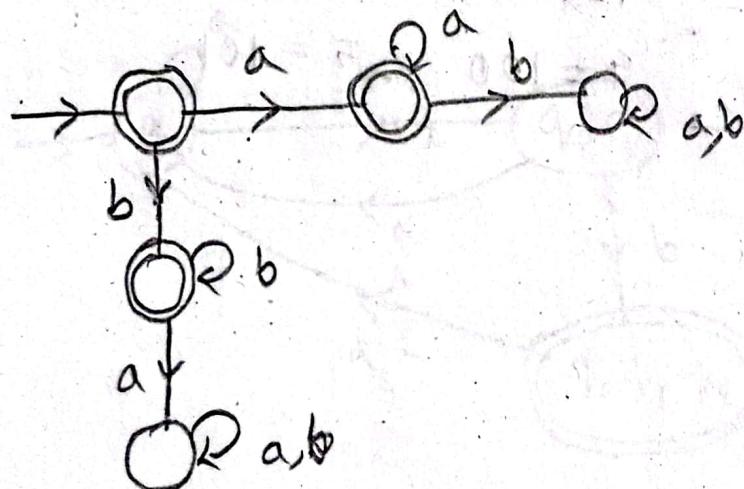
Problem-9 end with 'ab'



Problem-10 start with 'a' and 'b' or start 'b' and ends with 'a',
for solution [21-m016n09]

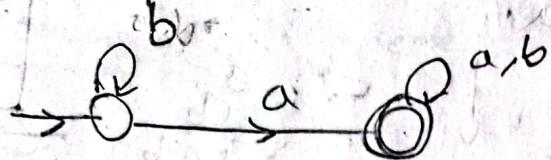


Problem-11 start and end with same symbol.



Problem - 4

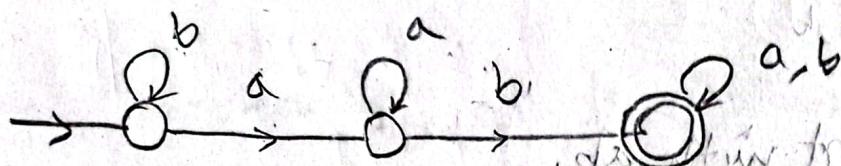
String contains a,



-baa

Problem - 5

contains a,b,



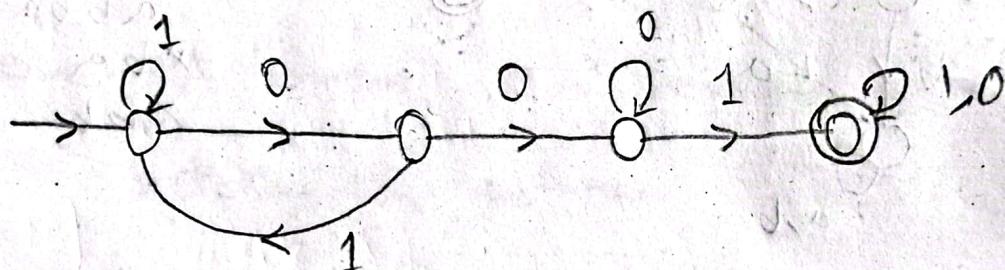
-babaa

~~babab~~

baba

-babab

Problem - 6 contains 001,



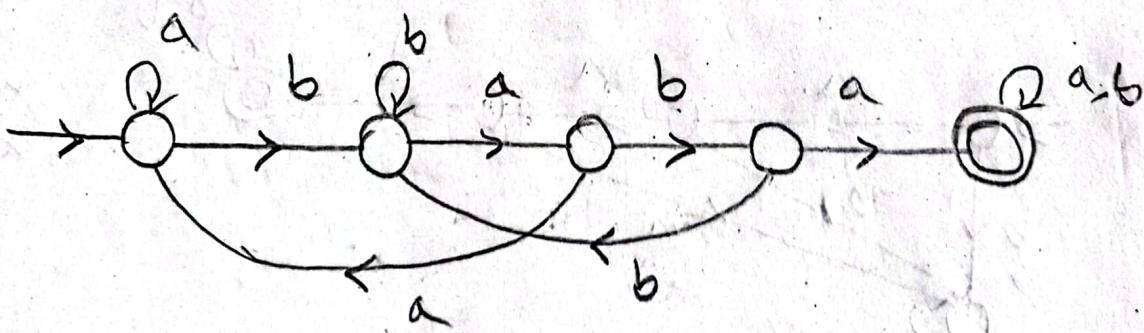
10010

10011

1101X

0001

Problem - 7 contains baba; see ntn note



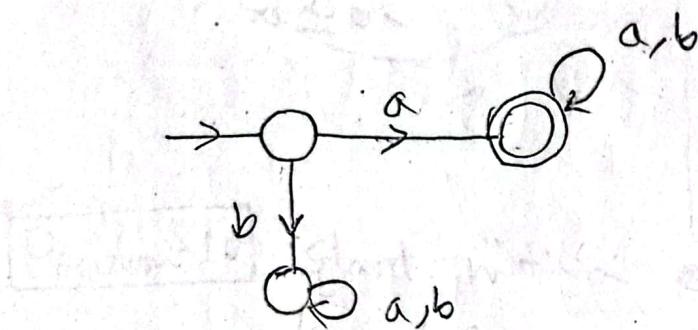
bbaba

bbaaa

babbaba

DFA

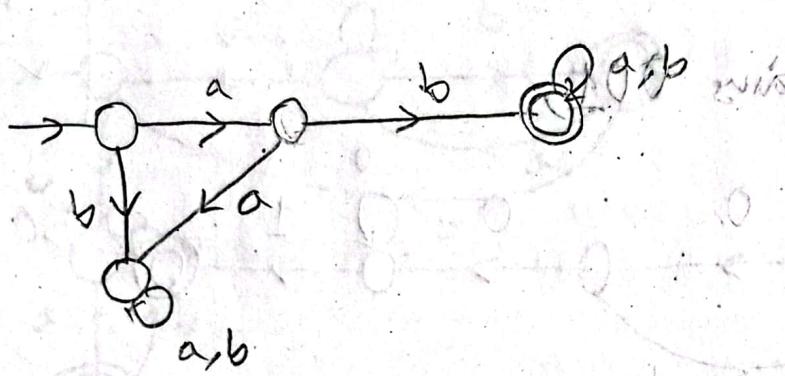
Example-1 Start with a.



\underline{a}
 \underline{ab}
 $\underline{\underline{a}ab}$
 $\underline{\underline{ab}}$

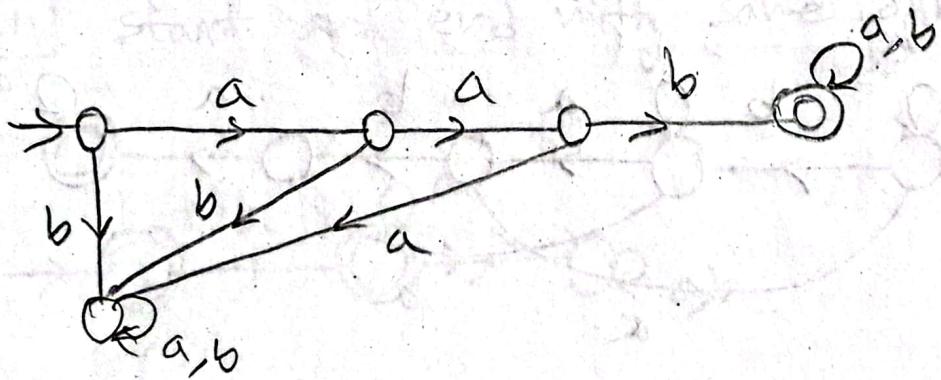
Start with +
2 = total
State

Example-2 Start with ab.



\underline{abab}
 \underline{xaa}
 \underline{abba}
 $x.b.a$
 $x.b.b$

Example-3 Start with aab.



\underline{aabab}
 \underline{xabb}
 \underline{xaaa}

String \rightarrow Collection of Alphabets.

w' \rightarrow शब्द का संग्रह होता है।

Ex: ab, abc, babba... जैसे शब्द।

$|w| = 7$, $w = ababbab$

(String is total symbol count)

Language \rightarrow Collection of strings over a particular rule. ये तो क्या होता है?

$L = \{aa, ab, ba, bb\}$ (मात्र सभी 2 अक्षरों की शब्दें हैं।)

अतः इनमें सब का लंबाई 2 है।

इनमें सब का लंबाई 2 है।

(string length 2)

Compiler → Higher level language or assembly code

→ convert $\text{HLL} \rightarrow \text{Assembly}$

Assembler → Assembly language \rightarrow object file

convert $\text{ASM} \rightarrow$

Linker → Build in function $\text{HLL} \rightarrow \text{Library}$ link ASM

Loader → Execute file create $\text{OS}(\text{exe})$ \rightarrow (exe)

Theory of computation:

Symbol \rightarrow Building block / smallest element

of a language.

Alphabet \rightarrow Finite collections of symbols.

format $\leftarrow \Sigma$ first symbol of Σ ,

$$\Sigma = \{a, b\}$$

$$\Sigma = \{0, 1\}$$

$$\Sigma = \{0, 1, \dots, 9\}$$

Compiler

Compiler is a 5 stage program which converts high level language into assembly language to convert it into machine code.

- ① Lexical analysis.
- ② Syntax analysis.
- ③ Semantic analysis.
- ④ Intermediate code generator.
- ⑤ Code optimization.
- ⑥ Code generation.

Language processing System 5 phase

- ① Hypertext preprocessor → File inclusion (#include header file removal)
- ② Compiler → Macro expansion
- ③ Assembler → Constant removal (dead code elimination)
- ④ Linker → Dead code elimination
- ⑤ Loader.

$$\{d, o\} = \emptyset$$

$$\{t, o\} = \emptyset$$

$$d \rightarrow \{d, o\} = \emptyset$$

Pranesh Chowdhury