Lab Report 3

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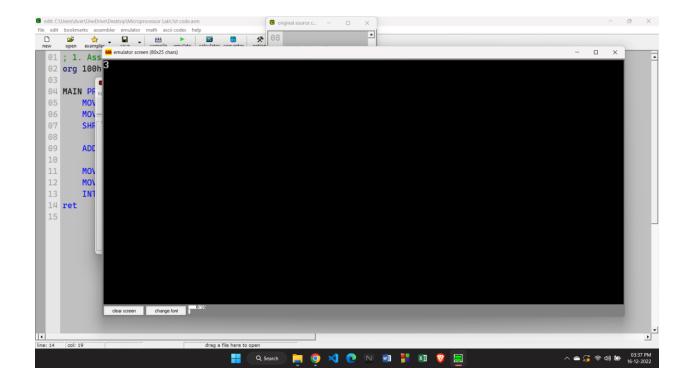
1. Assembly language for dividing the value of a register by 4.

CODE:

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OUTPUT:



2. Assembly language for converting an upper case letter into a lower case letter and vice versa.

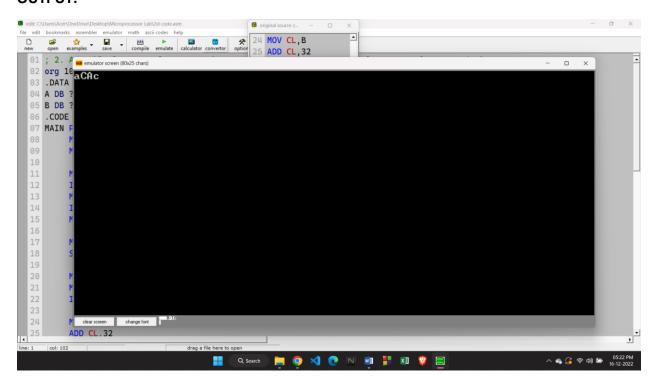
CODE:

```
01 ; 2. Assembly language for converting an upper case letter into a lower case letter and vice versa.
 02 org 100h
03 .DATA
04 A DB ?
             ; Input the Lower case Latter
  05 B DB ?
              ; Input the Upper case Latter
  06 .CODE
  07 MAIN PROC
         MOV AX, @DATA
  08
         MOV DS, AX
  09
         INT 21H
         MOV A, AL
         INT 21H
  15
         MOV B, AL
  16
  17
         MOV BL, A
  18
         SUB BL,32
  19
  20
         MOV AH, 2
         MOV DL, BL
         INT 21H
         MOV CL, B
         ADD CL.32
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| Compiler | Code |
                07 MAIN PROC
                                                                       MOV AX, @DATA
                 09
                                                                      MOV DS, AX
                10
                                                                      MOV AH,1
                                                                    INT 21H
                                                                    MOV A,AL
INT 21H
                 14
                                                                    MOV B,AL
                 15
                 16
                                                                    MOV BL, A
                 18
                                                                    SUB BL,32
                 19
                 20
                                                                    MOV AH, 2
                                                                      MOV DL, BL
                                                                    INT 21H
                 23
                                                                    MOV CL, B
                 24
                                                                    ADD CL,32
                 25
                 26
                                                                      MOV AH, 2
                 27
                                                                      MOV DL,CL
                 28
                 29
                                                                      INT 21H
                 30 ret
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     へ 🧠 🎜 🛜 ゆ) 🖢 05:22 PM
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OUTPUT:

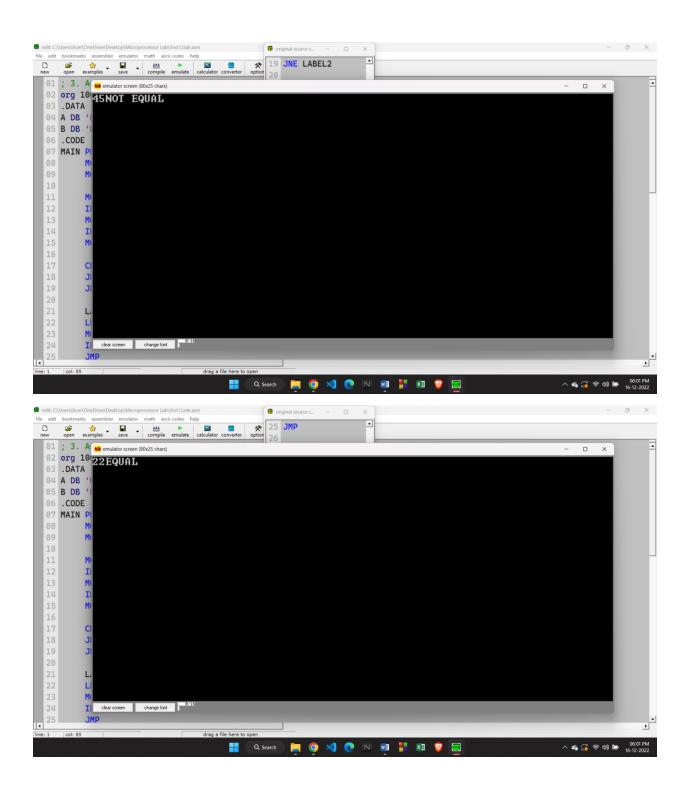


3. Assembly language for checking if values of two registers are equal or not.

CODE:

```
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01; 3. Assembly language for checking if values of two registers are equal or not.
  02 org 100h
03 .DATA
  04 A DB 'EQUAL$'
05 B DB 'NOT EQUAL$'
  06 .CODE
  07 MAIN PROC
         MOV AX, @DATA
         MOV DS, AX
  10
         INT 21H
         MOV BL, AL
  14
         INT 21H
  15
         MOV CL, AL
  16
         CMP BL,CL
JE LABEL1
  17
  18
         JNE LABEL2
  19
  20
         LABEL2:
         LEA DX,B
         MOV AH, 9
         INT 21H
                                                へ鳴(3 常中) 10·11
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10
         MOV AH,1
         INT 21H
         MOV BL, AL
         INT 21H
  14
         MOV CL, AL
  16
  17
         CMP BL,CL
         JE LABEL1
  18
         JNE LABEL2
  19
  20
         LABEL2:
         LEA DX,B
         MOV AH,9
         INT 21H
         JMP
  26
  27
         LABEL1:
  28
         LEA DX,A
  29
         MOV AH, 9
  30
         INT 21H
  31 ret
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OUTPUT:



4. Assembly language for multiplying the value of a register by 5.

CODE:

```
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101; 4. Assembly language for multiplying the value of a register by 5.
  04 MAIN PROC
          MOV AL,1
MOV BL,AL
  05
   06
           SHL AL, 2
  08
           ADD AL,48
   09
   10
           ADD AL, BL
            MOV AH, 2
            MOV DL, AL
            INT 21H
  15 ret
  16
17
   18
   19
   20
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```

OUTPUT:

