## **Enrolment No:**

## KADI SARVA VISHWAVIDYALAYA LDRP INSTITUTE OF TECHNOLOGY AND RESEARCH, GANDHINAGAR DEPARTMENT OF ELECTRONICS AND COMMUNICATION B.E. 5<sup>th</sup> SEMESTER MID SEMESTER EXAMINATION AUGUST-2014

AM

	ode: EC-505  ame : Integrated Circuits and Applications 08/2014  Branch: EC Total Marks: 30 Time: 08.30 AM to		
Instructions: - All questions are compulsory Figures to the right indicate full marks Make suitable assumption, wherever necessary.			
Que. 1 A)	Define: 1. Output offset voltage 2. CMRR 3. Slew rate 4. Large signal voltage gain	(4)	
а В)	3. Slew rate 4. Large signal voltage gain Draw frequency response of ideal and practical low pass and band pass filter.	(2)	
A) A) B) C) A) B)	Answer the following questions.  Draw and explain block Diagram of Typical OP-AMP.  What are the characteristics of an ideal op-amp?  Derive expressions for voltage gain, input resistance and bandwidth for Voltage-Series feedback amplifier.  OR  Explain two special cases of inverting amplifier with feedback.  Draw equivalent circuit of op-amp and ideal voltage transfer curve.	(4) (2) (6) (4) (2)	
(C)	Draw circuit diagram of differential amplifier with one op-amp and derive expression for its output signal as a function of input signals. Also derive expression of input resistance faced by each input signal.	(6)	
Que. 3	Answer the following questions. Design second order Butterworth low pass filter with -3dB frequency of 10 KHz. Choose $C = 1 \text{ nF}$ .	(6)	
B)	With the help of a circuit diagram explain the operation of first order high pass filter.	(6)	
	OR		
A)	Design wideband band pass filter using a single op-amp for $f_L = 100$ Hz, $f_H = 1$ KHz and pass band gain of 4. Also calculate the value of quality factor Q.	(6)	
B)	Derive the expression for filter transfer function of a first order low pass filter and draw its frequency response characteristics.	(6)	

\*\*\*\*\*All The Best\*\*\*\*