

KADI SARVA VISHWAVIDYALAYA
 LDRP INSTITUTE OF TECHNOLOGY AND RESEARCH, GANDHINAGAR
 DEPARTMENT OF ELECTRONICS AND COMMUNICATION
 B.E. 3RD SEMESTER
 MID SEMESTER EXAMINATION AUGUST-2014

Subject Code: EC-303

Subject Name : Basic Electronics

Date : 26/08/2014

Branch: EC

Total Marks: 30

Time: 12.00AM to 1.30 AM

Instructions: - All questions are compulsory.
 - Figures to the right indicate full marks.
 - Make suitable assumption, wherever necessary.

- Que. 1** Explain Clipping circuits 6
- Que. 2** Answer the following questions.
- A) State the Law of mass action. Derive the formula for concentration of holes in n-type material and concentration of electrons in p-type material. 4
- B) Explain Hall effect. Also state applications of Hall effect. 4
- C) A bar of n type silicon has length of 5 cm and circular cross section of 20 mm². When it is subjected to a voltage of 1 V applied across its length, the current flowing through it is 5 mA. Assume: Charge on one electron as $1.6 \times 10^{-19} \text{C}$ & Mobility of free electrons as 1300 cm²/v-s. 4
- Calculate: 1. Concentration of free electrons. 2. Drift velocity of electrons.
- OR**
- A) Explain drift and diffusion process. Also write down the equation for total current density resulting from drift and diffusion current. 4
- B) Derive the expression for potential difference present in the graded semiconductor material. 4
- C) The Hall experiment is used for a silicon bar known to be p-type. The resistivity of the bar is $220 \times 10^3 \Omega\text{-cm}$. width of the bar is 2 mm and distance between the two surfaces of the bar is 2.2mm. The magnetic field used has intensity of 0.1Wb/m². If measured value of current and Hall voltage are 5 micro-amp and 28 mv respectively. Calculate the mobility of holes. 4
- Que. 3** Answer the following questions.
- A) What is rectifier? Explain center tap full-wave rectifier. 5
- B) Difference between silicon diode vs germanium diode. 3
- C) Explain diode capacitance. 4
- OR**
- A) A bridge rectifier is applied with input from a step down transformer having turns ratio 8:1 and input 230 V, 50 Hz. If the diode forward resistance is 1 Ω , secondary resistance is 10 Ω and load resistance connected is 2 k Ω find : 4
1. DC power output 2. % efficiency
- B) Consider the step graded Germanium semiconductor with $N_D = 10^3 \text{ NA}$ with NA corresponding to 1 acceptor atom per 10^8 Germanium atoms. Calculate the contact potential V_0 at room temperature. 4
- Assume for Ge atoms per cm³ = 4.4×10^{22} and $n_i = 2.5 \times 10^{15}$ per cm³.
- C) Explain Clamper circuits. 4

*****All The Best*****